Intel® SGX PCK Certificate and Certificate Revocation List Profile Specification

Rev 1.0
November 26, 2018

Owners:
piotr.zmijewski@intel.com
vincent.r.scarlata@intel.com
james.d.beaney@intel.com

Architectural Contributors: Bo Zhang, Simon Johnson

Copyright © Intel Corporation 2007 – 2018
Table of Contents

1. Introduction .................................................................................................................................................. 2
   1.1. Terminology ........................................................................................................................................ 2
   1.2. References ........................................................................................................................................ 2

2. Certificate Hierarchy .................................................................................................................................. 3
   2.1. Certificates ......................................................................................................................................... 3
   2.2. Certificate Revocation Lists ............................................................................................................. 4

3. Certificate Formats ..................................................................................................................................... 5
   3.1. Intel® Software Guard Extensions Root CA Certificate ............................................................... 5
   3.2. Intel® SGX PCK Platform CA Certificate ....................................................................................... 5
   3.3. Intel® SGX PCK Processor CA Certificate .................................................................................... 6
   3.4. Intel® SGX TCB Signing Certificate ............................................................................................. 7
   3.5. Intel® SGX PCK Certificate ............................................................................................................. 7

4. Certificate Revocation List Formats ......................................................................................................... 10
   4.1. Intel® SGX Root CA CRL ................................................................................................................. 10
   4.2. Intel® SGX PCK Platform CA CRL ............................................................................................... 10
   4.3. Intel® SGX PCK Processor CA CRL ............................................................................................. 10

5. Appendices ................................................................................................................................................. 12
   5.1. Appendix A: Profile for Specific Certificate Extensions for the Intel® Software Guard Extensions ........................................................................................................................................ 12
1. Introduction

This document specifies the hierarchy and format of X.509 v3 certificates and X.509 v2 Certificate Revocation Lists (CRLs) issued by Intel for Provisioning Certification Keys. Certificates and CRLs use a standard set of extensions described in the document. Additionally, a number of PCK-specific extensions are defined.

1.1. Terminology

<table>
<thead>
<tr>
<th>Term</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Provisioning Certification Enclave (PCE)</td>
<td>Intel® Software Guard Extensions (Intel® SGX) enclave that uses a Provisioning Certification Key to sign proofs that attestation keys or attestation key provisioning protocol messages are created on genuine hardware.</td>
</tr>
<tr>
<td>Provisioning Certification Key (PCK)</td>
<td>Signing key available to a Provisioning Certification Enclave (PCE). The key is unique to the processor package or platform instance and its Trusted Computing Base (HW and PCE). The public part of the key is distributed as a PCK Certificate.</td>
</tr>
<tr>
<td>Platform Provisioning ID (PPID)</td>
<td>Unique Provisioning ID of the processor package or platform instance. The PPID does not depend on the Trusted Computer Base (TCB).</td>
</tr>
<tr>
<td>Trusted Computing Base (TCB)</td>
<td>Set of hardware and software components that are critical to the security of the solution.</td>
</tr>
<tr>
<td>Security Version Number (SVN)</td>
<td>Version number of a component that indicates when security relevant updates have been applied to the component. The SVN might not correlate with the functional version of the component.</td>
</tr>
<tr>
<td>Family-Model-Stepping-Platform-CustomSKU (FMSPC)</td>
<td>Description of the processor package or platform instance including its Family, Model, Stepping, Platform Type, and Customized SKU (if applies).</td>
</tr>
</tbody>
</table>

Table 1-1: Terminology

1.2. References

| RFC 4648 (Base16, Base32 and Base64) | October 2006 | http://www.ietf.org/rfc/rfc4648.txt |

Table 1-2: References
## 2. Certificate Hierarchy

The figure below illustrates the hierarchy of PCK certificates and CRLs issued by Intel. High level description of all the certificates and CRLs that are part of the hierarchy can be found in the 2.1 Certificates and 2.2 Certificate Revocation Lists tables.

![Certificate Hierarchy Diagram]

### Figure 2-1: Certificate Hierarchy

#### 2.1. Certificates

<table>
<thead>
<tr>
<th>Certificate</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Intel® SGX Root CA Certificate</strong></td>
<td>Intel® self-signed root Certificate Authority (CA) certificate (in X.509 format as defined in <a href="https://tools.ietf.org/html/rfc5280">RFC 5280</a>) that is designated to issue certificates related to Intel® Software Guard Extensions (Intel® SGX).&lt;br&gt;&lt;br&gt;<em>For more details see: <a href="#">Intel® SGX Root CA Certificate</a>.</em></td>
</tr>
<tr>
<td><strong>Intel® SGX PCK Platform CA Certificate</strong></td>
<td>Intermediate CA certificate (in X.509 format as defined in <a href="https://tools.ietf.org/html/rfc5280">RFC 5280</a>) that issues Intel® SGX PCK Certificates and corresponding CRLs for multi-package platforms.&lt;br&gt;&lt;br&gt;Currently not supported.&lt;br&gt;&lt;br&gt;<em>For more details see: <a href="#">Intel® SGX PCK Platform CA Certificate</a>.</em></td>
</tr>
<tr>
<td><strong>Intel® SGX PCK Processor CA Certificate</strong></td>
<td>Intermediate CA certificate (in X.509 format as defined in <a href="https://tools.ietf.org/html/rfc5280">RFC 5280</a>) that issues Intel® SGX PCK Certificates and corresponding CRLs for single-package platforms.</td>
</tr>
</tbody>
</table>
Table 2-1: Certificates

<table>
<thead>
<tr>
<th>Certificate Revocation List</th>
<th>Description</th>
</tr>
</thead>
</table>
| **Intel® SGX TCB Signing** Certificate | Leaf certificate (in X.509 format as defined in [RFC 5280](https://tools.ietf.org/html/rfc5280)) that signs the Intel® SGX TCB data for Intel® platforms (both single and multi-package).

*For more details see: Intel® SGX TCB Signing Certificate.* |
| **Intel® SGX PCK Certificate** | Leaf certificate (in X.509 format as defined in [RFC 5280](https://tools.ietf.org/html/rfc5280)) that contains a public part of a PCK for an Intel® SGX platform on a specific TCB level.

*For more details see: Intel® SGX PCK Certificate.* |

Table 2-2: Certificate Revocation Lists

<table>
<thead>
<tr>
<th>Certificate Revocation List</th>
<th>Description</th>
</tr>
</thead>
</table>
| **Intel® SGX Root CA CRL** | Certificate Revocation List (in X.509 format as defined in [RFC 5280](https://tools.ietf.org/html/rfc5280)) issued by the Intel® SGX Root Certification Authority (CA).

*For more details see: Intel® SGX Root CA CRL.* |

*For more details see: Intel® SGX PCK Platform CA CRL.* |
| **Intel® SGX PCK Processor CA CRL** | Certificate Revocation List (in X.509 format as defined in [RFC 5280](https://tools.ietf.org/html/rfc5280)) issued by the Intel® SGX PCK Processor CA.

*For more details see: Intel® SGX PCK Processor CA CRL.* |
3. Certificate Formats

All certificates described in this section follow X.509 standard (as defined in RFC 5280). Placeholders for variable values in the certificates, such as signatures, are marked as <placeholder>.

3.1. Intel® Software Guard Extensions Root CA Certificate

Certificate:
Data:
Version: 3 (0x2)
Serial Number:
<serial number>
Signature Algorithm: ecdsa-with-SHA256
Issuer: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Validity
Not Before: <time of issuing>
Not After: Dec 31 2049 23:59:59 UTC
Subject: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Subject Public Key Info:
Public Key Algorithm: id-ecPublicKey
Public-Key: (256 bit)
pub:
<public key>
ASN1 OID: prime256v1
X509v3 extensions:
X509v3 Authority Key Identifier:
keyid:<keyid of issuer public key>
X509v3 CRL Distribution Points:
Full Name:
URI: <URL>
X509v3 Subject Key Identifier:
keyid:<keyid of public key>
X509v3 Key Usage: critical
Certificate Sign, CRL Sign
X509v3 Basic Constraints: critical
CA:TRUE, pathlen:1
Signature Algorithm: ecdsa-with-SHA256
<signature>

3.2. Intel® SGX PCK Platform CA Certificate

Note: This is only a draft format, which may be changed in future Intel® Software Guard Extensions (Intel® SGX) updates.

Certificate:
Data:
Version: 3 (0x2)
Serial Number:
<serial number>
Signature Algorithm: ecdsa-with-SHA256
Issuer: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Validity
Not Before: <start of validity period>
Not After: <expiration date, should be valid for about 15 years>
Subject: CN=Intel SGX PCK Platform CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Subject Public Key Info:
Public Key Algorithm: id-ecPublicKey
Public-Key: (256 bit)
pub:
<public key>
ASN1 OID: prime256v1
X509v3 extensions:
X509v3 Authority Key Identifier:
keyid:<keyid of issuer public key>
X509v3 CRL Distribution Points:
Full Name:
URI: <URL>
X509v3 Subject Key Identifier:
<keyid of public key>
X509v3 Key Usage: critical
Certificate Sign, CRL Sign
X509v3 Basic Constraints: critical
CA:TRUE, pathlen:0
Signature Algorithm: ecdsa-with-SHA256
<signature>

3.3.Intel® SGX PCK Processor CA Certificate

Certificate:
Data:
Version: 3 (0x2)
Serial Number:
<serial number>
Signature Algorithm: ecdsa-with-SHA256
Issuer: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Validity
Not Before: <start of validity period>
Not After: <expiration date, should be valid for about 15 years>
Subject: CN=Intel SGX PCK Processor CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
Subject Public Key Info:
Public Key Algorithm: id-ecPublicKey
Public-Key: (256 bit)
pub:
<public key>
ASN1 OID: prime256v1
X509v3 extensions:
X509v3 Authority Key Identifier:
keyid:<keyid of issuer public key>
X509v3 CRL Distribution Points:
Full Name:
URI: <URL>
X509v3 Subject Key Identifier:
<keyid of public key>
X509v3 Key Usage: critical
  Certificate Sign, CRL Sign
X509v3 Basic Constraints: critical
  CA:TRUE, pathlen:0

Signature Algorithm: ecdsa-with-SHA256
  <signature>

3.4.Intel® SGX TCB Signing Certificate

Certificate:
  Data:
    Version: 3 (0x2)
    Serial Number:
      <serial number>
    Signature Algorithm: ecdsa-with-SHA256
    Issuer: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
    Validity
      Not Before: <start of validity period>
      Not After: <expiration date, should be valid for about 7 years>
    Subject: CN=Intel SGX TCB Signing, O=Intel Corporation, L=Santa Clara, ST=CA, C=US

Subject Public Key Info:
  Public Key Algorithm: id-ecPublicKey
  Public-Key: (256 bit)
    pub:
      <public key>
    ASN1 OID: prime256v1

X509v3 extensions:
  X509v3 Authority Key Identifier:
    keyid:<keyid of issuer public key>
  X509v3 CRL Distribution Points:
    Full Name:
      URI: <URL>

X509v3 Subject Key Identifier:
  <keyid of public key>
X509v3 Key Usage: critical
  Digital Signature, Non Repudiation
X509v3 Basic Constraints: critical
  CA:FALSE

Signature Algorithm: ecdsa-with-SHA256
  <signature>

3.5.Intel® SGX PCK Certificate

Certificate:
  Data:
    Version: 3 (0x2)
    Serial Number:
      <serial number>
    Signature Algorithm: ecdsa-with-SHA256
    Issuer: CN=Intel SGX PCK Platform CA | Intel SGX PCK Processor CA>, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
    Validity
Not Before: <start of validity period>
Not After : <expiration date, should be valid for about 7 years>
Subject: CN=Intel SGX PKC Certificate, O=Intel Corporation, L=Santa Clara, ST=CA, C=US

Subject Public Key Info:
Public Key Algorithm: id-ecPublicKey
Public-Key: (256 bit)
pub:
  <public key>
ASN1 OID: prime256v1

X509v3 extensions:
  X509v3 Authority Key Identifier:
    keyid:<keyid of issuer public key>

X509v3 CRL Distribution Points:
  Full Name:
    URI: <URL>

X509v3 Subject Key Identifier:
  <keyid of public key>

X509v3 Key Usage: critical
  Digital Signature, Non Repudiation

X509v3 Basic Constraints: critical
  CA:FALSE

<SGX Extensions OID>:
  <PPID OID>: <PPID value>
  <TCB OID>:
    <SGX TCB Comp01 SVN OID>: <SGX TCB Comp01 SVN value>
    <SGX TCB Comp02 SVN OID>: <SGX TCB Comp02 SVN value>
    ...
    <SGX TCB Comp16 SVN OID>: <SGX TCB Comp16 SVN value>
  <PCESVN OID>: <PCESVN value>
  <CPUSVN OID>: <CPUSVN value>
  <PCE-ID OID>: <PCE-ID value>
  <FMSPC OID>: <FMSPC value>
  <SGX Type OID>: <SGX Type value>

Signature Algorithm: ecdsa-with-SHA256
<signature>

The table below describes custom x.509 extensions for PKC Certificates. For details about ASN.1 encoding of the extensions, refer to Appendix A: Profile for Specific Certificate Extensions for the Intel® SGX.

<table>
<thead>
<tr>
<th>Name</th>
<th>Object Identifier (OID)</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SGX Extensions</td>
<td>1.2.840.113741.1.13.1</td>
<td>ASN.1 Sequence</td>
<td>Sequence of extensions specific to the Intel® Software Guard Extensions (Intel® SGX).</td>
</tr>
<tr>
<td>PPID</td>
<td>1.2.840.113741.1.13.1.1</td>
<td>ASN.1 Octet String</td>
<td>Base16-encoded string representation of PPID. (16 bytes)</td>
</tr>
<tr>
<td><strong>TCB</strong></td>
<td>1.2.840.113741.1.13.1.2</td>
<td><strong>ASN.1 Sequence</strong></td>
<td><strong>Sequence of TCB components.</strong></td>
</tr>
<tr>
<td><strong>SGX TCB Comp01 SVN</strong></td>
<td>1.2.840.113741.1.13.1.2.1</td>
<td><strong>ASN.1 Integer</strong></td>
<td><strong>Value of the Intel® SGX TCB Comp01 SVN.</strong></td>
</tr>
<tr>
<td><strong>SGX TCB Comp02 SVN</strong></td>
<td>1.2.840.113741.1.13.1.2.2</td>
<td><strong>ASN.1 Integer</strong></td>
<td><strong>Value of the Intel® SGX TCB Comp02 SVN.</strong></td>
</tr>
<tr>
<td>...</td>
<td>...</td>
<td>...</td>
<td>...</td>
</tr>
<tr>
<td><strong>SGX TCB Comp16 SVN</strong></td>
<td>1.2.840.113741.1.13.1.2.16</td>
<td><strong>ASN.1 Integer</strong></td>
<td><strong>Value of the Intel® SGX TCB Comp16 SVN.</strong></td>
</tr>
<tr>
<td><strong>PCESVN</strong></td>
<td>1.2.840.113741.1.13.1.2.17</td>
<td><strong>ASN.1 Integer</strong></td>
<td><strong>Value of PCESVN.</strong></td>
</tr>
<tr>
<td><strong>CPUSVN</strong></td>
<td>1.2.840.113741.1.13.1.2.18</td>
<td><strong>ASN.1 Octet String</strong></td>
<td><strong>Base16-encoded string representation of CPUSVN. (16 bytes)</strong></td>
</tr>
<tr>
<td><strong>PCE-ID</strong></td>
<td>1.2.840.113741.1.13.1.3</td>
<td><strong>ASN.1 Octet String</strong></td>
<td><strong>Base16-encoded string representation of PCE-ID. (2 bytes)</strong></td>
</tr>
<tr>
<td><strong>FMSPC</strong></td>
<td>1.2.840.113741.1.13.1.4</td>
<td><strong>ASN.1 Octet String</strong></td>
<td><strong>Base16-encoded string representation of FMSPC. (6 bytes)</strong></td>
</tr>
<tr>
<td><strong>SGXType</strong></td>
<td>1.2.840.113741.1.13.1.5</td>
<td><strong>ASN.1 Enumerated</strong></td>
<td><strong>Enum representing the Intel® SGX Type. One of the following values: - Standard (0)</strong></td>
</tr>
</tbody>
</table>

*Table 3-1: Custom OIDs for PCK certificate.*
4. Certificate Revocation List Formats

All certificate revocation lists described in this section follow X.509 standard (as defined in RFC 5280).

4.1. Intel® SGX Root CA CRL

Certificate Revocation List (CRL):
   Version 2 (0x1)
   Signature Algorithm: ecdsa-with-SHA256
   Issuer: CN=Intel SGX Root CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
   Last Update: <issuing date>
   Next Update: <date of next update>
   CRL extensions:
      X509v3 CRL Number:
         <crl number>
      X509v3 Authority Key Identifier:
         keyid: <keyid of issuer public key>

<list of revoked certificates>

Signature Algorithm: ecdsa-with-SHA256
<signature>

4.2. Intel® SGX PCK Platform CA CRL

Note: This is only a draft format, which may be changed in future Intel® Software Guard Extensions (Intel® SGX) updates.

Certificate Revocation List (CRL):
   Version 2 (0x1)
   Signature Algorithm: ecdsa-with-SHA256
   Issuer: CN=Intel SGX PCK Platform CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US
   Last Update: <issuing date>
   Next Update: <date of next update>
   CRL extensions:
      X509v3 CRL Number:
         <crl number>
      X509v3 Authority Key Identifier:
         keyid: <keyid of issuer public key>

<list of revoked certificates>

Signature Algorithm: ecdsa-with-SHA256
<signature>

4.3. Intel® SGX PCK Processor CA CRL

Certificate Revocation List (CRL):
   Version 2 (0x1)
   Signature Algorithm: ecdsa-with-SHA256
Issuer: CN=Intel SGX PCK Processor CA, O=Intel Corporation, L=Santa Clara, ST=CA, C=US

Last Update: <issuing date>
Next Update: <date of next update>
CRL extensions:
  X509v3 CRL Number:
    <crl number>
  X509v3 Authority Key Identifier:
    keyid: <keyid of issuer public key>

<list of revoked certificates>

Signature Algorithm: ecdsa-with-SHA256
  <signature>
5. Appendices

5.1. Appendix A: Profile for Specific Certificate Extensions for the Intel® Software Guard Extensions

This section describes a profile for specific certificate extensions used in the Intel® SGX PCK Certificate. This section is based on the X.509 v3 certificate format and the standard certificate extensions defined in RFC 5280 and uses 1988 ASN.1 syntax.

\[
id-ce-sGXExtensions\hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ 1 2 840 113741 1 13 1 \}
\]

\[
SGXExtensions ::= \text{SEQUENCE SIZE (1..MAX) OF SEQUENCE}
\hspace{1em}\text{sGXExtensionId} \hspace{1em}\text{SGXExtensionId},
\hspace{1em}\text{sGXExtensionValue} \hspace{1em}\text{ANY DEFINED BY sGXExtensionId}
\]

-- sGXExtensionIds for Intel SGX PCK Certificates

\[
\text{id-ce-sGXExtensions-pPID} \hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ \text{id-ce-sGXExtensions} \text{ 1} \}
\]

\[
\text{id-ce-sGXExtensions-tCB} \hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ \text{id-ce-sGXExtensions} \text{ 2} \}
\]

\[
\text{id-ce-sGXExtensions-pCE-ID} \hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ \text{id-ce-sGXExtensions} \text{ 3} \}
\]

\[
\text{id-ce-sGXExtensions-fMSPC} \hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ \text{id-ce-sGXExtensions} \text{ 4} \}
\]

\[
\text{id-ce-sGXExtensions-sGXType} \hspace{1em}\text{OBJECT IDENTIFIER} ::= \{ \text{id-ce-sGXExtensions} \text{ 5} \}
\]

\[
\text{SGXExtensionId} ::= \text{OBJECT IDENTIFIER} \{ \text{id-ce-sGXExtensions-pPID} \hspace{1em}| \text{id-ce-sGXExtensions-tCB} \hspace{1em}| \text{id-ce-sGXExtensions-pCE-ID} \hspace{1em}| \text{id-ce-sGXExtensions-fMSPC} \hspace{1em}| \text{id-ce-sGXExtensions-sGXType} \}
\]

\[
\text{SGXExtensionValue} ::= \text{CHOICE} \{ 
\hspace{1em}\text{pPID} \hspace{1em}[0] \text{PPID,}
\hspace{1em}\text{tCB} \hspace{1em}[1] \text{TCB,}
\hspace{1em}\text{pCE-ID} \hspace{1em}[3] \text{PCE-ID,}
\hspace{1em}\text{fMSPC} \hspace{1em}[4] \text{FMSPC,}
\hspace{1em}\text{sGXType} \hspace{1em}[5] \text{SGXType} \}
\]

\[
\text{PPID} \hspace{1em} ::= \text{OCTET STRING (SIZE (16))}
\]

\[
\text{TCB} ::= \text{SEQUENCE SIZE (1..18) OF SEQUENCE} \{ 
\hspace{1em}\text{tCBId} \hspace{1em}\text{TCBId,}
\hspace{1em}\text{tCBValue} \hspace{1em}\text{ANY DEFINED BY tCBId} \}
\]

-- tCBIds for Intel SGX PCK Certificates
id-ce-tCB-sGXTCBComp01SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 1 }
id-ce-tCB-sGXTCBComp02SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 2 }
id-ce-tCB-sGXTCBComp03SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 3 }
id-ce-tCB-sGXTCBComp04SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 4 }
id-ce-tCB-sGXTCBComp05SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 5 }
id-ce-tCB-sGXTCBComp06SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 6 }
id-ce-tCB-sGXTCBComp07SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 7 }
id-ce-tCB-sGXTCBComp08SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 8 }
id-ce-tCB-sGXTCBComp09SVN  OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 9 }
id-ce-tCB-sGXTCBComp10SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 10 }
id-ce-tCB-sGXTCBComp11SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 11 }
id-ce-tCB-sGXTCBComp12SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 12 }
id-ce-tCB-sGXTCBComp13SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 13 }
id-ce-tCB-sGXTCBComp14SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 14 }
id-ce-tCB-sGXTCBComp15SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 15 }
id-ce-tCB-sGXTCBComp16SVN OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 16 }
id-ce-tCB-pCESVN       OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 17 }
id-ce-tCB-cPUSVN       OBJECT IDENTIFIER ::= { id-ce-sGXExtensions-tCB 18 }

TCBId ::= OBJECT IDENTIFIER (id-ce-tCB-sGXTCBComp01SVN | id-ce-tCB-sGXTCBComp02SVN | id-
ce-tCB-sGXTCBComp03SVN | id-ce-tCB-sGXTCBComp04SVN | id-ce-tCB-sGXTCBComp05SVN | id-ce-
tCB-sGXTCBComp06SVN | id-ce-tCB-sGXTCBComp07SVN | id-ce-tCB-sGXTCBComp08SVN | id-ce-
tCB-sGXTCBComp09SVN | id-ce-tCB-sGXTCBComp10SVN | id-ce-tCB-sGXTCBComp11SVN | id-ce-
tCB-sGXTCBComp12SVN | id-ce-tCB-sGXTCBComp13SVN | id-ce-tCB-sGXTCBComp14SVN | id-ce-
tCB-sGXTCBComp15SVN | id-ce-tCB-sGXTCBComp16SVN | id-ce-tCB-pCESVN | id-ce-tCB-cPUSVN)

TCBValue ::= CHOICE {
  sGXTCBComp01SVN [0] INTEGER,
  sGXTCBComp02SVN [1] INTEGER,
  sGXTCBComp03SVN [2] INTEGER,
  sGXTCBComp04SVN [3] INTEGER,
  sGXTCBComp05SVN [4] INTEGER,
  sGXTCBComp06SVN [5] INTEGER,
  sGXTCBComp07SVN [6] INTEGER,
  sGXTCBComp08SVN [7] INTEGER,
  sGXTCBComp09SVN [8] INTEGER,
  sGXTCBComp10SVN [9] INTEGER,
  sGXTCBComp11SVN [10] INTEGER,
  sGXTCBComp12SVN [11] INTEGER,
  sGXTCBComp13SVN [12] INTEGER,
  sGXTCBComp14SVN [13] INTEGER,
sGXTCBComp15SVN [14] INTEGER,
sGXTCBComp16SVN [15] INTEGER,
pCESVN [16] INTEGER,
cPUSVN [17] CPUSVN }

CPUSVN ::= OCTET STRING (SIZE (16))
PCE-ID ::= OCTET STRING (SIZE (2))
FMSPC ::= OCTET STRING (SIZE (6))

SGXType ::= ENUMERATED {
    standard (0) }

- 14 -