

# **Intel® Software Guard Extensions (Intel® SGX) Enclave Common Loader API Reference**

**Rev 1.1  
April 2019**



*Intel® Software Guard Extensions (Intel® SGX) Enclave Common Loader API Reference*

Copyright © Intel Corporation 2007 – 2019

---

## Table of Contents

<b>1</b>	<b><i>Introduction</i></b> .....	<b>2</b>
1.1	Terminology.....	2
<b>2</b>	<b><i>Overview</i></b> .....	<b>3</b>
<b>3</b>	<b><i>Enclave Loading APIs</i></b> .....	<b>4</b>
3.1	Enclave Creation.....	4
3.2	Loading Enclave Data.....	5
3.3	Enclave Initialization .....	6
3.4	Enclave Deletion.....	7
<b>4</b>	<b><i>Enclave Management APIs</i></b> .....	<b>8</b>
4.1	Enclave Get Information.....	8
4.2	Enclave Set Information .....	9
<b>5</b>	<b><i>Data Types</i></b> .....	<b>11</b>
5.1	ENCLAVE_ERROR Enumeration.....	11
5.2	ENCLAVE_TYPE Enumeration .....	12
5.3	ENCLAVE_PAGE_PROPERTIES Enumeration.....	12
5.4	ENCLAVE_INFO_TYPE Enumeration.....	13
5.5	enclave_create_sgx_t Structure.....	13
5.6	enclave_init_sgx_t Structure .....	13
5.7	enclave_launch_token_t Structure.....	13

---

# 1 Introduction

This document provides a proposal for a universal interface for loading the Intel® Software Guard Extensions (Intel® SGX) enclaves on different operating systems. It proposes to put an abstraction layer between a loader function, which creates an image of an enclave, and a lower level APIs, which create an interface to kernel modules to load the enclave contents into the Enclave Page Cache (EPC) memory (including the creation of the enclave SECS and the initialization of the enclave). The lower level APIs are currently implemented differently on various operating systems and environments.

## 1.1 Terminology

SGX	Software Guard Extensions
EDMM	Enclave Dynamic Memory Management
EPC	Enclave Page Cache – memory reserved for SGX enclaves
EINITTOKEN	EINIT Token – an Enclave Launch Token
SECS	Secure Enclave Control Structure
SIGSTRUCT	Enclave Signature Structure

*Table 1-1: Terminology*

---

## **2 Overview**

This document covers the following topics:

- Enclave Loading APIs – APIs used in creating, loading, initializing, and deleting SGX enclaves
- Enclave Management APIs – APIs used in configuring enclaves or requesting enclave information
- Data Structures – data types and structures used in the APIs.

---

## 3 Enclave Loading APIs

### 3.1 Enclave Creation

---

The **enclave\_create** API provides a generic API to create an enclave.

#### Syntax

```
void* cdecl enclave_create(  
    _In_opt_ void*      base_address,  
    _In_      size_t    virtual_size,  
    _In_      size_t    initial_commit,  
    _In_      uint32_t  type,  
    _In_      const void* info,  
    _In_      size_t    info_size,  
    _Out_opt_ uint32_t*  enclave_error  
);
```

#### Parameters

*base\_address* [in, optional]

Optional preferred base address for the enclave. Specify *NULL* to have the base address assigned by the interface.

*virtual\_size* [in]

Virtual address range of the enclave in bytes. This is the amount of virtual memory to reserve for the enclave to use.

*initial\_commit* [in]

Amount of physical memory to reserve for the initial load of the enclave in bytes. If the system does not have enough physical memory to commit to the enclave load, the function fails.

The interface may commit larger size if required to adhere to architectural restrictions and may reserve physical pages. Any memory that remains unused after you initialize the enclave by calling **enclave\_initialize** is returned as a list of empty pages.

*type* [in]

Architecture type of the enclave that you want to create.

The value must be of type **ENCLAVE\_TYPE** (see 4.2 **ENCLAVE\_TYPE** Enumeration).

*info* [in]

Pointer to the architecture-specific information to use for the enclave creation.

For **ENCLAVE\_TYPE\_SGX1** and **ENCLAVE\_TYPE\_SGX2**, you must specify a pointer to the **enclave\_create\_sgx\_t** structure (see section 5.5).

*info\_size* [in]

---

Length of the structure that the *info* parameter points to, in bytes. The length must match the structure that is relevant for the enclave architecture, otherwise it is set to 0.

*enclave\_error* [out, optional]

Optional pointer to a variable that receives an enclave error code. Possible values are described in the ENCLAVE\_ERROR Enumeration section.

### Return value

If the function succeeds, the return value is the base address of the created enclave.

If the function fails, the return value is *NULL*. The extended error information is stored in the *enclave\_error* parameter if used.

### Remarks:

This function must be called to start loading the enclave. You should retain the return value to use it in other Enclave Common Loader APIs.

## 3.2 Loading Enclave Data

---

The **enclave\_load\_data** API provides a generic API to add pages to an enclave created by the **enclave\_create** API.

### Syntax

```
size_t cdecl enclave_load_data(  
    _In_      void*      target_address,  
    _In_      size_t    target_size,  
    _In_opt_  const void* source_buffer,  
    _In_      uint32_t   data_properties,  
    _Out_opt_ uint32_t*  enclave_error  
);
```

### Parameters

*target\_address* [in]

Address in the enclave where to load the data.

*target\_size* [in]

Size of the range to load in the enclave, in bytes. This value must be whole-number multiple of the page size.

*source\_buffer* [in, optional]

Optional pointer to the data to load into the enclave.

If used, the size of the buffer must be identical to the *target\_size*. If *NULL*, the loaded data is all zeros.

*data\_properties* [in]

---

Properties of the pages to add to the enclave, including access permissions and others. For example, specific properties for the Intel® SGX include the page type and whether the loading data should be measured.

The value must be bitwise OR of the `ENCLAVE_PAGE_PROPERTIES` enumeration (see section 5.3)

*enclave\_error* [out, optional]

Optional pointer to a variable that receives an enclave error code. Possible values are described in the `ENCLAVE_ERROR` enumeration (see section 5.1).

### Return value

Return value is the number of bytes that was loaded into the enclave.

If the number differs from the *target\_size* parameter value, this indicates an error. The extended error information is stored in the *enclave\_error* parameter if used.

### Remarks

The *target\_address* must be specified within a previously created enclave memory range.

## 3.3 Enclave Initialization

---

The `enclave_initialize` API provides a generic API to initialize a created enclave with loaded data.

### Syntax

```
bool cdecl enclave_initialize(  
    _In_      void*      base_address,  
    _In_      const void* info,  
    _In_      size_t     info_size,  
    _Out_opt_ uint32_t*  enclave_error  
);
```

### Parameters

*base\_address* [in]

Enclave base address as returned from the `enclave_create` API.

*info* [in]

Pointer to the architecture-specific information to use for the enclave initialization.

For `ENCLAVE_TYPE_SGX1` and `ENCLAVE_TYPE_SGX2`, you must specify a pointer to the `enclave_init_sgx_t` structure (see section 5.6).

*info\_size* [in]

Length of the structure that the *info* parameter points to, in bytes. The length must match the structure that is relevant for the enclave architecture, otherwise it is set to 0.

*enclave\_error* [out, optional]

Optional pointer to a variable that receives an enclave error code. Possible values are described in the `ENCLAVE_ERROR` Enumeration section.



---

## Return value

If the function succeeds, the return value is nonzero.

If the function fails, the return value is 0 and the extended error information is stored in the *enclave\_error* parameter if used.

## 3.4 Enclave Deletion

---

The **enclave\_delete** API provides a generic API to delete an existing enclave.

### Syntax

```
bool cdecl enclave_delete(  
    _In_ void* base_address,  
    _Out_opt_ uint32_t* enclave_error  
);
```

### Parameters

*base\_address* [in]

Enclave base address as returned from the **enclave\_create** API.

*enclave\_error* [out, optional]

Optional pointer to a variable that receives an enclave error code. Possible values are described in the **ENCLAVE\_ERROR** Enumeration section.

### Return value

If the function succeeds, the return value is nonzero.

If the function fails, the return value is 0 and the extended error information is stored in the *enclave\_error* parameter if used.

---

## 4 Enclave Management APIs

### 4.1 Enclave Get Information

The **enclave\_get\_information** API provides an extensible API to get specific information from an enclave.

#### Syntax

```
bool cdecl enclave_get_information(  
    _In_ void* base_address,  
    _In_ uint32_t info_type,  
    _Out_ void* output_info,  
    _In_Out_ size_t* output_info_size,  
    _Out_opt_ uint32_t* enclave_error  
);
```

#### Parameters

*base\_address* [in]

Enclave base address as returned from the `enclave_create` API.

*info\_type*[in]

Type of the requested information. Supported types are provided in the 0

ENCLAVE\_INFO\_TYPE Enumeration section:

- ENCLAVE\_LAUNCH\_TOKEN: provides a launch token. A valid Launch Token can be provided only for an initialized enclave.

*output\_info*[in]

Pointer to the information returned by the API.

*output\_info\_size*[in, out]

Size of the output buffer, in bytes. If the function succeeds, the number of bytes is returned in the `output_info`. If the function fails with, ENCLAVE\_INVALID\_SIZE, the required size is returned.

*enclave\_error* [out, optional]

Optional pointer to the variable that receives an enclave error code. Possible values are described in the **ENCLAVE\_ERROR** enumeration (section 5.1).

#### Return value

If the function succeeds, the return value is nonzero. If the function fails, the return value is zero and the extended error information is stored in the *enclave\_error* parameter.

If the function fails with the ENCLAVE\_INVALID\_SIZE error, the requested size is stored in the *output\_info\_size* parameter

If the function fails with the ENCLAVE\_NOT\_SUPPORTED error, the operation is not supported for the defined *info\_type*.

If the function receives an unknown *info\_type*, the ENCLAVE\_NOT\_SUPPORTED error is returned.

---

For specific *info\_type* values:

- ENCLAVE\_LAUNCH\_TOKEN may fail with the following errors:
  - ENCLAVE\_NOT\_INITIALIZED : a launch token may only be returned for an initialized enclave
  - ENCLAVE\_NOT\_SUPPORTED: for platforms where the launch token is not provided to user space, the API returns ENCLAVE\_NOT\_SUPPORTED

## Remarks

This API returns specific enclave information based on the *info\_type* parameter.

---

## 4.2 Enclave Set Information

---

The **enclave\_set\_information** API provides an extensible API to set specific information from an enclave.

### Syntax

```
bool cdecl enclave_set_information(  
    _In_      void*      base_address,  
    _In_      uint32_t   info_type,  
    _In_      void*      input_info,  
    _In_      size_t     input_info_size,  
    _Out_opt_ uint32_t*   enclave_error  
);
```

### Parameters

*base\_address* [in]

Enclave base address as returned from the `enclave_create` API.

*info\_type*[in]

Type of the requested information. Supported types are provided in 0

ENCLAVE\_INFO\_TYPE Enumeration:

- ENCLAVE\_LAUNCH\_TOKEN: provides a launch token. A valid Launch Token can be provided only for a **uninitialized** enclave.

*input\_info*[in]

Pointer to information provided to the API.

*input\_info\_size*[in]

Size of the information, in bytes, provided in the *input\_info* parameter.

*enclave\_error* [out, optional]

Optional pointer to a variable that receives an enclave error code. Possible values are described in the **ENCLAVE\_ERROR** enumeration (section 5.1).

### Return value

If the function succeeds, the return value is nonzero. If the function fails, the return value is zero and the extended error information is stored in the *enclave\_error* parameter.

The ENCLAVE\_INVALID\_SIZE error indicates an invalid value of the *input\_info\_size*.

---

If the function fails with the ENCLAVE\_NOT\_SUPPORTED error, the operation is not supported for the defined *info\_type*.

If the API receives an unknown *info\_type*, the ENCLAVE\_NOT\_SUPPORTED error is returned.

For specific *info\_type* values:

- ENCLAVE\_LAUNCH\_TOKEN may fail with:
  - ENCLAVE\_ALREADY\_INITIALIZED : a launch token may only be provided for an uninitialized enclave (the API is not useful if the enclave is initialized)
  - ENCLAVE\_NOT\_SUPPORTED: for platforms where the launch token is not provide from user space, the API returns ENCLAVE\_NOT\_SUPPORTED

### **Remarks**

This API sets specific enclave information based on the *info\_type* parameter.

## 5 Data Types

### 5.1 ENCLAVE\_ERROR Enumeration

Enclave error enumeration describes the result of an enclave action and the specific error occurred if any.

Enumeration	Value	Meaning
ENCLAVE_ERROR_SUCCESS	0x00000000	No error
ENCLAVE_NOT_SUPPORTED	0x00000001	Enclave type not supported, Intel® SGX is not supported, the Intel® SGX device is not present, or the AESM Service is not running.
ENCLAVE_INVALID_SIG_STRUCT	0x00000002	SGX – SIGSTRUCT contains an invalid value
ENCLAVE_INVALID_SIGNATURE	0x00000003	SGX – invalid signature or the SIGSTRUCT value
ENCLAVE_INVALID_ATTRIBUTE	0x00000004	SGX – invalid SECS attribute
ENCLAVE_INVALID_MEASUREMENT	0x00000005	SGX – invalid measurement
ENCLAVE_NOT_AUTHORIZED	0x00000006	Enclave not authorized to run. For example, the enclave does not have a signing privilege required for a requested attribute.
ENCLAVE_INVALID_ENCLAVE	0x00000007	Address is not a valid enclave
ENCLAVE_LOST	0x00000008	SGX – enclave is lost (likely due to a power event)
ENCLAVE_INVALID_PARAMETER	0x00000009	Invalid Parameter (unspecified) – may occur due to a wrong length or format type
ENCLAVE_OUT_OF_MEMORY	0x0000000a	Out of memory. May be a result of allocation failure in the API or internal function calls
ENCLAVE_DEVICE_NO_RESOURCES	0x0000000b	Out of EPC memory
ENCLAVE_ALREADY_INITIALIZED	0x0000000c	Enclave has already been initialized
ENCLAVE_INVALID_ADDRESS	0x0000000d	Address is not within a valid enclave Address has already been committed
ENCLAVE_RETRY	0x0000000e	Please retry the operation – an unmasked event occurred in EINIT
ENCLAVE_INVALID_SIZE	0x0000000f	Invalid size

<b>ENCLAVE_NOT_INITIALIZED</b>	0x00000010	Enclave is not initialized – the operation requires an initialized enclave
<b>ENCLAVE_SERVICE_TIMEOUT</b>	0x00000011	The launch service timed out when attempting to obtain a launch token. Check to ensure that the AESM service is running and accessible.
<b>ENCLAVE_UNEXPECTED</b>	0x00001001	Unexpected error in the API

## 5.2 ENCLAVE\_TYPE Enumeration

The enclave type enumeration describes the architecture type of the enclave.

Value	Meaning
<b>ENCLAVE_TYPE_SGX1</b> 0x00000001	Enclave for the Intel® Software Guard Extensions (Intel® SGX) architecture version 1.
<b>ENCLAVE_TYPE_SGX2</b> 0x00000002	Enclave for the Intel® Software Guard Extensions (Intel® SGX) architecture version 2 or SGX2. SGX2 adds Enclave Dynamic Memory Management instructions, which permit an application or the enclave to modify page attributes and add or remove pages from the enclave after EINIT.

## 5.3 ENCLAVE\_PAGE\_PROPERTIES Enumeration

Value	Meaning
<b>ENCLAVE_PAGE_READ</b> 0x00000001	Enables read access to the committed region of pages.
<b>ENCLAVE_PAGE_WRITE</b> 0x00000002	Enables write access to the committed region of pages.
<b>ENCLAVE_PAGE_EXECUTE</b> 0x00000004	Enables execute access to the committed region of pages.
<b>ENCLAVE_PAGE_THREAD_CONTROL</b> 0x00000100	Page contains a thread control structure.
<b>ENCLAVE_PAGE_UNVALIDATED</b> 0x00001000	Provided page contents are excluded from measurement and content validation.

---

## 5.4 ENCLAVE\_INFO\_TYPE Enumeration

---

The enclave type enumeration describes the information type.

Value	Meaning
<b>ENCLAVE_LAUNCH_TOKEN</b> 0x00000001	Get or set the launch token.

---

## 5.5 enclave\_create\_sgx\_t Structure

---

Contains architecture-specific information to use for enclave creation when the enclave type is ENCLAVE\_TYPE\_SGX1 or ENCLAVE\_TYPE\_SGX2, which specifies an enclave for the Intel® Software Guard Extensions (Intel® SGX) architecture.

### Syntax

```
typedef struct enclave_create_sgx_t {  
    uint8_t      secs[4096];  
} enclave_create_sgx_t;
```

### Members

*secs*

The Intel SGX enclave control structure (SECS) to use for the enclave creation.

---

## 5.6 enclave\_init\_sgx\_t Structure

---

Contains architecture-specific information used to initialize an enclave when the enclave type is ENCLAVE\_TYPE\_SGX1 or ENCLAVE\_TYPE\_SGX2, which specifies an enclave for the Intel® Software Guard Extensions (Intel® SGX) architecture.

### Syntax

```
typedef struct enclave_init_sgx_t {  
    uint8_t      sigstruct[1808];  
} enclave_init_sgx_t;
```

### Members

*sigstruct*

The Intel SGX Enclave Signature Structure (SIGSTRUCT) to use for the enclave initialization.

---

## 5.7 enclave\_launch\_token\_t Structure

---

Contains architecture-specific information used to initialize an enclave of type ENCLAVE\_TYPE\_SGX1 or ENCLAVE\_TYPE\_SGX2, which specifies an enclave for the Intel® Software Guard Extensions (SGX) architecture.

---

**Syntax**

```
typedef struct enclave_launch_token_t {  
    uint8_t    einittoken[304];  
} enclave_launch_token_t;
```

**Members**

*einittoken*

SGX Launch Token (EINITTOKEN) for the enclave initialization.



---

## 6 Disclaimer and Legal Information

No license (express or implied, by estoppel or otherwise) to any intellectual property rights is granted by this document.

Intel disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade.

This document contains information on products, services and/or processes in development. All information provided here is subject to change without notice. Contact your Intel representative to obtain the latest forecast, schedule, specifications and roadmaps.

The products and services described may contain defects or errors known as errata which may cause deviations from published specifications. Current characterized errata are available on request.

Intel technologies features and benefits depend on system configuration and may require enabled hardware, software or service activation. Learn more at Intel.com, or from the OEM or retailer.

Copies of documents which have an order number and are referenced in this document may be obtained by calling 1-800-548-4725 or by visiting [www.intel.com/design/literature.htm](http://www.intel.com/design/literature.htm).

Intel, the Intel logo, Xeon, and Xeon Phi are trademarks of Intel Corporation in the U.S. and/or other countries.

### Optimization Notice

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804

\* Other names and brands may be claimed as the property of others.

### Copyright 2014-2018 Intel Corporation.

This software and the related documents are Intel copyrighted materials, and your use of them is governed by the express license under which they were provided to you (**License**). Unless the License provides otherwise, you may not use, modify, copy, publish, distribute, disclose or transmit this software or the related documents without Intel's prior written permission.

This software and the related documents are provided as is, with no express or implied warranties, other than those that are expressly stated in the License.