

Intel[®] Open Network Platform Release 2.0 Performance Test Report

SDN/NFV Solutions with Intel® Open Network Platform

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1.0 Audience and Purpose

Intel® Open Network Platform (Intel ONP) is a Reference Architecture that provides engineering guidance and ecosystem-enablement support to encourage widespread adoption of Software-Defined Networking (SDN) and Network Functions Virtualization (NFV) solutions in Telco, Enterprise, and Cloud. Intel® Open Network Platform Reference Architecture Guides, Performance Test Reports and Release Notes are available on 01.org.

The primary audiences for this test report are architects and engineers implementing the Intel[®] ONP Reference Architecture using open-source software ingredients that include:

- OpenStack*
- OpenDaylight*
- Data Plane Development Kit (DPDK)*
- Open vSwitch* with DPDK
- Fedora*.

This test report provides a guide to packet processing performance testing of the Intel® ONP. The report includes baseline performance data and provides system configuration and test cases relevant to SDN/NFV. The purpose of documenting these configurations and methods is not to imply a single "correct" approach, but rather to provide a baseline of well-tested configurations and test procedures. This will help guide architects and engineers who are evaluating and implementing SDN/NFV solutions more generally and can greatly assist in achieving optimal system performance.

Ideally, the same hardware platform specifications and software versions are used for both Intel[®] ONP Reference Architecture Guide and Performance Test Reports. Exceptions can however occur due to software issues, version revisions and other factors during integration and benchmarking activities. Information on these exceptions is provided in Intel[®] ONP Release 2.0 Hardware and Software Specifications Application Note available on 01.org.



2.0 Summary

Benchmarking an SDN/NFV system is not trivial and requires expert knowledge of networking and virtualization technologies. Engineers also need benchmarking and debugging skills, as well as a good understanding of the device-under-test (DUT) across compute, networking, and storage domains. Knowledge of specific network protocols and hands-on experience with relevant open-source software, such as Linux, kernel-based virtual machine (KVM), quick emulator (QEMU), DPDK, OvS, etc., are also required.

Repeatability is essential when testing complex systems and this can be difficult to achieve with manual configuration and test methods. Automated integration, deployment and test methods are needed for developing robust SDN/NFV solutions. Many of these challenges are being addressed through industry forums such as OPNFV. Future versions of Intel® ONP will also provide guidance on adopting automation tools and methods.

This report is built on earlier Intel[®] ONP test reports (available on 01.org as archived documents). These earlier reports have baseline performance data and configuration procedures (for Linux operating system setup, BIOS configurations, configuration for OvS, VM setup, building DPDK and OvS, etc.).

The focus of this report is to present the packet processing performance using the Intel® Xeon® Processor D-1540 (formerly Broadwell-DE) that can provide up to 8 Xeon®-class physical cores in a single, power-efficient System-on-Chip (SoC) and is ideal for NFV solutions for virtual customer premises equipment (vCPE). Note that for reasons of availability and stability, this benchmarking used software ingredient versions specified in Intel® ONP 1.5.

The software stack with Open vSwitch updated to the version 2.4.9 was benchmarked using Intel[®] Xeon[®] Processor E5-2697 v3 (formerly Haswell) and compared to data published with Intel[®] ONP 1.5.

In this document "native OvS" refers to Open vSwitch which is a multilayer virtual switch licensed under the open source Apache 2.0 license (http://openvswitch.org/). The DPDK accelerated version of OvS is referred to as "OvS with DPDK" in this document.

This report includes:

- New versions of software ingredients (compared to Intel ONP 1.5)
- vHost user for QEMU VM fast-path interface
- 40Gbps performance testing with the Intel® Ethernet X710-DA2 Adapter
- Updated Virtual eXtensible LAN (VXLAN) performance tests
- Tuning methods and troubleshooting tips for achieving good packet processing performance with Open vSwitch with DPDK
- Information on industry NFV test activities
- Performance using host with DPDK i.e. "bare-metal" (used to establish a performance baseline)
- Virtual Switch performance comparing native OvS and OvS with DPDK
- Performance scaling of OvS with DPDK
- Throughput performance with one and two VMs
- VXLAN performance comparing native OvS and OvS with DPDK.



3.0 Platform Specifications

3.1 Hardware Ingredients

Table 3-1 Intel[®] Xeon[®] Processor D-1540 SoC Platform – hardware ingredients used in performance tests

Item	Description
Server Platform	SuperMicro SuperServer 5018D-FN4T
	Dual LAN via Intel® i350-AM2 Gigabit Ethernet
	Dual LAN via SoC 10GBase-T
	One processor per platform
Chipset	Lynx-H Chipset
Processor	Intel® Xeon® Processor D-1540 (formerly Broadwell-DE)
	2-2.6 GHz; 45 W; 12 MB cache per processor
	8 cores, 16 hyper-threaded cores per processor
Memory	32GB Total; Micron 8GB 1Rx4 PC4-2133MHz, 16GB per channel, 2 Channels,
Local Storage	500 GB HDD Seagate SATA Barracuda 7200.12 (SN:Z6EM258D)
PCle	1x PCI-E 3.0 x16 slot
NICs	1 x Intel® Ethernet Converged Network Adapter X710-DA4, total: 4 Ports (formerly Fortville)
BIOS	AMIBIOS Version: 1.0a Release Date: 05/27/2015

Table 3-2 Intel® Xeon® Processor E5-2697 v3 Platform – hardware ingredients used in performance tests

Item	Description
Server Platform	Intel® Server Board S2600WT2 DP (formerly Wildcat Pass)
	Two processors per platform
Chipset	Intel® C610 series chipset (formerly Wellsburg)
Processor	Dual Intel® Xeon® Processor E5-2697 v3 (formerly Haswell)
	2.60-3.6 GHz; 145 W; 35 MB cache per processor
	14 cores, 28 hyper-threaded cores per processor for 56 total hyper-threaded cores
	9.6 GT/s QPI; DDR4-1600/1866/2133
Memory	128 GB Total; Micron 16 GB 1Rx4 PC4-2133MHz, 16 GB per channel, 8 Channels
Local Storage	500 GB HDD Seagate SATA Barracuda 7200.12 (SN:9VMKQZMT)
PCle	Port 3a and Port 3c x8
NICs	2 x Intel® Ethernet Converged Network Adapter X710-DA2 Adapter total: 4 x 10GE Ports (formerly Fortville)
QAT	QAT acceleration was not used for benchmarking purposes.
BIOS	SE5C610.86B.01.01.0008.021120151325 Release Date: 02/11/2015



3.2 Software Versions

Table 3-3 Software Versions – Platform with Intel® Xeon® D-1540

Software Component	Version
Host Operating System	Fedora 21 x86_64 (Server version)
	Kernel version: 3.17.4-301.fc21.x86_64
VM Operating System	Fedora 21 (Server version)
	Kernel version: 3.17.4-301.fc21.x86_64
libvirt	libvirt-1.2.9.3-2.fc21.x86_64
QEMU	QEMU-KVM version 2.2.1
	http://wiki.qemu-project.org/download/qemu-2.2.1.tar.bz2
DPDK	DPDK 2.0.0
	http://www.dpdk.org/browse/dpdk/snapshot/dpdk-2.0.0.tar.gz
Native OvS	Ov\$ 2.4.0
	./configurewith-linux=/lib/modules/3.17.4-301.fc21.x86_64/build/ CFLAGS="-Ofast -g" (PHY-PHY)
	make CLFAGS="-Ofast -g -march=native"
OvS with DPDK	OvS 2.4.0
	DPDK compiled with "-Ofast -g"
	./configurewith-dpdk= <dpdk path="" sdk="">/x86_64-native-linuxapp CFLAGS="-Ofast -g" (PHY-PHY)</dpdk>
	make CLFAGS="-Ofast -g -march=native"
	http://openvswitch.org/releases/openvswitch-2.4.0.tar.gz

Table 3-4 Updated software versions tested with Intel® Xeon® E5-2697 v3 (refer to versions in Table 3-3)

Software Component	Version
OvS with DPDK	Open vSwitch Release 2.4.9
	Commit ID 2a33a3c20f56b8ac09abebc8b14fca9314abfacb



3.3 Boot Settings

Table 3-5 Boot Settings

System Capability	Description
Host Boot Settings	Hugepage size = 1G ; No. of Hugepages = 16 Hugepage size=2MB; No. of Hugepages = 2048 Hyper-threading disabled: isolcpus = 1-7 Hyper-threading enabled: isolcpus = 1-7,9-15
BIOS settings	P-state Enabled, HT Off/On and Turbo Boost enabled
VM Kernel Boot Parameters	<pre>GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024 isolcpus=1,2 rhgb quiet"</pre>

3.4 Compile Options

Table 3-6 Compile Option Configurations

System Capability	Configuration
DPDK Compilation	CONFIG_RTE_BUILD_COMBINE_LIBS=y CONFIG_RTE_LIBRTE_VHOST=y CONFIG_RTE_LIBRTE_VHOST_USER=y DPDK compiled with "-Ofast -g"
OvS Compilation	<pre>OvS configured and compiled as follows: # ./configurewith-dpdk=\</pre>
DPDK Settings	Build L3fwd: (in l3fwd/main.c) #define RTE_TEST_RX_DESC_DEFAULT 2048 #define RTE_TEST_TX_DESC_DEFAULT 2048



3.5 Operating System Settings

Table 3-7 Operating System Settings

System Capability	Settings
Linux OS Services Settings	<pre># systemctl disable NetworkManager.service</pre>
	# chkconfig network on
	<pre># systemctl restart network.service</pre>
	<pre># systemctl stop NetworkManager.service</pre>
	<pre># systemctl stop firewalld.service</pre>
	<pre># systemctl disable firewalld.service</pre>
	<pre># systemctl stop irqbalance.service</pre>
	# killall irqbalance
	<pre># systemctl disable irqbalance.service</pre>
	<pre># service iptables stop</pre>
	# kill -9 dhclient
	<pre># echo 0 > /proc/sys/kernel/randomize_va_space</pre>
	SELinux disabled
	net.ipv4.ip_forward=0
Linux Module Settings	<pre># rmmod ipmi_msghandler</pre>
	# rmmod ipmi_si
	<pre># rmmod ipmi_devintf</pre>
	<pre># rmmod lpc_ich</pre>
	# rmmod bridge



4.0 Test Configurations

The test setup is shown in Figure 4-1. The system-under-test is Intel® ONP Reference Architecture Release 2.0. The traffic is generated by Ixia running RFC 2544 (IxNetwork 7.40.929.15 EA; Protocols: 4.40.1075.13; IxOS: IxOS 6.80.1100.7 EA). The maximum theoretical system forwarding throughput is 40 Gbps aggregated across four 10 GbE ports, except for VXLAN tests which use two ports. Physical ports are paired (one as ingress and one as egress), i.e., one 10 Gbps bidirectional flow "consumes" two ports. Unless otherwise stated, all tests are for zero packet loss.

The VM network interface used is vhost-user with DPDK acceleration. The vhost-user information is available at http://dpdk.readthedocs.org/en/latest/prog_guide/vhost_lib.html along with DPDK documentation.

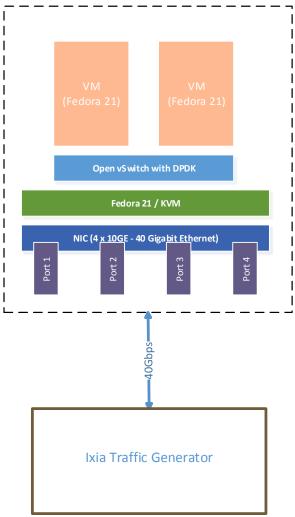


Figure 4-1 High-Level Overview of Test Setup



Allocation of cores has large impact on performance. This document include test configurations with hyper-threading enabled and disabled as well as 1, 2 and 4 physical cores. Table 4-1 summarizes the combinations of physical and hyper-threaded cores used for each test case.

	Hyper-Threading off Number of Physical cores		Hyper-Threading on Number of Hyper-Threaded Cores		ded Cores	
Test	1	2	4	2	4	8
Host Tests	~	~	✓			
Virtual Switching Tests	~	~	~	~	~	
PHY-to-VM Tests	~	~	~	~	~	
VM-to-VM Tests	~	✓		✓	~	
VXLAN Tests	~	~		~		

Table 4-1 Number of cores used for each test category (8 physical cores total per CPU)

4.1 Traffic Profiles

The IP traffic profile used conforms to RFC 2544:

- Frame sizes (bytes): 64, 128, 256, 512, 1024, 1280, and 1518
- L3 protocol: IPv4
- L4 protocol: UDP
- All tests are bidirectional with the same data rate being offered from each direction.
- For VXLAN, a header is used to encapsulate IP packets per RFC 7348.



5.0 Test Metrics

5.1 Packet Processing Performance Metrics

RFC 2544 is an Internet Engineering Task Force (IETF) RFC that outlines a benchmarking methodology for network interconnect devices. The methodology results in performance metrics (e.g., latency, frame loss percentage, and maximum data throughput).

In this document, network "throughput" (measured in millions of frames per second) is based on RFC 2544, unless otherwise noted. "Frame size" refers to Ethernet frames ranging from the smallest frames of 64 bytes to the largest of 1518 bytes.

RFC 2544 specifies the following types of tests:

- Throughput tests define the maximum number of frames per second that can be transmitted without any error. Throughput is the fastest rate at which the count of test frames transmitted by the DUT is equal to the number of test frames sent to it by the test equipment. Test time during which frames are transmitted must be at least 60 seconds.
- Latency tests measure the time required for a frame to travel from the originating device through the network to the destination device.
- Frame loss tests measure the network's response in overload conditions—a critical indicator of the network's
 ability to support real-time applications in which a large amount of frame loss rapidly degrades service quality.
- **Burst tests** assess the buffering capability of a switch. They measure the maximum number of frames received at full-line rate before a frame is lost. In carrier Ethernet networks, this measurement validates the excess information rate as defined in many service-level agreements (SLAs).
- System recovery tests characterize speed of recovery from an overload condition.
- Reset tests characterize the speed of recovery from device or software reset.

"Test duration" refers to the measurement period for a particular packet size with an offered load and assumes the system has reached a steady state. Using the RFC 2544 test methodology, this is specified as at least 60 seconds.



5.2 Throughput

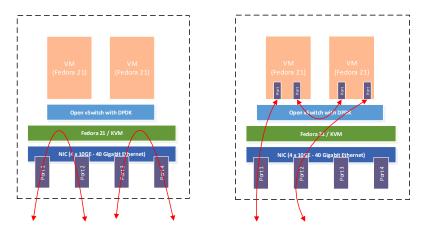
The throughput test data provided in this document represents "platform throughput" as measured by the Ixia traffic generator. Switching performance metrics include the number of switching operations for the particular configuration. This is illustrated in Table 5-1 using two examples of configurations with two and three switching operations, respectively. Figure 5-1 shows the two configuration examples. Careful analysis of all configuration variables is needed before making performance comparisons that are meaningful.

	Configuration Examples		
Parameter	PHY-OVS-PHY	PHY-VM-VM-PHY	
Physical Ports	4	2	
Physical cores	2	2	
Hyper-threaded cores	4	4	
Flows per Port (in each direction)	1	1	
Total Flows	4	2	
Switching Operations	2	3	
Line-rate	40 Gbps	20 Gbps	
OvS with DPDK: Throughput (packets/sec) 128B packets	28,786,530 pps 85% of line rate	5,590,820 pps 33% of line rate	
OvS with DPDK: Switching (packets/sec) 128B packets	57,573,059 pps 170% of line rate	16,772,460 pps 99% of line rate	

Table 5-1 Throughput and	switching performance	metrics for two example test-case	S
rubie o i iniougriputure	Surfering periorinaries	method for the chample lest case	-

In this example while the number of physical cores and hyper-threaded cores are the same in both scenarios the number of ports and switching operations is different. Line rate is therefore different and cannot be used as a comparison metric. A more reasonable comparison would be to compare the switching performance as measured by the throughput (packets/second) multiplied by the number of switching operations. In this case we can see that by adding 2 VMs introduces an extra switching operation and results in approximately 70% additional overhead compared to the case with only a vSwitch and no VMs.







5.2.1 Layer 2 Throughput

This test determines the DUT's maximum Layer 2 forwarding rate without traffic loss, as well as average and minimum/maximum latency for different packet sizes.

This test is performed full duplex with traffic transmitting in both directions.

The DUT must perform packet parsing and Layer 2 address lookups on the ingress port, and then modify the header before forwarding the packet on the egress port.

5.2.2 Layer 3 Throughput

This test determines the DUT's maximum IPv4 Layer 3 forwarding rate without packet loss, as well as average and minimum/maximum latency for different packet sizes.

This test is performed full duplex with traffic transmitting in both directions.

The DUT must perform packet parsing and route lookups for Layer 3 packets on the ingress port and then forward the packet on the egress port without modifying the header.

5.3 Latency

With latency (i.e., packet delay) and packet delay variation, it is generally the worst-case performance that must be considered. Outliers can create customer disappointment at the carrier scale and cost service providers.

The RFC 2544 measurement of latency is extensively used in traditional testing. NFV requires more information on latency, including packet delay variation. Ideally, the delay of all packets should be considered, but in practice some form of sampling is needed (this may not be periodic sampling).

Average and minimum/maximum latency numbers are usually collected with throughput tests; however, the distribution of latency is a more meaningful metric (i.e., a test that collects latency distribution for different packet sizes and over an extended duration to uncover outliers; latency tests should run for at least 1 hour and ideally for 24 hours). Collecting test data for all traffic conditions can take a long time. One approach is to use the highest throughput that has demonstrated zero packet loss for each packet size as determined with throughput tests.



RFC 2679 defines a metric for one-way delay of packets across Internet paths and describes a methodology for measuring "Type-P-One-way-Delay" from source to destination.

5.4 Packet Delay Variation (PDV)

RFC 3393 provides definitions of PDV metrics for IP packets and is based on RFC 2679. This RFC notes that variation in packet delay is sometimes called "jitter" and that this term causes confusion because it is used in different ways by different groups of people. The ITU Telecommunication Standardization Sector also recommends various delay variation metrics [Y.1540] [G.1020]. Most of these standards specify multiple ways to quantify PDV.

RFC 5481 specifies two forms of measuring variation of packet delay:

- Inter-Packet Delay Variation (IPDV) is where the reference is the previous packet in the stream (according to a sending sequence), and the reference changes for each packet in the stream. In this formulation, properties of variation are coupled with packet sequence. This form was called Instantaneous Packet Delay Variation in early IETF contributions and is similar to the packet spacing difference metric used for inter-arrival jitter calculations in RFC 3550.
- Packet Delay Variation (PDV) is where a single reference is chosen from the stream based on specific criteria. The most common criterion for the reference is the packet with the minimum delay in the sample. This term derives its name from a similar definition for Cell Delay Variation, an ATM performance metric [1.356].

Both metrics are derived from "one-way-delay" metrics and, therefore, require knowledge of time at the source and destination. Results are typically represented by histograms showing statistical distribution of delay variation. Packet loss has great influence for results (extreme cases are described in the RFC). For reporting and SLA purposes, simplicity is important and PDV lends itself better

(e.g., percentiles, median, mean, etc.). PDV metrics can also be used with different stream characteristics, such as Poisson streams [RFC 3393] and periodic streams [RFC 3432], depending on the purpose and testing environment.



6.0 Test Cases

A summary of test cases is shown in Table 6-1.

Table 6-1 Summary of Test Cases

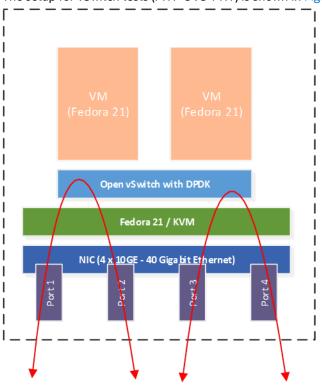
Ref.	Test Description	Metrics	Packet Size (Bytes)	Test Duration	Flows per Port
Host F	Performance (PHY-PHY)				
8.1	L3 Fwd (no pkt modification) 4 ports	Throughput Latency (min, max, avg)	64, 72, 128, 256, 512, 768, 1024, 1280, 1518	60 sec	One flow/port in both directions
vSwite	h Performance (PHY-OVS-PHY)				
8.2	L3 Fwd 4 ports	Throughput Latency (min, max, avg)	64, 72, 128, 256, 512, 768, 1024, 1280, 1518	60 sec	One flow/port in both directions; One thousand flows/port in both directions
One V	M Throughput (PHY-VM-PHY)				
8.3	Single VM (vhost-user) L3 Fwd 4 ports	Throughput Latency (avg)	64, 256	60 sec	One flow/port in both directions; One thousand flows/port in both directions
Two V	M Throughput (PHY-VM-VM-PHY)				
8.4	Two VMs in series (vhost-user) L3 Fwd 2 ports	Throughput Latency (min, max, avg)	64, 72, 128, 256, 512, 768, 1024, 1280, 1518	60 sec	One flow/port in both directions
VXLAN	N Performance (PHY-OVS-VM-OVS	-PHY)		1	
8.5	VXLAN encap/decap using vSwitch Tunnel End Point (TEP) L3 Fwd 2 ports	Throughput Latency (min, max, avg)	64, 72, 128, 256, 512, 768, 1024, 1280, 1518	60 sec	One flow/port in both directions



7.0 Test Results - Intel[®] Xeon[®] Processor E5-2697 v3

The Intel® ONP Release 1.5 Performance Report showed that OvS with DPDK provides significantly better performance over native OvS with Intel® Xeon® E5-2697 v3 (report available on 01.org as archived document). Intel® ONP performance reports have showed that performance of OvS with DPDK continues to improve over time.

Intel® ONP Release 2.0 Reference Architecture Guide uses Open vSwitch with DPDK version 2.4.9. Benchmarks showed that performance is similar to Open vSwitch with DPDK version 2.4.0 used in Intel® ONP Release 1.5 Reference Architecture Guide. A number of features expected to significantly improve performance were not yet up streamed in time for Intel® ONP Release 2.0.



The setup for vSwitch tests (PHY-OVS-PHY) is shown in Figure 7-1 below.

Figure 7-1 Setup for vSwitch Tests (PHY-OVS-PHY)

Table 7-1 Configuration	variables for testing performa	nce of OvS with DPDK

Configuration variables	Physical NIC Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	4	1	1, 2, 4	No hyper-threaded cores

Figure 7-2 presents throughput performance results for vSwitch test case with OvS 2.4.0 (ONP 1.5 Release) and OvS 2.4.9 (ONP 2.0 Release) in use.



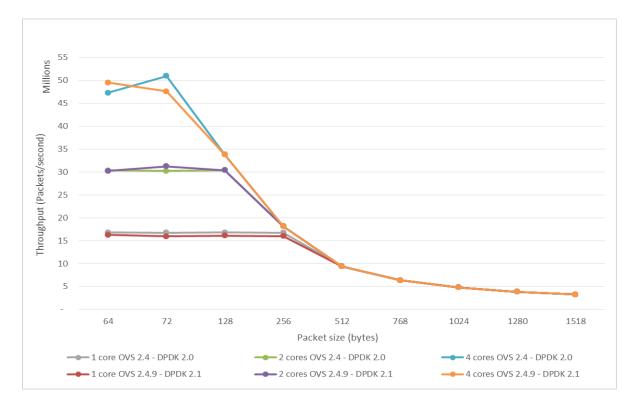


Figure 7-2 Throughput performance for vSwitch test case with 1, 2, and 4 physical cores comparing OvS 2.4.0 and OvS 2.4.9

Test results shown in Figure 7-2 indicate that performance of OvS with DPDK version 2.4.9 (Intel[®] ONP Release 2.0 Reference Architecture Guide) is similar to version 2.4.0 (Intel[®] ONP Release 1.5 Reference Architecture Guide).

	Packets/sec						
Packet size	1 core		2 cores	2 cores		4 cores	
(bytes)	OvS 2.4.0 - DPDK 2.0.0	OvS 2.4.9 - DPDK 2.1.0	OvS 2.4.0 - DPDK 2.0.0	OvS 2.4.9 - DPDK 2.1.0	OvS 2.4.0 - DPDK 2.0.0	OvS 2.4.9 - DPDK 2.1.0	
64	16,766,108	16,248,187	30,347,400	30,232,181	47,266,490	49,510,704	
72	16,726,835	15,938,685	30,230,572	31,228,848	50,932,610	47,569,946	
128	16,766,876	16,080,954	30,387,023	30,387,023	33,783,782	33,783,781	
256	16,749,871	15,979,229	18,115,941	18,115,940	18,115,939	18,115,940	
512	9,398,497	9,398,495	9,398,496	9,398,497	9,398,490	9,398,495	
768	6,345,179	6,345,179	6,345,177	6,345,172	6,345,174	6,345,177	
1024	4,789,273	4,789,273	4,789,272	4,789,272	4,789,271	4,789,271	
1280	3,846,154	3,846,154	3,846,154	3,846,154	3,846,153	3,846,153	
1518	3,250,973	3,250,976	3,250,975	3,250,975	3,250,975	3,250,973	

Table 7-2 Throughput performance with	1.2 and 4 physical cores	comparing OvS 2.4.0 and OvS 2.4.9
Table 7 2 Throughput performance with	1, 2, and 4 physical cores	



8.0 Test Results - Intel[®] Xeon[®] CPU D-1540

8.1 Host Performance (PHY-PHY)

The test setup for measuring host (i.e. no VM) throughput performance is shown in Figure 8-1. The host is installed with DPDK and it uses DPDK's L3 forwarding sample application. This test creates a good baseline for comparing more complex test cases as well as comparing "bare-metal" performance between different platforms. This is very important when trying to establish "apples-for-apples" comparisons for more complex test scenarios between different platforms. If the "bare-metal" performance cannot be calibrated between the platforms on a per-core / per-port basis, more complex scenarios involving virtualization will certainly not provide valid comparisons. Host tests attempt to achieve system throughput of 40 Gbps, using a 4-port configuration with 4 physical cores.

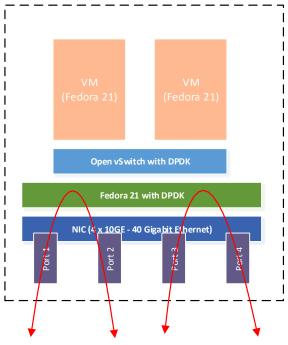


Figure 8-1 Host Performance test setup (PHY-PHY)

Table 8-1 summarizes the permutations of test configuration variables. In this case all tests uses 4 ports, 1 TX/RX queue per port, 1 bi-directional flow per port (i.e. total of 4 unidirectional flows). Permutations were tested with 1, 2, and 4 physical cores and hyper-threading was not enabled.

	Configuration	Variable			
Test	Ports	TX/RX Queues per Core	Flows per Port in Each Direction	Physical Cores	Hyper-Threaded Cores
L3 Forwarding	4	1	1	1, 2, 4	0

Table 8-1 PHY-PHY test configuration variables

Test results are shown in the Figure 8-2 below for all packet sizes.



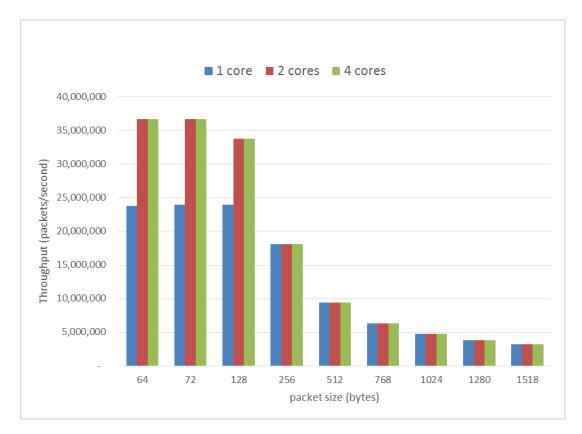


Figure 8-2 Forwarding throughput performance with 1, 2, and 4 physical cores

L3 test data is presented for the 2 core configuration in the

Table 8-2. Line-rate (i.e. 40 Gbps) is achieved for all packet sizes equal or larger than 128 bytes.

L3 Forwarding –	– Bidirectional T	hroughput with Zero Pa	acket Loss			
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)
64	24,647	36,677,494	62	22	7	211
72	27,006	36,693,290	68	22	7	207
128	39,961	33,751,256	100	17	9	198
256	40,000	18,115,905	100	107	13	193
512	40,000	9,398,481	100	115	19	201
768	40,000	6,345,164	100	120	25	203
1024	40,000	4,789,264	100	126	15	217
1280	40,000	3,846,147	100	131	38	214
1518	40,000	3,250,969	100	135	36	221
Affinity Details	./l3fwd -c 0xc 4P FVL Card	-n 2p 0x3config=	"(0,0,2)(1,0,2)(2,	0,3)(3,0,3)"	·	·

Table 8-2 PHY-PHY test results for 2 physical cores (HT disabled)



8.2 Virtual Switching Performance (PHY-OVS-PHY)

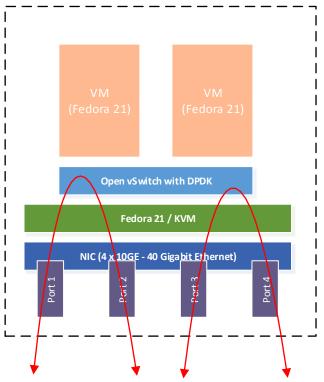


Figure 8-3 shows test setup for PHY-OVS-PHY with four 10GbE ports.

Figure 8-3 Virtual Switching Performance test setup (PHY-OVS-PHY)

Virtual switching tests attempt to achieve aggregated system throughput of 40 Gbps using 4 ports to compare the following variables' configurations:

- Native OvS versus OvS with DPDK
- 1, 2, or 4 physical cores
- One flow per port (total four flows) or 1K flows per port (total 4K flows)
- Hyper-threading on or off
- 1 physical core vs 2 hyper-threaded cores
- 2 physical cores vs 4 hyper-threaded cores.



8.2.1 Native OvS and OvS with DPDK

This test compares the throughput performance of native OvS and OvS with DPDK for various packet sizes, assuming one flow per port and single physical core in use (hyper-threads enabled and disabled). The data shows that for small packet sizes throughput increases by over 7x using OVS with DPDK.

Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
Native OvS	4	1	1	2
OvS with DPDK	4	1	1	0, 2



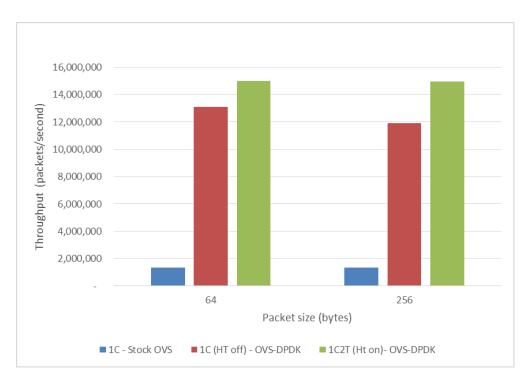


Figure 8-4 Throughput performance of native OvS (with hyper-threading) and OvS with DPDK (without and with hyper-threading)



L3 Forwarding -	– Bidirectional T	hroughput with Zero Pa	acket Loss			
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)
64	903	1,343,374	2	19	7	5,673
72	980	1,331,648	2	19	6	6,731
128	1,599	1,350,381	4	19	7	6,703
256	2,991	1,354,647	7	19	7	6,937
512	5,814	1,366,107	15	20	7	7,810
768	8,715	1,382,391	22	21	8	9,026
1024	11,499	1,376,798	29	21	7	9,730
1280	14,322	1,377,125	36	22	7	9,965
1518	17,107	1,390,323	43	223	8	10,761
Affinity Details	0% Loss resolution 4P FVL Card Port0 IRQ's Affinity to lcore2 Port1 IRQ's Affinity to lcore3 Port2 IRQ's Affinity to lcore4 Port3 IRQ's Affinity to lcore5					

Table 8-4 PHY-OVS-PHY test results for native OvS (1 core with HT, 1 flow per port)

Table 8-5 PHY-OVS-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port)

L3 Forwarding –	- Bidirectional T	hroughput with Zero	Packet Loss			
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)
64	10,068	14,982,108	25	18	8	249
72	11,035	14,992,903	28	19	9	343
128	17,764	15,003,137	44	21	9	323
256	33,000	14,945,853	83	21	13	409
512	40,000	9,398,496	100	31	19	53
768	40,000	6,345,178	100	29	17	48
1024	40,000	4,789,272	100	25	16	42
1280	40,000	3,846,154	100	27	16	43
1518	40,000	3,250,975	100	28	14	58
Affinity Details	2PMD thread based OvS and 0.0% Loss resolution tails ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=404 4P FVL Card					



8.2.2 Performance Scaling of OvS with DPDK

Figure 8-5 shows throughput performance of processing 64-byte packets with 1, 2, and 4 physical cores and compares 4 flows (1 flow per port) with 4K total flows (1K flows per port). This data show fairly linear performance scaling as the number of cores is increased.

Today, due to the current configuration of OvS hash lookup tables, significant degradation in performance is observed when using more than 8K flows. This is related to the size of the EMC (exact match cache) which is a hash table in OvS. The current size of the EMC is set to 8K (flows) by default. Using this default configuration, a larger numbers of flows may result in packets using a slow data path.

Table 8-6 PHY-OVS-PHY test configuration variables for OvS with DPDK (scaling with physical cores)

Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	4	1, 1k	1, 2, 4	0

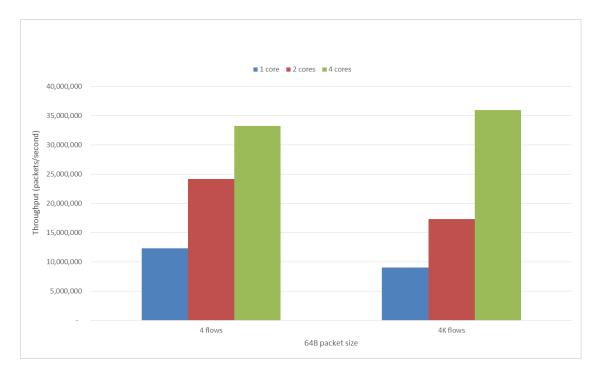


Figure 8-5 64-byte throughput performance scaling of OvS with DPDK for 1, 2, 4 cores, no hyper-threading



L3 Forwarding — Bidirectional Throughput with Zero Packet Loss						
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)
64	24,183	35,987,156	60	12	8	217
72	26,426	35,905,137	66	11	8	233
128	39,962	33,751,273	100	18	10	192
256	40,000	18,115,937	100	41	17	83
512	40,000	9,398,494	100	38	18	62
768	39,961	6,339,051	100	12	7	79
1024	40,000	4,789,269	100	45	18	87
1280	40,000	3,846,150	100	76	17	106
1518	40,000	3,250,971	100	84	16	131
Affinity Details	4PMD thread based OvS and 0.0% Loss resolution ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3c 4P FVL Card					

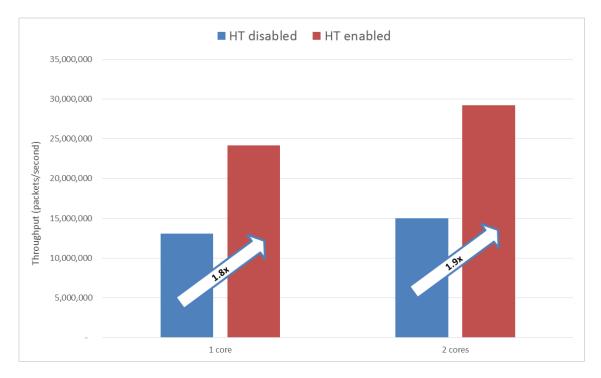
Table 8-7 PHY-OVS-PHY test results for OvS with DPDK (4 cores, 1k flows per port, HT off)

8.2.3 Performance with Hyper-Threading

The impact of hyper-treading on performance is illustrated below by comparing 64B throughput with 1 and 2 physical cores and hyper-threading turned on and off.

Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	4	1, 1k	1, 2	0, 2, 4







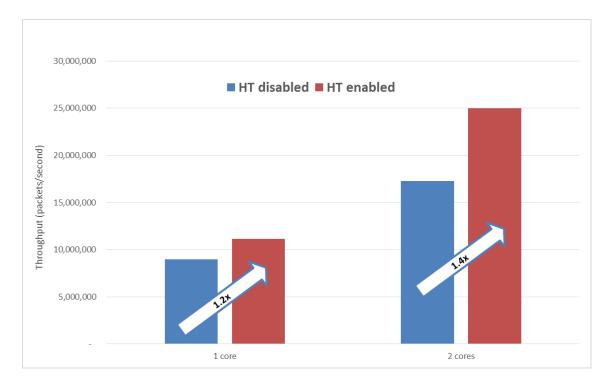


Figure 8-7 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows)



8.3 One VM Throughput (PHY-VM-PHY)

This test uses a single VM with two bidirectional flows across four 10 GbE ports as shown in Figure 8-8. Maximum theoretical platform throughput is 40Gbps (four flows aggregated).

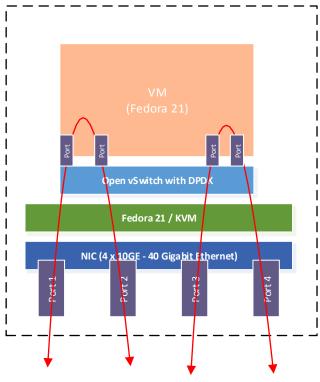


Figure 8-8 One VM Throughput Performance test setup

Note: Four switching operations take place while packets are being routed through the system.

The single VM tests attempt to achieve aggregated system throughput of 40 Gbps using 4 ports to compare the following configurations for small packet sizes:

- L3 performance of native OvS versus OvS with DPDK
- L3 performance of OvS with DPDK
- Hyper-threading on or off (1 flow per port and 1k flows per port using 1 or 2 physical cores)
- Using 1 core, 2 cores, 4 cores (1 flow per port and 1k flows per port).

8.3.1 Native OvS and OvS with DPDK

This test compares the throughput performance of native OvS and OvS with DPDK.

In Figure 8-9 both native OvS and OvS with DPDK use one physical core. The relative performance ratio of OvS with DPDK is shown with and without hyper-threading.



Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
Native OvS	4	1	1	2
OvS with DPDK	4	1	1	0, 2

Table 8-9 PHY-VM-PHY test configuration variables for native OvS and OvS with DPDK

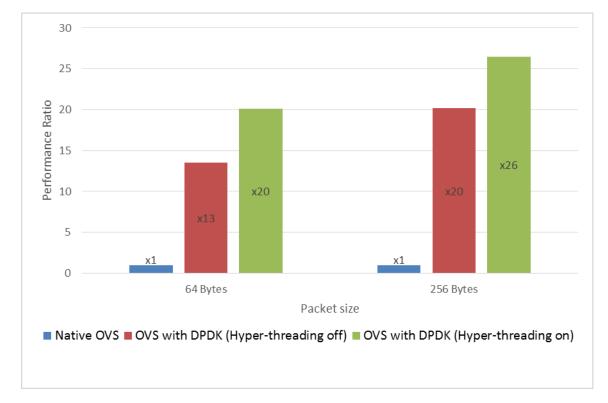


Figure 8-9 Throughput performance with 1 VM comparing native OvS and OvS with DPDK (using one physical core)

Table 8-10 shows measured small packet performance data for native OvS with hyper-threading on. Throughput performance is less than 1% of line-rate. Table 8-11 shows measured small packet performance of OvS with DPDK also with one physical core (with hyper-threading). The throughput performance of OvS with DPDK is over 20 times greater than native OvS.



	L3 Forwarding — Bidirectional					
	Throughput with Z	ero Packet Loss				
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)		
64	164	243,773	0.41	59		
256	380	172,250	0.95	35		
Affinity Details	0% Loss resolution 4P from two DUAL Port0 IRQ's Affinity Port1 IRQ's Affinity Port2 IRQ's Affinity Port3 IRQ's Affinity On a VM: ./testpmo	cards y to lcore2 y to lcore3 y to lcore4 y to lcore5	32 -itxd=2048rxd=20	048txqflags=0xf00		

Table 8-10 PHY-VM-PHY test results for native OvS (1 core with HT, 1 flow per port)

Table 8-11 PHY-VM-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port)

	L3 Forwarding — Bidirectional				
	Throughput with Zero Packet Loss				
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	
64	2,218	3,299,968	6	28	
256	7,670	3,473,872	19	65	
Affinity Details	1PMD thread based OvS and 0.0% Loss resolution ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4 ./testpmd -c 0x3 -n 4burst=32 -itxd=2048rxd=2048txqflags=0xf00 4P FVL Card				



Figure 8-10 shows improved performance of OvS with DPDK using 2 physical cores instead of only 1. This test illustrates the ability of OvS with DPDK to increase performance by consuming additional cores. For both 64B and 256B packets the performance more than doubles.

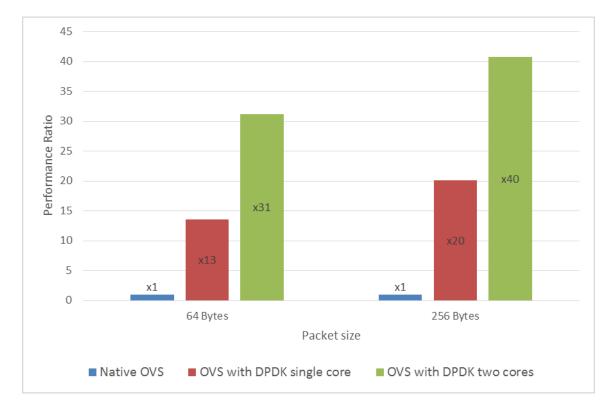


Figure 8-10 Throughput performance with a 1 VM comparing native OvS and OvS with DPDK using one and two physical cores (no hyper-threading)

Table 8-12 shows measured small packet performance data for OvS with DPDK using 2 physical cores.

	L3 Forwarding — Bidirectional				
	Throughput with Ze	ero Packet Loss			
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	
64	5,118	7,616,028	13	75	
256	15,521	7,029,304	39	96	
Affinity Details	2PMD thread based OvS and 0.0% Loss resolution ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3c ./testpmd -c 0x3 -n 4burst=32 -itxd=2048rxd=2048txqflags=0xf00 4P FVL Card				

			(
Table 8-12 OvS with	DPDK no	hyper-threading	(2 physical	cores)
	Di Digino	ingper an caaing	(E priyoreat	



8.3.2 Performance Scaling of OvS with DPDK

		<u> </u>		
Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	4	1, 1k	1, 2, 4	0



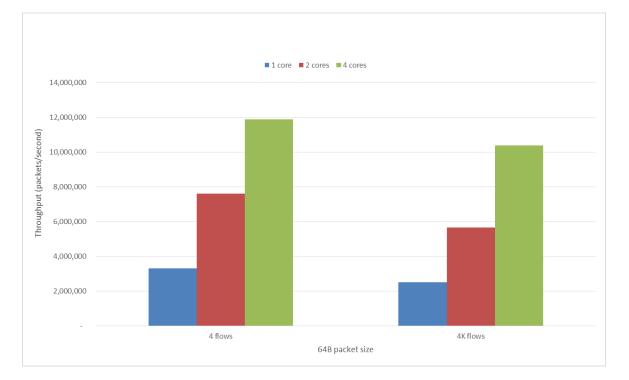


Figure 8-11 64-byte throughput performance scaling of OvS with DPDK - 1 VM (PHY-VM-PHY) with 1, 2, 4 cores, no hyper-threading

	L3 Forwarding — Bidirectional					
	64B throughput with Zero Packet Loss					
Number of cores	Mbps	Packets/sec	% Line Rate	Average Latency (us)		
1	2,218	3,299,968	6	28		
2	5,118	7,616,028	13	75		
4	7,980	11,874,557	20	29		
Affinity Details	1PMD, 2PMD, 4PMD thread based OvS and 0.0% Loss resolution ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4 (1 core) ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=c (2 cores) ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3c (4 cores) ./testpmd -c 0x3 -n 4burst=32 -itxd=2048rxd=2048txqflags=0xf00 4P FVL Card					

Table 8-14 PHY-VM-PHY test results for OvS with DPDK (1, 2, 4 cores, 1 flow per port, HT off)



8.3.3 Performance with Hyper-Threading

The test data in this section compares throughput performance with 1 VM using DPDK with and without hyperthreading (1 flow per port and 1k flows per port using 1 or 2 physical cores). See Figure 8-12 and Figure 8-13.

Table 8-15 PHY-VM-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT)

Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	4	1, 1k	1, 2	0, 2, 4

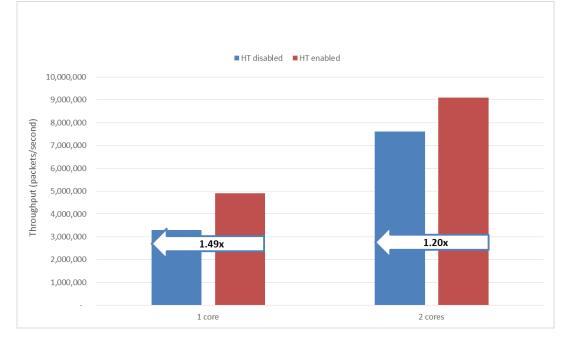


Figure 8-12 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4 flows, 1 VM)



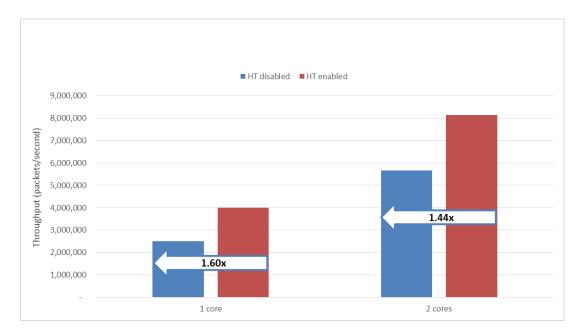
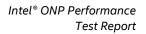


Figure 8-13 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows, 1 VM)

Table 8-16 shows the measured performance data for 2 physical cores with hyper-threading off and on.

		L3 Forwarding -			
		64B throughput		Average	
	Hyper-threading	Mbps	Packets/sec	% Line Rate	Latency (us)
1 flow per port	Off	5,118	7,616,028	13	74
	On	6,123	9,112,250	15	140
1k flows per port	Off	3,803	5,659,408	10	113
	On	5,466	8,133,937	14	166
Affinity Details		./ovs-vsctl set O ./ovs-vsctl set O	D thread based OvS and pen_vSwitch . other_cor pen_vSwitch . other_cor 8 -n 4burst=32 -it	nfig:pmd-cpu-ma nfig:pmd-cpu-ma	sk=C (HT off) sk=COC (HT on)

Table 8-16 PHY-VM-PHY test results for OvS DPDK (2 physical cores)





8.4 Two VM Throughput (PHY-VM-VM-PHY)

Figure 8-14 shows the VM-VM test setup with 2 x 10 GbE ports (maximum 20 Gbps aggregate throughput) with packets being forwarded from the first VM to the second VM (and in the opposite direction).

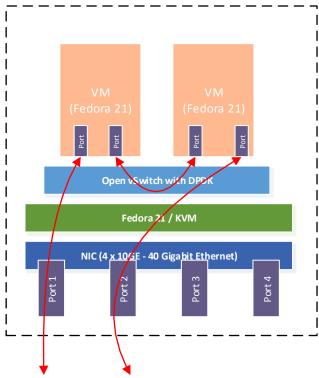


Figure 8-14 Two-VM Throughput performance test setup

Note: There are 3 switching operations taking place while packets are being routed through the system.

This test compares two VM throughput with DPDK using 1 flow per port in each direction (total 2 flows).

Configuration variables	Ports	Flows per Port in each Direction	Physical Cores	Hyper-threaded cores
OvS with DPDK	2	1	1, 2	0, 2, 4

Table 8-17 PHY-VM-VM-PHY test configuration variables for OvS with DPDK

Figure 8-15 shows packet throughput with 2 flows (one flow per port in each direction) using 1 physical core, 1 hyper-threaded core, 2 physical cores, 2 hyper-threaded cores.



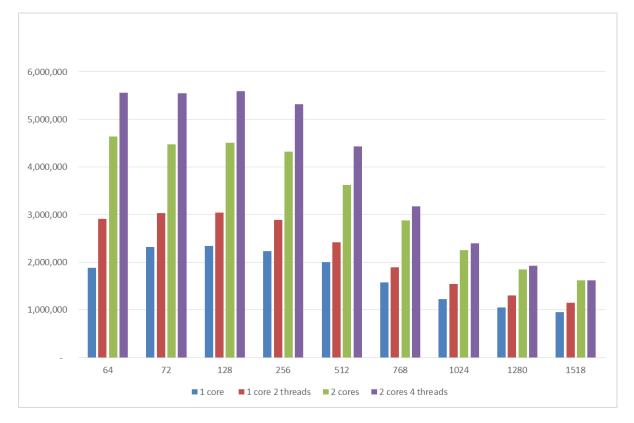


Figure 8-15 Two-VM Throughput (PHY-VM-VM-PHY) with 2 ports and 2 Flows Table 8-18 shows throughput and latency data for each packet size for 4 hyper-threaded cores.

L3 Forwarding —	L3 Forwarding — Bidirectional Throughput with Zero Packet Loss										
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)					
64	3,874	5,764,625	19	159	12	252					
72	4,067	5,526,069	20	120	12	770					
128	6,600	5,574,490	33	143	12	329					
256	12,111	5,485,009	61	258	28	690					
512	20,000	4,699,237	100	196	27	274					
768	20,000	3,172,586	100	88	25	101					
1024	20,000	2,394,633	100	100	20	135					
1280	20,000	1,923,074	100	97	15	115					
1518	20,000	1,625,486	100	99	16	115					
Affinity Details	y Details y Details 4PMD thread based OvS and 0.0% Loss resolution ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C0C ./testpmd -c 0x3 -n 4burst=32 -itxd=2048rxd=2048txqflags=0xf00 4P FVL Card										



8.5 VXLAN Performance (PHY-OVS-VM-OVS-PHY)

This test case investigates performance of VXLAN (https://tools.ietf.org/html/rfc7348) using native Open vSwitch* and Open vSwitch* with DPDK. The performance data provides a baseline for scenarios using VXLAN Tunnel End Points (VTEPs) in the vSwitch and establishes a test methodology for future comparisons. The test data here cannot be compared directly with other data in this document because the test setups are not equivalent. Future tests will include realistic use-case scenarios where traffic passes through VMs. The methodology described here attempts to emulate the scenario in Figure 8-16. An important difference, however, is that traffic does not pass through a VM (described below).

Figure 8-16 shows a VXLAN scenario using 2 x 10GbE ports (maximum 20 Gbps aggregate throughput using two flows). VXLAN decapsulation and encapsulation processing occurs in the vSwitch VTEP.

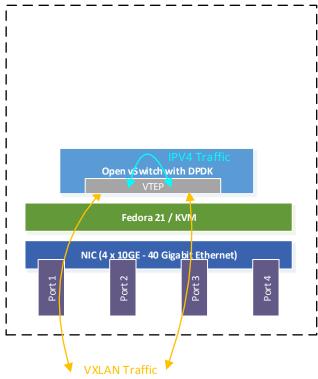


Figure 8-16 VXLAN Scenario with 2 Physical Ports and VTEP in the vSwitch

8.5.1 VXLAN Test Methodology

In this test methodology, 2 hosts are used with VTEPs in each host doing both encapsulation and decapsulation. Figure 8-17 shows the test setup with packet flows between each host using the VXLAN tunnel and between each host and the lxia traffic generator.

Each host machine requires 2 x 10GbE network ports:

- 1. Port 1 (eth0) is used for the VXLAN tunnel connection between the host machines.
- 2. Port 2 (eth1) is used for IPv4 traffic to and from the Ixia traffic generator.

VXLAN test setup details are provided in 14.0 VXLAN Test Setup.



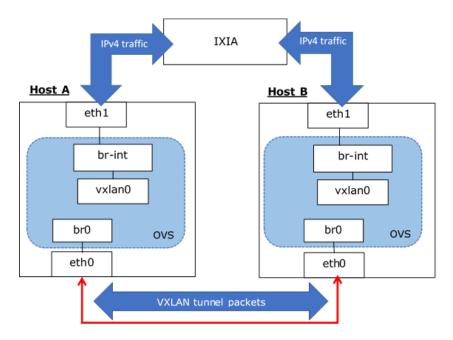


Figure 8-17 Test Setup Showing Packet Flows between Hosts and Ixia Traffic Generator

In this setup, 2 identical hosts are used (if hosts are not identical bottlenecks can impact test measurements). The 2 hosts are connected using 10GbE ports to create a VXLAN tunnel. The following steps show the flow of packets between Host A and Host B:

- 1. Ixia generates IPv4 packets.
- 2. OvS in Host A receives the IPv4 packets through eth1.
- 3. VTEP configured at vxlan0 in Host A encapsulates the IPv4 packets.
- 4. OvS in Host A forwards the VXLAN packets to Host B via the VXLAN tunnel.
- 5. In Host B, OvS receives the packets at br0 and forwards the VXLAN packets to the VTEP configured at vxlan0.
- 6. The VXLAN packets are decapsulated into IPv4 packets and OvS sends the packets to the Ixia via eth1.
- 7. Step 1 6 are repeated for IPv4 traffic from IXIA to Host B and Host B forwards the encapsulated VXLAN packets to Host A, which would be decapsulated and sent back to IXIA.

If the flow is unidirectional, Host A would encapsulate the IPv4 packet and Host B decapsulate the VXLAN packet. This test methodology uses bidirectional flows in order to measure both encapsulation and decapsulation performance (which occurs in each host).

8.5.2 Native OvS and OvS with DPDK

VXLAN tests attempt to achieve system throughput of 20 Gbps using 2 physical ports and 1 flow per port in each direction (see Table 8-19). Performance data shows comparison between:

- Native OvS and OvS with DPDK
- OvS with DPDK when using 1 and 2 physical cores.

Figure 8-18 shows VXLAN performance comparing native OvS and OvS with DPDK for 1 core and 2 cores configurations for all packet sizes. Throughput and latency data for 2 cores are provided in



Table 8-20.

Table 8-19 VXLAN test configuration variables for native OvS and OvS with DPDK

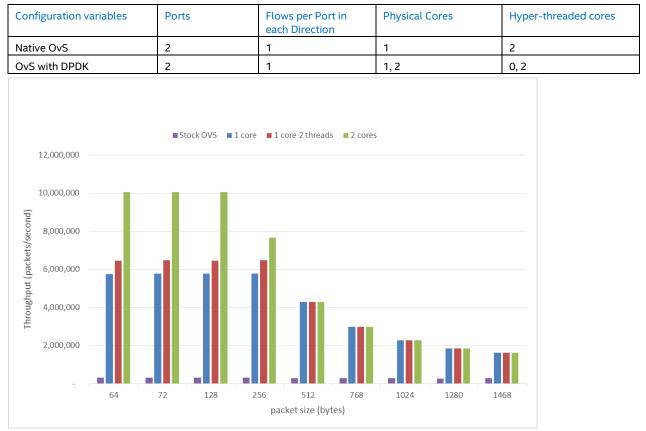


Figure 8-18 VXLAN Performance for 64-byte packets comparing native OvS and OvS with DPDK



L3 Forwarding —	L3 Forwarding — Bidirectional Throughput with Zero Packet Loss										
Packet Size	Mbps	Packets/sec	% Line Rate	Average Latency (us)	Minimum Latency (us)	Maximum Latency (us)					
64	6,755	10,051,947	34	24	13	112					
72	7,393	10,044,808	37	24	13	104					
128	11,898	10,049,224	59	25	13	186					
256	16,926	7,665,588	85	18	14	54					
512	18,279	4,294,906	91	16	12	67					
768	18,801	2,982,421	94	15	13	80					
1024	19,072	2,283,510	95	14	13	75					
1280	19,246	1,850,568	96	14	12	77					
1518	19,343	1,624,881	97	14	12	69					
Affinity Details	2PMD thread based OvS and 0% Loss resolution y Details ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C 2 Ports from FVL Quad Port Card										

Table 8-20 VXLAN Packet Throughput with 2 cores (hyper-threading off)



9.0 Industry Benchmarks

9.1 ETSI NFV

The European Telecommunications Standards Institute (ETSI) NFV (Phase II) is developing test methodologies and test specifications relevant to performance testing. Certain draft specification documents are available publically here: https://docbox.etsi.org/ISG/NFV/Open/Drafts/. This includes a "NFV Pre-Deployment Validation" specification with the following:

- Test methods for pre-deployment validation:
 - Validating physical DUTs and systems-under-test:
 - Data plane validation
 - Control plane validation
 - Management plane validation
 - Impact of virtualization on test methods
 - Considerations on choice of virtualized versus hardware based test appliances
- Pre-deployment validation of NFV infrastructure
- Pre-deployment validation of VNFs:
 - VNF life-cycle testing:
 - VNF instantiation testing
 - VNF termination
 - VNF data plane benchmarking
- Pre-deployment validation of network services
- Reliability & resiliency requirements
- Security considerations.

9.2 IETF

The Benchmark Working Group (BMWG) is one of the longest-running working groups in IETF. This group was rechartered in 2014 to include benchmarking for virtualized network functions (VNFs) and their infrastructure.

An active Internet draft, "Considerations for Benchmarking Virtual Network Functions and Their Infrastructure," is available here: https://tools.ietf.org/html/draft-ietf-bmwg-virtual-net-00. Many RFCs referenced originated in the BMWG, including foundational RFC 1242 and RFC 2544:

- RFC 1242 Benchmarking Terminology for Network Interconnection Devices
- RFC 2544 Benchmarking Methodology for Network Interconnect Devices
- RFC 2285 Benchmarking Terminology for LAN Switching Devices
- RFC 2889 Benchmarking Methodology for LAN Switching Devices
- RFC 3918 Methodology for IP Multicast Benchmarking
- RFC 4737 Packet Reordering Metrics
- RFC 5481 Packet Delay Variation Applicability Statement
- RFC 6201 Device Reset Characterization



9.3 Open Platform for NFV (OPNFV)

OPNFV (https://wiki.opnfv.org) is a carrier-grade, integrated, open-source platform to accelerate the introduction of new NFV products and services. As an open-source project, OPNFV aims to bring together the work of standards bodies, open-source communities, and commercial suppliers to deliver a de facto open-source NFV platform for the industry. By integrating components from upstream projects, the community can conduct performance and use case-based testing to ensure the platform's suitability for NFV use cases.

Figure 9-1 illustrates the wide variety of performance test projects currently in OPNFV (this is a snapshot and evolving rapidly) and includes:

- infrastructure KPI verification
- platform performance benchmarking
- vSwitch performance
- system bottlenecks
- storage performance benchmarking
- controller performance.

For more information on test projects for OPNFV upcoming release (Brahmaputra) refer to OPNFV Wiki: https://wiki.opnfv.org/brahmaputra_testing_page.

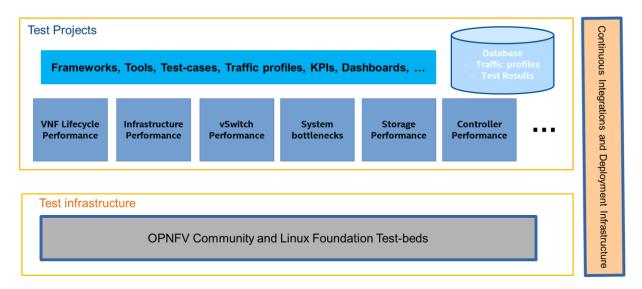


Figure 9-1 Snapshot of OPNFV Test projects and Infrastructure

The project "Characterize vSwitch performance for telco NFV Use Case" is highly relevant to this test report https://wiki.opnfv.org/characterize_vswitch_performance_for_telco_nfv_use_cases. A released Internet draft for benchmarking vSwitches in OPNFV is available https://tools.ietf.org/html/draft-vsperf-bmwg-vswitch-opnfv-00. The Internet draft describes the progress of the OPNFV project on vSwitch performance with additional considerations when vSwitches are implemented in general-purpose hardware. The project intends to build on the current and completed work of the Benchmarking Working Group in IETF.



10.0 Performance Tuning

10.1 Tuning Methods

There are a few important tuning methods that can improve throughput performance for PHY-PHY, PHY-VM, and VM-VM test cases:

- CPU core isolation for OvS-DPDK
- HugePage size 1 GB
- CPU core affinity for ovs-vswitchd and OvS PMD threads
- CPU core affinity for the VM (qemu-kvm)

This section provides some fundamental optimization and tunings for the OvS with DPDK setup. Refer to https://github.com/openvswitch/ovs/blob/master/INSTALL.DPDK.md#performance-tuning for more information on tuning-related optimization.

10.2 CPU Core Isolation for OvS-DPDK

While the threads used by OvS are pinned to logical cores on the system, the Linux scheduler can also run other tasks on those cores. To help prevent additional workloads from running on them, the isolcpus Linux* kernel parameter can be used to isolate the cores from the general Linux scheduler. Add the isolcpus Linux* parameter in the Linux boot kernel of the host machine. For example, if the OvS vswitchd and qemu-kvm process are to run on logical cores 2, 4, and 6, the following should be added to the kernel parameter list:

isolcpus=2,4,6

10.3 HugePage Size 1 GB

HugePage support is required for the large-memory pool allocation used for packet buffers. By using HugePage allocations, performance is increased because fewer pages are needed, and therefore less translation lookaside buffers (TLBs, high-speed translation caches). This reduces the time it takes to translate a virtual page address to a physical page address. Without HugePages, high TLB miss rates would occur with the standard 4K page size, slowing performance.

The allocation of HugePages should be done at boot time or as soon as possible after system boot to prevent memory from being fragmented in physical memory. To reserve HugePages at boot time, a parameter is passed to the Linux* kernel on the kernel command line. For example, to reserve 16G of HugePage memory in the form of 16 1G pages, the following options should be passed to the kernel:

default_hugepagesz=1G hugepagesz=1G hugepages=16

Note: For 1G HugePages, it is not possible to reserve the HugePage memory after the system has booted.

After the machine is up and running, mount the huge table file system:

mount -t hugetlbfs -o pagesize=1G none /dev/hugepages



10.4 CPU Core Affinity for ovs-vswitchd and OvS PMD Threads

With PMD multi-threading support, OvS creates one PMD thread for each NUMA node as default. The PMD thread handles the I/O of all DPDK interfaces on the same NUMA node. The following command can be used to configure the multi-threading behavior:

ovs-vsctl set Open vSwitch . other config:pmd-cpu-mask=<hex string>

The above command asks for a CPU mask for setting the affinity of PMD threads. A set bit in the mask means a PMD thread is created and pinned to the corresponding CPU core. Ideally, for maximum throughput, the PMD thread should not be scheduled out, which temporarily halts its execution. Therefore, with the CPU core isolation being on the host machine during boot time, the CPU-isolated cores will be used to set the affinity of the PMD threads. For example, to configure PMD threads on core 2 and 3 using 'pmd-cpu-mask':

```
# ovs-vsctl set Open vSwitch . other config:pmd-cpu-mask=C
```

Check that the OvS PMD thread is set to the correct CPU1 and ovs-vswitchd threads are set to CPU2 and CPU3 using this command:

```
# top -p `pidof ovs-vswitchd` -H -d1
```

```
top - 17:31:09 up 2:46, 3 users, load average: 0.40, 0.11, 0.08
Threads: 18 total, 1 running, 17 sleeping, 0 stopped, 0 zombie
%Cpu(s): 8.4 us, 0.0 sy, 0.0 ni, 91.6 id, 0.0 wa, 0.0 hi, 0.0 si, 0.0 st
KiB Mem: 32748524 total, 11233304 free, 21292684 used, 222536 buff/cache
KiB Swap: 4194300 total, 4194300 free,
                                          0 used. 11237940 avail Mem
               PR NI
                                     SHR S %CPU %MEM
  PID USER
                       VIRT
                               RES
                                                        TIME+ COMMAND
               20 0 3836184 8896 5140 R 99.0 0.0 0:28.55 pmd28
 2150 root
 2152 root
               20 0 3836184 8896 5140 R 99.0 0.0
                                                      0:28.55 pmd29
 2041 root
              20 0 3836184
                              8896 5140 S 0.0 0.0
                                                      0:13.47 ovs-
vswitchd
 2042 root
               20 0 3836184
                              8896
                                     5140 S 0.0 0.0
                                                      0:00.00 ovs-
vswitchd
```

Note: The PMD threads on a NUMA node are created only if there is at least one DPDK interface from the NUMA node that has been added to OvS. To understand where most of the time is spent and whether the caches are effective, these commands can be used:

```
# ovs-appctl dpif-netdev/pmd-stats-clear #To reset statistics
# ovs-appctl dpif-netdev/pmd-stats-show
```

10.5 CPU Core Affinity for the Virtual Machine (qemukvm)

When configuring a PHY-VM test environment, it is important to set the CPU core affinity for the virtual machine (VM). Depending on the number of cores being assigned to the VM, the CPU core affinity should be set according to the QEMU threads. For example, to configure a VM with 4 cores, start the VM on CPU 4-6 (0x70):

taskset 70 qemu-system-x86_64 -m 4096 -smp 4 -cpu host -hda /root/vmimages/vm-fc21.img -boot c -enable-kvm -pidfile /tmp/vm1.pid -monitor





unix:/tmp/vmlmonitor,server,nowait -name 'FC21-VM1' -net none -no-reboot object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on numa node,memdev=mem -mem-prealloc -net none \
-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \
-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-netpci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off \
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-netpci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-netpci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off
--nographic -vnc :14

Once the VM is running, there will be multiple QEMU threads that are spawned running on the host. Check the main QEMU thread process ID (PID) to track the spawned threads:

```
# ps -e |grep qemu
2511 pts/3 22:27:53 qemu-system-x86
```

Use the top command to provide a list of the main and child process QEMU threads. The main QEMU thread PID 2511 is always active with utilization close to 100% of CPU:

top -p 2511 -H -d1

top - 17:06:42 up 1 day, 3:03, 3 users, load average: 2.00, 2.01, 2.02
Threads: 6 total, 1 running, 5 sleeping, 0 stopped, 0 zombie
%Cpu(s): 16.7 us, 0.0 sy, 0.0 ni, 83.3 id, 0.0 wa, 0.0 hi, 0.0 si, 0.0 st
KiB Mem: 32748524 total, 10566116 free, 21308332 used, 874076 buff/cache
KiB Swap: 4194300 total, 4194300 free, 0 used. 11189840 avail Mem

PID USER 2520 root system-x86	PR 20	NI O	VIRT 4704308	RES 24944			%CPU 99.9	%MEM 0.1	TIME+ 1339:34	COMMAND qemu-
2511 root system-x86	20	0	4704308	24944	6848	S	0.0	0.1	0:11.69	qemu-
2518 root system-x86	20	0	4704308	24944	6848	S	0.0	0.1	2:11.77	qemu-
2519 root system-x86	20	0	4704308	24944	6848	S	0.0	0.1	0:11.13	qemu-
2521 root system-x86	20	0	4704308	24944	6848	S	0.0	0.1	7:57.56	qemu-
2523 root system-x86	20	0	4704308	24944	6848	S	0.0	0.1	0:03.76	qemu-

Then, use htop to check the % CPU usage in runtime for each QEMU child thread and determine the active QEMU threads:

htop -p 2520,2511,2518,2519,2521,2523



Output:

1 [0.09	1		ſ				1		ſ					0.0%		ſ			0.0%]
2 []]						j i					100.04	j i		i					0.0%		i.			0.5%
								[0.0%					
Mem[40,	36 t								
Swp															verag		.00 2.0	1 2.02						
														ptime	: 1 d	ay,	03:10:4	5						
PID	USER	PRI	NI VI	IRT	RES	SHR	S	CPU%	MEM%	TIME+	Command													
2511	root	20	0 459	94M 2	4944	6848	S 1	100.	0.1	22h37:46	gemu-system-x80	5_64	-m	4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi
2520	root)4M 2	4944	6848		100.	0.1	22h27:08	gemu-system-x8	64		4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi
	root)4M 2	4944	6848		0.0	0.1	2:11.86	gemu-system-x8	5 64		4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi
	root			94M 2	4944	<mark>6</mark> 848		0.0	0.1	0:11.23	gemu-system-x8	5 64		4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi
2521	root			94M 2	4944	6848		0.0	0.1	7:57.68	gemu-system-x8	5 64		4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi
2523	root			94M 2	4944	6848		0.0	0.1	0:03.76	gemu-system-x8	5 64		4096	-smp	4 -c	pu host	-hda	/root/vm-i	mages,	/fc21-vm.qcow2	-boot	c -enabl	e-kvm -pi

Figure 10-1 Output from htop showing high CPU usage for active QEMU threads

From the htop output screen, you can view two active QEMU threads that have a high CPU usage. In this example, PID 2511 and PID 2520 (screen output) are using 100% CPU. We have to set these two active threads to specific CPU logical cores. We are going to set PID 2511 to CPU4 (0x10), and PID 2520 to CPU 5 (0x20). The other 4 threads (PID: 2518, 2519, 2521, 2523) are going to be set to CPU6 (0x40).

It is important to assign each active (100% CPU) QEMU thread to separate CPU cores to sustain good optimal throughput performance. If the active QEMU threads are not core-affinitized, the overall throughput performance is impacted.

10.6 Troubleshooting Tips for OvS

In the OvS controller, there are a few management tools in ovs-vswitchd that are useful to monitor the status of ports and OpenFlow activities:

- ovs-vsctl manages the switch through interaction with ovsdb-server.
- ovs-ofctl is a management utility for OpenFlow.
- ovs-appctl is a utility for managing logging levels.

After creating and configuring the ports, the ovs-vsctl command tool is useful to check the overall view of the bridges and ports created in the ovsdb-server database:

```
# ovs-vsctl show
7bdd3285-c5db-4944-b963-3ecedf661a41
Bridge "br0"
Port "br0"
Interface "br0"
type: internal
Port "dpdk0"
Interface "dpdk0"
type: dpdk
Port "dpdk1"
Interface "dpdk1"
type: dpdk
```

The ovs-ofctl command tool is useful to check the OpenFlow flow configuration and port statistics. To check port information on a particular bridge, such as the port's media access control (MAC) address and number, ovs-ofctl show <bridge-name> or ovs-ofctl dump-ports-desc <bridge-name> provides the following information on all ports:



```
OFPT FEATURES REPLY (xid=0x2): dpid:000001b21a272e4
n tables:254, n buffers:256
capabilities: FLOW STATS TABLE STATS PORT STATS QUEUE STATS ARP MATCH IP
actions: output enqueue set vlan vid set vlan_pcp strip_vlan mod_dl_src
mod dl dst mod nw src mod nw dst mod nw tos mod tp src mod tp dst
1(dpdk0): addr:00:1b:21:a2:72:e4
    config:
                 0
     state:
                 LINK DOWN
    current:
                AUTO NEG
     speed: 0 Mbps now, 0 Mbps max
 2(vxlan0): addr:c2:7a:99:d6:01:e2
    config:
                 Ω
                 0
     state:
     speed: 0 Mbps now, 0 Mbps max
 LOCAL (br-int): addr:00:1b:21:a2:72:e4
                 Ω
    config:
     state:
                 0
               10MB-FD COPPER
     current:
     speed: 10 Mbps now, 0 Mbps max
OFPT GET CONFIG REPLY (xid=0x4): frags=normal miss send len=0
```

When the test is running, you can monitor packets sending and receiving at the ports configured in OvS by checking the flow and port statistics. For example, if you want to check if the packets are being received and sent in a flow, ovs-ofctl dump-flows
dump-flows
dump-flows and receiving exist and are being used with n_packets equal to non-zero.

```
# /root/ovs/utilities/ovs-ofctl dump-flows br-int
NXST_FLOW reply (xid=0x4): cookie=0x0, duration=177593.242s, table=0,
n_packets=1300667542, n_bytes=78040052520, idle_age=65534, hard_age=65534,
ip,in_port=2 actions=output:1
```

The ovs-ofctl dump-ports
bridge-name> command prints port statistics for RX/TX packets, packets that are dropped, and packet errors (if they occur). In this example, there are packet errors in port 1. One of the reasons may be that the packet rate being received at port 1 is too high and beyond the port's capacity. The packet sending rate to the port, therefore, needs to be reduced to fix the packet error. If there is a packet drop in the OvS, check the CPU core affinitization for the QEMU threads for the PHY-VM test case, and if the HugePage size is set correctly, and the ovs-vswitchd and OvS PMD threads are running on isolated cores.



To check the Address Resolution Protocol (ARP) cache content, ovs-appctltnl/arp/show prints the learned MAC address and IP address.

<pre># /root/ovs/ut;</pre>	ilities/ovs-appctl	tnl/arp/show
IP	MAC	Bridge
2.2.2.1	00:1b:21:a2:72:e5	br0
2.2.2.2	00:1b:21:a2:72:e6	br0



11.0 OvS Test Setup

11.1 Configure the Host Machine

```
1. Stop and disable the interruption request (IRQ) balance:
    # killall irgbalance
    # systemctl stop irgbalance.service
   # systemctl disable irqbalance.service
2. Stop and disable the Firewall and iptables:
    # systemctl stop firewalld.service
    # systemctl disable firewalld.service
   # systemctl stop iptables.service
3. Disable Security-enhanced Linux (SELinux):
   [root@localhost ~]# vi /etc/selinux/config
   SELINUX=disabled
4. Disable address space layout randomization:
   # echo "# Disable Address Space Layout Randomization (ASLR)" > /etc/ \
              sysctl.d/ aslr.conf
   # echo "kernel.randomize va space=0" >> /etc/sysctl.d/aslr.conf
5. Disable IPv4 forwarding:
   # echo "# Enable IPv4 Forwarding" > /etc/sysctl.d/ip forward.conf
   # echo "net.ipv4.ip forward=0" >> /etc/sysctl.d/ip forward.conf
   # systemctl restart systemd-sysctl.service
   # cat /proc/sys/kernel/randomize va space
   0
   # cat /proc/sys/net/ipv4/ip forward
   0
6. Remove the following modules:
```

```
# rmmod ipmi_msghandler
```

```
# rmmod ipmi_si
```

```
# rmmod ipmi_devintf
```

11.2 Set the Kernel Boot Parameters

- 1. With hyper-threading enabled, add the following to the kernel boot parameters /etc/default/grub: GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=16 hugepagesz=20 hugepagesz=2048 intel_iommu=off isolcpus=1-7,9-15 rhgb quiet"
- 2. With hyper-threading disabled, add the following to the kernel boot parameters /etc/default/grub:



GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=1G hugepages=16 hugepagesz=2M hugepages=2048 intel iommu=off isolcpus=1-7 rhgb quiet"

- 3. Save the file and update the GRUB config file: # grub2-mkconfig -o /boot/grub2/grub.cfg
- 4. Reboot the host machine and check to make sure 1GB and 2MB HugePage sizes are created. You should see 16 1GB HugePages and 2048 2MB HugePages:

```
# ls /sys/devices/system/node/node0/hugepages/hugepages-*
hugepages-1048576kB/ hugepages-2048kB/
# cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
16
# cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
2048
```

11.3 Compile DPDK 2.0

- 1. Go to the DPDK-2.0.0 directory and run the following: # make install T=x86_64-ivshmem-linuxapp-gcc # cd x86_64-ivshmem-linuxapp-gcc
- 2. Edit the config file (vim .config) and set the configuration options: CONFIG_RTE_BUILD_COMBINE_LIBS=y CONFIG_RTE_LIBRTE_VHOST=y CONFIG_RTE_LIBRTE_VHOST_USER=y
- 3. Save the config file and run make:
 # EXTRA_CFLAGS="-g -Ofast"
 # make

11.4 Install OvS

1. Go to the OvS directory and run:

```
# ./boot.sh
```

```
# make 'CFLAGS=-g -Ofast'
```

11.5 Prepare to Start OvS

- 1. Mount the 1GB HugePage and 2MB HugePage:
 - # mkdir -p /mnt/huge
 - # mkdir -p /mnt/huge_2mb
 - # mount -t hugetlbfs nodev /mnt/huge
 - # mount -t hugetlbfs nodev /mnt/huge_2mb -o pagesize=2MB
- 2. Check that HugePages are mounted:



mount

```
nodev on /mnt/huge type hugetlbfs (rw,relatime)
```

nodev on /mnt/huge 2mb type hugetlbfs (rw,relatime,pagesize=2MB)

```
3. Remove the following Linux modules and load the modules for OvS:
```

- # rmmod ixgbe
- # rmmod igb_uio
- # rmmod cuse
- # rmmod fuse
- # rmmod openvswitch
- # rmmod uio
- # rmmod eventfd_link
- # rmmod ioeventfd
- # rm -rf /dev/vhost-net
- # modprobe uio
- # insmod \$DPDK_BUILD/kmod/igb_uio.ko

4. Check the PCI ID for the 10GbE NIC ports:

```
# lspci | grep Ethernet
01:00.0 Ethernet controller: Intel Corporation Ethernet Controller X710 for
10GbE SFP+ (rev 01)
01:00.1 Ethernet controller: Intel Corporation Ethernet Controller X710 for
10GbE SFP+ (rev 01)
01:00.2 Ethernet controller: Intel Corporation Ethernet Controller X710 for
10GbE SFP+ (rev 01)
01:00.3 Ethernet controller: Intel Corporation Ethernet Controller X710 for
10GbE SFP+ (rev 01)
```

11.6 Bind 10 GbE NIC Ports to the igb_uio Driver

1. To create a 4-port configuration:

```
# python $DPDK DIR/tools/dpdk nic bind.py --bind=igb uio 01:00.0
# python $DPDK DIR/tools/dpdk nic bind.py --bind=igb uio 01:00.1
# python $DPDK DIR/tools/dpdk nic bind.py --bind=igb uio 01:00.2
# python $DPDK DIR/tools/dpdk nic bind.py --bind=igb uio 01:00.3
# python $DPDK DIR/tools/dpdk nic bind.py -status
Network devices using the DPDK-compatible driver:
_____
0000:01:00.0 'Ethernet Controller X710 for 10GbE SFP+' drv=igb uio unused=i40e
0000:01:00.1 'Ethernet Controller X710 for 10GbE SFP+' drv=igb uio unused=i40e
0000:01:00.2 'Ethernet Controller X710 for 10GbE SFP+' drv=iqb uio unused=i40e
0000:01:00.3 'Ethernet Controller X710 for 10GbE SFP+' drv=igb uio unused=i40e
Network devices using the kernel driver:
_____
0000:00:19.0 'Ethernet Connection I217-LM' if=enp0s25 drv=e1000e unused=iqb uio
*Active*
0000:05:00.0 'I210 Gigabit Network Connection' if=enp5s0 drv=igb unused=igb uio
Other network devices:
_____
```



<none>

11.7 Remove and Terminate Previous-Run OvS and Prepare

```
# pkill -9 ovs
```

- # rm -rf /usr/local/var/run/openvswitch
- # rm -rf /usr/local/etc/openvswitch/
- # rm -f /tmp/conf.db
- # mkdir -p /usr/local/etc/openvswitch
- # mkdir -p /usr/local/var/run/openvswitch

11.8 Initialize the New OvS Database

- 1. Initialize the new OvS database:
 - # export OVS_DIR=/root/OVS/ovs
 - # cd \$OVS_DIR
- 2. Start the database server:

3. Initialize the OvS database:
 # ./utilities/ovs-vsctl --no-wait init

11.9 Start OvS-vSwitchd

1. Start OvS with DPDK portion using 2GB on CPU2 (0x2):
./vswitchd/ovs-vswitchd --dpdk -c 0x2 -n 4 --socket-mem 2048 \

```
-- unix:/usr/local/var/run/openvswitch/db.sock --pidfile
```

11.10 Tune OvS-vswitchd

You can check the thread siblings list (when hyper-threading is enabled) with the following:

cat /sys/devices/system/cpu/cpuN/topology/thread_siblings_list

Based on the core thread siblings, you can set/check the PMD mask so that the multiple logical cores are on the same physical core.



1 PMD Configuration

1. Set the default OvS PMD thread usage to CPU2 (0x4):

```
# ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4
```

```
# ./ovs-vsctl set Open vSwitch . other config:max-idle=30000
```

2 PMD Configuration

- 1. For 1 physical core, 2 logical cores (2 PMDs) on a system with HT enabled, check the thread siblings:
 # cat /sys/devices/system/cpu/cpu1/topology/thread_siblings_list
 2,9
- 2. Then set the pmd-cpu-mask to CPU2 and CPU10 (0x404):

```
# ./ovs-vsctl set Open vSwitch . other config:pmd-cpu-mask=404
```

```
# ./ovs-vsctl set Open vSwitch . other config:max-idle=30000
```

3. For **2** physical cores and 2 logical cores (2 PMDs) on system HT disabled, set the default OvS PMD thread usage to CPU2 and CPU3 (0xC):

```
# ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C
```

```
# ./ovs-vsctl set Open vSwitch . other config:max-idle=30000
```

4 PMD Configuration

1. For **2** physical cores, 2 logical cores (4PMDs) on system with HT enabled, check the thread siblings:

```
# cat /sys/devices/system/cpu/cpu2/topology/thread_siblings_list
2,9
# cat /sys/devices/system/cpu/cpu3/topology/thread_siblings_list
```

```
3,10
```

2. Then set the pmd-cpu-mask to CPU2, CPU3, CPU10, and CPU11 (0xCOC).

```
# ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask= COC
```

```
# ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
```

3. For **4** physical cores (4 PMDs) on system HT disabled, set the default OvS PMD thread usage and set the default OvS PMD thread usage to CPU2, CPU3, CPU4, and CPU5 (0x3C):

```
# ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3C
```

```
# ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
```

11.11 Create the Ports

4-Port Configuration

```
# cd /root/ovs
# ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
# ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk1 -- set Interface dpdk1 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk2 -- set Interface dpdk2 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk3 -- set Interface dpdk3 type=dpdk
# ./utilities/ovs-vsctl show
```



11.12 Add the Port Flows

1. Clear current flows:

- # export OVS DIR=/root/ovs
- # cd \$OVS_DIR
- # ./utilities/ovs-ofctl del-flows br0

2. Add flow:

- # ./utilities/ovs-ofctl add-flow br0 \
 - in_port=1,dl_type=0x800,idle_timeout=0,action=output:2
- # ./utilities/ovs-ofctl add-flow br0 \
 - in_port=2,dl_type=0x800,idle_timeout=0,action=output:1
- # ./utilities/ovs-ofctl add-flow br0 \
- in_port=3,dl_type=0x800,idle_timeout=0,action=output:4
- # ./utilities/ovs-ofctl add-flow br0 \
 - in_port=4,dl_type=0x800,idle_timeout=0,action=output:3
- # ./utilities/ovs-ofctl dump-flows br0



12.0 PHY-VM-PHY Test Setup

Follow the steps on the PHY-to-PHY test setup up to the section 11.10 Tune OvS-vswitchd, and set up 1 core with 1 PMD thread configuration (without hyper-threading) for the PHY-to-VM tests. Follow the instructions in this section to continue on the PHY-to-VM.

12.1 Create the Ports

4-Port configuration

12.2 Add the Port Flows



```
# ./utilities/ovs-ofctl dump-flows br0
```

12.3 Power on the VM

```
1. Start the VM on CPU 4, CPU 5, and CPU 6 (0x70) with the following configuration:
```

taskset 70 qemu-system-x86_64 -m 4096 -smp 3 -cpu host -hda /root/vmimages/vm-fc21.img -boot c -enable-kvm -pidfile /tmp/vm1.pid -monitor unix:/tmp/vm1monitor,server,nowait -name 'FC21-VM1' -net none -no-reboot object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on numa node,memdev=mem -mem-prealloc \

-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \

-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-netpci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso 6=off,guest_ecn=off,mrg_rxbuf=off \

-chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \

-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-netpci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso 6=off,guest_ecn=off,mrg_rxbuf=off \

-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \

-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-netpci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso 6=off,guest_ecn=off,mrg_rxbuf=off \

-chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-netpci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off \

--nographic -vnc :14

12.4 Set the VM Kernel Boot Parameters

1. Add the following to the kernel boot parameters /etc/default/grub:

GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024 isolcpus=1,2 rhgb quiet"

- 2. Save the file and update the GRUB config file: # grub2-mkconfig -o /boot/grub2/grub.cfg
- 3. Reboot the VM and check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePage and 1024 2MB HugePages:

ls /sys/devices/system/node/node0/hugepages/hugepages-*

hugepages-1048576kB/ hugepages-2048kB/

cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr hugepages



1
cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
1024

12.5 Set up the VM HugePages

1. Mount the HugePage for 1 GB and 2 MB:

mount -t hugetlbfs hugetlbfs /mnt/huge

mount -t hugetlbfs none /mnt/huge 2mb -o pagesize=2MB

12.6 Set up DPDK 2.0

- 1. Download DPDK 2.0.0 and compile it: # make install T=x86_64-native-linuxapp-gcc
- 2. Edit the test-pmd apps input and output queue size to 2K for better throughput performance: # vi /root/dpdk-2.0.0/app/test-pmd/test-pmd.c

```
/*
* Configurable number of RX/TX ring descriptors.
*/
#define RTE_TEST_RX_DESC_DEFAULT 2048
#define RTE_TEST_TX_DESC_DEFAULT 2048
Save and build the test and ease;
```

3. Save and build the test-pmd app:

```
# export RTE_SDK=/root/dpdk-2.0.0
# export RTE_TARGET=x86_64-native-linuxapp-gcc
# make
```

12.7 Set up the vhost Network in the VM

1. Load the UIO kernel module in the VM:

```
# modprobe uio
```

```
# insmod /root/dpdk-2.0.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko
```

2. Check the PCI ID for the 10GbE NIC ports:

```
# lspci -nn
```

```
00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
[1af4:1000]
00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
[1af4:1000]
00:06.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
[1af4:1000]
```



```
00:07.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
  [laf4:1000]
3. Bind the user-side vhost network devices with the igb_uio driver:
   # /root/dpdk-2.0.0/tools/dpdk nic bind.py -b igb uio 00:04.0
   # /root/dpdk-2.0.0/tools/dpdk nic bind.py -b igb uio 00:05.0
   # /root/dpdk-2.0.0/tools/dpdk nic bind.py -b igb uio 00:06.0
   # /root/dpdk-2.0.0/tools/dpdk nic bind.py -b igb uio 00:07.0
   # /root/dpdk-2.0.0/tools/dpdk nic bind.py -status
   Network devices using DPDK-compatible driver
   ------
   0000:00:04.0 'Virtio network device' drv=igb uio unused=virtio pci
   0000:00:05.0 'Virtio network device' drv=igb uio unused=virtio pci
   0000:00:06.0 'Virtio network device' drv=iqb uio unused=virtio pci
   0000:00:07.0 'Virtio network device' drv=iqb uio unused=virtio pci
   Network devices using kernel driver
   _____
   <none>
```

12.8 Start the test-pmd Application in the VM

```
1. Run test-pmd app on vCPU1 and vCPU2 (0x6):
   # cd /root/dpdk-2.0.0/x86 64-native-linuxapp-gcc/build/app/test-pmd
   # ./testpmd -c 0x3 -n 4 -- --burst=32 -i --disable-hw-vlan --txd=2048 \
              --rxd=2048 --txqflaqs=0xf00
2. In the application, enter the fwd and mac retry commands:
   testpmd> set fwd mac retry
3. Set the mac_retry packet forwarding mode.
4. Start the PMD forwarding operation:
   testpmd> start
   mac retry packet forwarding - CRC stripping disabled - packets/burst=32
   nb forwarding cores=1 - nb forwarding ports=2
   RX queues=1 - RX desc=2048 - RX free threshold=32
   RX threshold registers: pthresh=8 hthresh=8 wthresh=0
   TX queues=1 - TX desc=2048 - TX free threshold=0
   TX threshold registers: pthresh=32 hthresh=0 wthresh=0
   TX RS bit threshold=0 - TXQ flags=0xf00
```

12.9 CPU Affinity Tuning

The tables below show the host's CPU core affinity settings for PHY-to-VM test configuration for 1 physical core (no hyper-threading). When the VM starts, there are multiple QEMU threads spawned. Refer to section 10.5 CPU Core Affinity for the Virtual Machine (qemu-kvm), to set the active QEMU threads to the correct core affinity.



Table 12-1 CPU Affinity Setting on the Host

Logical Core	Process
1	ovs-vswitchd
2	PMD0
4, 5, 6	QEMU

Table 12-2 QEMU Threads CPU Affinity

Logical Core	Process	CPU% (from htop)
4	QEMU (main thread)	100
5	QEMU	100
6	QEMU	0

Note: Two active threads (with 100% CPU) are set to 2 different logical cores.



13.0 VM-VM Test Setup

Refer to section 11.0 **OvS Test Setup** and follow up to the section 11.10 Tune OvS-vswitchd, to set up the host configurations, and then set up 1 core with 1 PMD thread configuration (without hyper-threading) for 2 VMs series tests. Follow the instructions below to continue on the VM-to-VM setup.

13.1 Create the Ports

13.2 Add the Port Flows

```
1. Clear current flows
   # export OVS DIR=/root/ovs
   # cd $OVS DIR
   # ./utilities/ovs-ofctl del-flows br0
2. Add Flow
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=1,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=2,dl type=0x800,idle timeout=0,action=output:6
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=3,dl type=0x800,idle timeout=0,action=output:1
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=4,dl type=0x800,idle timeout=0,action=output:5
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=6,dl type=0x800,idle timeout=0,action=output:2
   # ./utilities/ovs-ofctl add-flow br0 \
             in port=5,dl type=0x800,idle timeout=0,action=output:4
```



./utilities/ovs-ofctl dump-flows br0

13.3 Power on the VM

1. Start the first VM on CPU 3, CPU 4, and CPU 5 (0x38) with the following configuration:

taskset 38 qemu-system-x86_64 -m 4096 -smp 3 -cpu host -hda /root/vmimages/vm2-fc21.img -boot c -enable-kvm -pidfile /tmp/vm1.pid -monitor unix:/tmp/vm2monitor,server,nowait -name 'FC21-VM2' -net none -no-reboot object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on numa node,memdev=mem -mem-prealloc \

-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \

-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-netpci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso 6=off,guest_ecn=off,mrg_rxbuf=off \

-chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-netpci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off \

--nographic -vnc :14

13.3.1 VM Kernel Boot Parameters

- 1. Add the following to the kernel boot parameters /etc/default/grub in the VM: GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024 isolcpus=1,2 rhgb quiet"
- 2. Save the file and update the GRUB config file: # grub2-mkconfig -o /boot/grub2/grub.cfg
- Reboot the VM and then check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePages and 1024 2MB HugePages:

ls /sys/devices/system/node/node0/hugepages/hugepages-*
hugepages-1048576kB/ hugepages-2048kB/
#cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
1

#cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
1024

 Start the second VM by making a copy of the first VM. Start the second VM on CPU 5, CPU6, and CPU7 (0xE0) with the following command:

```
# taskset E0 qemu-system-x86_64 -m 4096 -smp 3 -cpu host -hda /root/vm-
images/vm2-fc21.img -boot c -enable-kvm -pidfile /tmp/vm2.pid -monitor
unix:/tmp/vm2monitor,server,nowait -name 'FC21-VM2' -net none -no-reboot -
object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on -
numa node,memdev=mem -mem-prealloc \
```

```
-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \
```

-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-netpci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso 6=off,guest_ecn=off,mrg_rxbuf=off \

-chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \



```
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-net-
pci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso
6=off,guest_ecn=off,mrg_rxbuf=off \
--nographic -vnc :15
```

13.4 Set up the VM HugePages

- 1. Mount the HugePage for 1GB and 2MB:
 - # mount -t hugetlbfs hugetlbfs /mnt/huge
 # mount -t hugetlbfs none /mnt/huge_2mb -o pagesize=2MB

13.5 Set up DPDK 2.0

```
    Download DPDK 2.0.0 and compile it:

# make install T=x86_64-native-linuxapp-gcc
    Edit the test-pmd app input and output queue size to 2K for better throughput performance:

# vi /root/dpdk-2.0.0/app/test-pmd/test-pmd.c
    /*
        * Configurable number of RX/TX ring descriptors.
        */
        #define RTE_TEST_RX_DESC_DEFAULT 2048
        #define RTE_TEST_TX_DESC_DEFAULT 2048
        #export RTE_SDK=/root/dpdk-2.0.0
        # export RTE_SDK=/root/dpdk-2.0.0
        # export RTE_TARGET=x86_64-native-linuxapp-gcc
```

```
# make
```

13.6 Set up the vHost Network in the VM

1. Load the UIO kernel module in the VM:

```
# modprobe uio
```

```
# insmod /root/dpdk-2.0.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko
```

2. Check the PCI ID for the 10GbE NIC ports:

```
# lscpi -nn
00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
[1af4:1000]
00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
[1af4:1000]
```

3. Bind the user side vhost network devices with the igb_uio driver:

```
# /root/dpdk-2.0.0/tools/dpdk_nic_bind.py -b igb_uio 00:04.0
```

```
# /root/dpdk-2.0.0/tools/dpdk_nic_bind.py -b igb_uio 00:05.0
```



13.7 Start test-pmd Application in the VM

TX RS bit threshold=0 - TXQ flags=0xf00

13.8 CPU Affinity Tuning

The tables below show the host's CPU core affinity settings for VM-to-VM tests configuration for 1 physical core (no hyper-threading). When the two VMs start, there will be multiple QEMU threads spawned. Refer to section 10.5 CPU Core Affinity for the Virtual Machine (qemu-kvm), to set the active QEMU threads to the correct core affinity.

Logical Core	Process
1	ovs-vswitchd
2	PMD0
3, 4, 5	QEMU (VM1)
5, 6, 7	QEMU (VM2)

Table 13-1 CPU affinity setting on the host



Logical Core	Process	CPU% (from htop)
3	QEMU (main thread for VM1)	100
4	QEMU	100
5	QEMU	0

Table 13-2 VM1 QEMU threads CPU affinity

Note: Two active threads (with 100% CPU) are set to 2 different logical cores

Table 13-3 VM2 QEMU threads CPU affinity

Logical Core	Process	CPU% (from htop)
6	QEMU (main thread for VM2)	100
7	QEMU	100
5	QEMU	0

Note: Two active threads (with 100% CPU) are set to 2 different logical cores



14.0 VXLAN Test Setup

Follow the instructions below to configure VXLAN test setup. Test setup configurations include using native OvS and OvS with DPDK.

14.1 Native OvS Setup

To setup and start regular OvS in Host A and Host B, please refer to section 11.1 Configure the Host Machine and follow the instructions below.

14.1.1 Set the Kernel Boot Parameters

1. With hyper-threading disabled, add the following to the kernel boot parameters /etc/default/grub for 2 sockets:

```
GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=16 hugepagesz=2M hugepages=2048
intel_iommu=off isolcpus=1-7 rhgb quiet"
```

- 2. Save the file and update the GRUB config file: # grub2-mkconfig -o /boot/grub2/grub.cfg
- Reboot the host machine and check to make sure 1GB and 2MB HugePage sizes are created. You should see 16 1GB HugePages and 2048 2MB HugePages:

```
# ls /sys/devices/system/node/node0/hugepages/hugepages-*
hugepages-1048576kB/ hugepages-2048kB/
# cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
16
# cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
2048
```

14.1.2 Compile and Install OvS

1. Go to the OvS directory and run:



14.1.3 Prepare to Start OvS

```
1. Mount the 1GB HugePage and 2MB HugePage:
```

- # mkdir -p /mnt/huge
- # mkdir -p /mnt/huge_2mb
- # mount -t hugetlbfs nodev /mnt/huge
- # mount -t hugetlbfs nodev /mnt/huge_2mb -o pagesize=2MB

2. Check that HugePages are mounted:

```
# mount
```

nodev on /mnt/huge type hugetlbfs (rw,relatime)

```
nodev on /mnt/huge 2mb type hugetlbfs (rw,relatime,pagesize=2MB)
```

3. Load the modules:

```
# modprobe openvswitch
# modprobe i40e
```

4. Remove and terminate previous-run OvS and prepare:

```
# pkill -9 ovs
```

- # rm -rf /usr/local/var/run/openvswitch
- # rm -rf /usr/local/etc/openvswitch/
- # rm -f /tmp/conf.db
- # mkdir -p /usr/local/etc/openvswitch
- # mkdir -p /usr/local/var/run/openvswitch

5. Initialize the new OvS database and start the server:

export OVS_DIR=/root/OVS-2.4/ovs

cd \$OVS DIR

./ovsdb/ovsdb-tool create /usr/local/etc/openvswitch/conf.db \

./vswitchd/vswitch.ovsschema

6. Start the database server:

7. Initialize the OvS database:

./utilities/ovs-vsctl --no-wait init

8. Start OvS-vswitchd:
 # ./vswitchd/ovs-vswitchd

14.1.4 Create the Ports and VXLAN VTEP

Host A Configuration

- 1. Create the VXLAN tunnel between 2 hosts:
 - # ./utilities/ovs-vsctl add-br br0
 - # ifconfig br0 2.2.2.1/24
 - # ./utilities/ovs-vsctl add-port br0 eth3
 - # ./utilities/ovs-appctl ovs/route/add 2.2.2.2/24 br0
- 2. Create an internal bridge:



```
# ./utilities/ovs-vsctl add-br br-int
```

```
# ifconfig br-int 1.1.1.1/24
```

```
# ./utilities/ovs-vsctl add-port br-int eth2
```

3. Add VXLAN VTEP:

```
# ./utilities/ovs-vsctl show
```

./utilities/ovs-appctl ovs/route/show

Host B Configuration

1. Create a VXLAN tunnel between the 2 hosts:

./utilities/ovs-vsctl add-br br0

```
# ifconfig br0 2.2.2.2/24
```

- # ./utilities/ovs-vsctl add-port br0 eth3
- # ./utilities/ovs-appctl ovs/route/add 2.2.2.1/24 br0

2. Create an internal bridge:

./utilities/ovs-vsctl add-br br-int

```
# ifconfig br-int 1.1.1.2/24
```

./utilities/ovs-vsctl add-port br-int eth2

3. Add VXLAN VTEP:

```
# ./utilities/ovs-vsctl add-port br-int vxlan0 -- set Interface vxlan0 \
```

```
type=vxlan options:remote_ip=2.2.2.1 options:key=1000
```

- # ./utilities/ovs-vsctl show
- # ./utilities/ovs-appctl ovs/route/show

14.1.5 Add the Port Flows

Host A and Host B Configuration

- 1. Clear current flows:
 - # cd \$OVS_DIR
 - # ./utilities/ovs-ofctl del-flows br-int
- 2. Add flow for port 1 (physical) to port 2 (VTEP):

```
# ./utilities/ovs-ofctl add-flow br-int \
```

```
in_port=1,dl_type=0x800,idle_timeout=0,action=output:2
```

```
# ./utilities/ovs-ofctl add-flow br-int \
```

```
in_port=2,dl_type=0x800,idle_timeout=0,action=output:1
```

```
# ./utilities/ovs-ofctl dump-flows br-int
```

14.2 OvS with DPDK Setup

To set up and start OvS with DPDK in Host A and Host B, to section 11.0 **OvS Test Setup** and follow up to the section 11.9 Start OvS-vSwitchd. Then follow the instructions below to configure the VXLAN test setup.



14.2.1 Tune OvS-vSwitchd for VXLAN

Once the OvS-vSwitchd is running, we setup the CPU core affinity for the OvS PMD threads to 1 core, and 2 cores respectively.

One-PMD Configuration

1. Set the default OvS PMD thread usage to CPU2 (0x4):

./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4
./ovs-vsctl set Open vSwitch . other config:max-idle=30000

Two-PMD Configuration

- 1. For 2 physical cores and 2 logical cores (2 PMDs) on system HT disabled, set the default OvS PMD thread usage to CPU2 and CPU3 (0xC):
 - # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C
 - # ./ovs-vsctl set Open vSwitch . other config:max-idle=30000

14.2.2 Create the Ports and VXLAN VTEP

Host A Configuration

```
1. Create the VXLAN tunnel between 2 hosts:
```

- # ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
- # ifconfig br0 2.2.2.1/24
- # ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
- # ./utilities/ovs-appctl ovs/route/add 2.2.2.2/24 br0
- 2. Create an internal bridge:
 - # ./utilities/ovs-vsctl add-br br-int -- set bridge br-int datapath_type=netdev
 # ifconfig br-int 1.1.1.1/24
 - # ./utilities/ovs-vsctl add-port br-int dpdk1 -- set Interface dpdk1 type=dpdk

3. Add VXLAN VTEP:

- # ./utilities/ovs-vsctl add-port br-int vxlan0 -- set Interface \
 - vxlan0 type=vxlan options:remote ip=2.2.2.2 options:key=1000
- # ./utilities/ovs-vsctl show
- # ./utilities/ovs-appctl ovs/route/show

Host B Configuration

4. Create a VXLAN tunnel between the 2 hosts:

- # ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
 # ifconfig br0 2.2.2.2/24
- # ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
- # ./utilities/ovs-appctl ovs/route/add 2.2.2.1/24 br0
- 5. Create an internal bridge:
 - # ./utilities/ovs-vsctl add-br br-int -- set bridge br-int datapath_type=netdev
 # ifconfig br-int 1.1.1.2/24
 - # ./utilities/ovs-vsctl add-port br-int dpdk1 -- set Interface dpdk1 type=dpdk
- 6. Add VXLAN VTEP:



- # ./utilities/ovs-vsctl add-port br-int vxlan0 -- set Interface vxlan0 \
 type=vxlan options:remote_ip=2.2.2.1 options:key=1000
- # ./utilities/ovs-vsctl show
- # ./utilities/ovs-appctl ovs/route/show

14.2.3 Add the Port Flows

Host A and Host B Configuration

- 1. Clear current flows:
 - # cd \$OVS_DIR
 - # ./utilities/ovs-ofctl del-flows br-int
- 2. Add flow for port 1 (physical) to port 2 (VTEP):
 - # ./utilities/ovs-ofctl add-flow br-int \
 - in_port=1,dl_type=0x800,idle_timeout=0,action=output:2
 - # ./utilities/ovs-ofctl add-flow br-int \
 - in_port=2,dl_type=0x800,idle_timeout=0,action=output:1
 - # ./utilities/ovs-ofctl dump-flows br-int



Appendix A: Acronyms and Abbreviations

BMWG Benchmark W CPU Central Process DPDK Data Plane De DUT Device-Under EMC Exact Match C ETSI European Tele	ssing Unit evelopment Kit -Test
BMWG Benchmark W CPU Central Process DPDK Data Plane De DUT Device-Under EMC Exact Match C ETSI European Tele	forking Group ssing Unit evelopment Kit -Test fache
CPU Central Process DPDK Data Plane De DUT Device-Under EMC Exact Match C ETSI European Tele	ssing Unit evelopment Kit -Test Gache
DPDK Data Plane De DUT Device-Under EMC Exact Match C ETSI European Tele	evelopment Kit -Test Gache
DUT Device-Under- EMC Exact Match C ETSI European Tele	-Test ache
EMC Exact Match C ETSI European Tele	ache
ETSI European Tele	
	ecommunications Standards Institute
GbE Gigabit Ethern	net
GRUB GRand Unified	Bootloader
IETF Internet Engin	neering Task Force
IPDV Inter-Packet D	Delay Variation
IPv4 Internet Proto	pcol version 4
IRQ Interruption R	lequest
ITU International 1	Telecommunication Union
ITU-T ITU Telecomm	nunication Standardization Sector
KVM Kernel-based	Virtual Machine
LAN Local Area Net	twork
MAC Media Access	Control
NFV Network Func	tions Virtualization
NIC Network Inter	face Card
NUMA Non-Uniform	Memory Access
ONP Intel® Open Ne	etwork Platform



	1
Abbreviation	Description
OPNFV	Open Platform for NFV
OvS	Open vSwitch
PCI	Peripheral Component Interconnect
PDV	Packet Delay Variation
РНҮ	Physical Layer
PID	Process ID
PMD	Poll Mode Driver
QEMU	Quick Emulator
RFC	Request for Comments
SDN	Software-Defined Networking
SELinux	Security-Enhanced Linux
SLA	Service-Level Agreement
TLB	Translation Lookaside Buffer
vCPE	Virtual Customer Premises Equipment
vhost	Virtual Host
VM	Virtual Machine
VNF	Virtualized Network Function
VTEP	VXLAN Tunnel End Point
VXLAN	Virtual eXtensible LAN



Appendix B: References

Title	Reference
01.org: Intel® Open Network Platform	https://01.org/packet-processing/intel%C2%AE-onp-servers
01.org: Intel® ONP 2.0 Reference Architecture Guide	https://01.org/packet-processing/intel%C2%AE-onp-servers
01.org: Intel® ONP 2.0 Release Notes	https://01.org/packet-processing/intel%C2%AE-onp-servers
Intel® Ethernet Converged Network Adapter X710-DA2	http://ark.intel.com/products/83964/Intel-Ethernet-Converged-Network-Adapter- X710-DA2
Intel® Ethernet Converged Network Adapter X710-DA4	http://ark.intel.com/products/83965/Intel-Ethernet-Converged-Network-Adapter- X710-DA4?q=X710%20DA4
Intel® Server Board S2600WT2	http://ark.intel.com/products/82155/Intel-Server-Board-S2600WT2
Intel® Xeon® Processor D-1540	http://ark.intel.com/products/87039/Intel-Xeon-Processor-D-1540-12M-Cache-2_00-GHz
Intel® Xeon® Processor E5-2697 v3	http://ark.intel.com/products/81059/Intel-Xeon-Processor-E5-2697-v3-35M-Cache- 2_60-GHz
RFC 2544 Benchmarking Methodology for Network Interconnect Devices	https://tools.ietf.org/html/rfc2544
RFC 2679 A One-way Delay Metric for IPPM	https://tools.ietf.org/html/rfc2679
RFC 3393 IP Packet Delay Variation Metric for IP Performance Metrics (IPPM)	https://tools.ietf.org/html/rfc3393
RFC 3432 Network performance measurement with periodic streams	https://tools.ietf.org/html/rfc3432
RFC 3550 RTP: A Transport Protocol for Real-Time Applications	https://tools.ietf.org/html/rfc3550
RFC 5481 Packet Delay Variation Applicability Statement	https://tools.ietf.org/html/rfc5481
RFC 7348 Virtual eXtensible Local Area Network (VXLAN): A Framework for Overlaying Virtualized Layer 2 Networks over Layer 3 Networks	https://tools.ietf.org/html/rfc7348



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