## Revision History

<table>
<thead>
<tr>
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<tr>
<td>March 31, 2016</td>
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</table>
## Contents

1.0 Audience and Purpose .......................................................................................................................... 8
2.0 Summary .................................................................................................................................................. 9
3.0 Platform Specifications ........................................................................................................................... 10
  3.1 Hardware Ingredients ............................................................................................................................ 10
  3.2 Software Versions ................................................................................................................................. 10
  3.3 Boot Settings ....................................................................................................................................... 11
  3.4 Compile Options ................................................................................................................................. 11
  3.5 Operating System Settings .................................................................................................................. 12
4.0 Test Configurations ................................................................................................................................. 13
  4.1 Traffic Profiles .................................................................................................................................... 14
5.0 Test Metrics ............................................................................................................................................ 15
  5.1 Packet Processing Performance Metrics ............................................................................................ 15
  5.2 Throughput .......................................................................................................................................... 15
    5.2.1 Layer 2 Throughput ....................................................................................................................... 16
    5.2.2 Layer 3 Throughput ....................................................................................................................... 16
  5.3 Latency ................................................................................................................................................. 17
  5.4 Packet Delay Variation (PDV) ............................................................................................................. 17
6.0 Test Cases ................................................................................................................................................ 18
  7.0 Test Results .......................................................................................................................................... 19
    7.1 Host Performance (PHY-PHY) .......................................................................................................... 19
    7.2 Virtual Switching Performance (PHY-OVS-PHY) ......................................................................... 21
      7.2.1 Native OvS and OvS with DPDK ................................................................................................. 22
      7.2.2 Performance Scaling of OvS with DPDK ................................................................................... 24
      7.2.3 Performance with Hyper-Threading .......................................................................................... 26
    7.3 One VM Throughput (PHY-VM-PHY) ............................................................................................... 28
      7.3.1 Native OvS and OvS with DPDK ................................................................................................. 28
      7.3.2 Performance Scaling of OvS with DPDK ................................................................................... 32
      7.3.3 Performance with Hyper-Threading .......................................................................................... 33
    7.4 Two VM Throughput (PHY-VM-VM-PHY) ...................................................................................... 35
    7.5 40 Gbps Switching Performance .................................................................................................... 37
    7.6 VXLAN Performance (PHY-OVS-VM-OVS-PHY) .......................................................................... 40
      7.6.1 VXLAN Test Methodology ........................................................................................................ 40
      7.6.2 OvS and OvS with DPDK .......................................................................................................... 41
8.0 Industry Benchmarks ............................................................................................................................... 43
  8.1 ETSI NFV .............................................................................................................................................. 43
  8.2 IETF ...................................................................................................................................................... 43
  8.3 Open Platform for NFV (OPNFV) ........................................................................................................ 44
9.0 Performance Tuning ................................................................................................................................. 45
  9.1 Tuning Methods ................................................................................................................................... 45

Test Report
9.2 CPU Core Isolation for OvS-DPDK ........................................................................................................ 45
9.3 HugePage Size 1 GB ................................................................................................................................ 45
9.4 CPU Core Affinity for ovs-vswitchd and OvS PMD Threads .................................................................. 46
9.5 CPU Core Affinity for the Virtual Machine (qemu-kvm) ..................................................................... 46
9.6 Troubleshooting Tips for OvS ..................................................................................................................... 48
10.0 OvS Test Setup ......................................................................................................................................... 50
10.1 Configure the Host Machine .................................................................................................................... 50
10.2 Set the Kernel Boot Parameters ............................................................................................................ 50
10.3 Compile DPDK 2.2 .................................................................................................................................... 51
10.4 Install OvS ................................................................................................................................................ 51
10.5 Prepare to Start OvS ................................................................................................................................. 51
10.6 Bind 10 GbE NIC Ports to the igb_uio Driver ........................................................................................ 52
10.7 Remove and Terminate Previous-Run OvS and Prepare ...................................................................... 53
10.1 Initialize the New OvS Database ............................................................................................................. 53
10.2 Start OvS-vSwitchd .................................................................................................................................. 53
10.3 Tune OvS-vswitchd .................................................................................................................................... 53
10.4 Create the Ports ...................................................................................................................................... 54
10.5 Add the Port Flows ................................................................................................................................. 55
11.0 PHY-VM-PHY Test Setup ....................................................................................................................... 56
11.1 Create the Ports ...................................................................................................................................... 56
11.2 Add the Port Flows ................................................................................................................................. 56
11.3 Power on the VM .................................................................................................................................... 57
11.4 Set the VM Kernel Boot Parameters .................................................................................................... 57
11.5 Set up the VM HugePages ...................................................................................................................... 58
11.6 Set up DPDK 2.2 ...................................................................................................................................... 58
11.7 Set up the vhost Network in the VM ...................................................................................................... 58
11.8 Start the test-pmd Application in the VM .............................................................................................. 59
11.9 CPU Affinity Tuning ............................................................................................................................... 60
12.0 VM-VM Test Setup ................................................................................................................................. 61
12.1 Create the Ports ...................................................................................................................................... 61
12.2 Add the Port Flows ................................................................................................................................. 61
12.3 Power on the VM .................................................................................................................................... 62
12.3.1 VM Kernel Boot Parameters ........................................................................................................... 62
12.4 Set up the VM HugePages ....................................................................................................................... 63
12.5 Set up DPDK 2.2 ...................................................................................................................................... 63
12.6 Set up the vHost Network in the VM ..................................................................................................... 63
12.7 Start test-pmd Application in the VM .................................................................................................... 63
12.8 CPU Affinity Tuning ............................................................................................................................... 64
13.0 VXLAN Test Setup .................................................................................................................................. 66
13.1 Native OvS Setup ................................................................................................................................. 66
13.1.1 Set the Kernel Boot Parameters ...................................................................................................... 66
13.1.2 Compile and Install OvS .................................................................................................................... 66
13.1.3 Prepare to Start OvS .............................................................................................................................................. 67
13.1.4 Create the Ports and VXLAN VTEP .................................................................................................................. 67
13.1.5 Add the Port Flows ............................................................................................................................................... 68
13.1.6 Power on the VM ................................................................................................................................................. 68
13.1.7 VM Kernel Boot Parameters ............................................................................................................................... 69
13.1.8 Set up the VM HugePages .................................................................................................................................. 69
13.1.9 Set up the vHost Network in the VM .................................................................................................................... 69
13.1.10 Start test-pmd Application in the VM .................................................................................................................. 70
13.2 OvS with DPDK Setup .............................................................................................................................................. 70
13.2.1 Tune OvS-vSwitchd for VXLAN ........................................................................................................................... 70
13.2.2 Create the Ports and VXLAN VTEP ....................................................................................................................... 71
13.2.3 Add the Port Flows ................................................................................................................................................. 71
13.2.4 Power on the VM .................................................................................................................................................... 71
13.2.5 VM Kernel Boot Parameters ................................................................................................................................ 72
13.2.6 Set up the VM HugePages .................................................................................................................................. 72
13.2.7 Set up the vHost Network in the VM ..................................................................................................................... 72
13.2.8 Start test-pmd Application in the VM ..................................................................................................................... 73
Appendix A: Acronyms and Abbreviations .................................................................................................................... 74
Appendix B: References .................................................................................................................................................... 76
Legal Information ............................................................................................................................................................... 77
Figures

Figure 4-1 High-Level Overview of Test Setup.................................................................................................13
Figure 7-1 Host Performance test setup (PHY-PHY)...........................................................................................19
Figure 7-2 Forwarding throughput performance with 1, 2, and 4 physical cores .................................................20
Figure 7-3 Virtual Switching Performance test setup (PHY-OVS-PHY).................................................................21
Figure 7-4 Throughput performance of native OvS (with hyper-threading) and OvS with DPDK (without and with hyper-threading)..................................................................................................................22
Figure 7-5 64-byte throughput performance scaling of OvS with DPDK for 1, 2, 4 cores, no hyper-threading ...........25
Figure 7-6 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4 flows)..................................................................................................................................26
Figure 7-7 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows)..................................................................................................................................27
Figure 7-8 One VM Throughput Performance test setup ......................................................................................28
Figure 7-9 Throughput performance with 1 VM comparing native OvS and OvS with DPDK (using one physical core) .................................................................................................................................................29
Figure 7-10 Throughput performance with a 1 VM comparing native OvS and OvS with DPDK using one and two physical cores (no hyper-threading) ........................................................................................................31
Figure 7-11 64-byte throughput performance scaling of OvS with DPDK - 1 VM (PHY-VM-PHY) with 1, 2, 4 cores, no hyper-threading........................................................................................................................32
Figure 7-12 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4 flows, 1 VM)..............................................................................................................33
Figure 7-13 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows, 1 VM)..............................................................................................................34
Figure 7-14 Two-VM Throughput performance test setup ....................................................................................35
Figure 7-15 Two-VM Throughput (PHY-VM-VM-PHY) with 2 ports and 2 Flows.....................................................36
Figure 7-16 40 Gbps switching performance – test scenario 1....................................................................37
Figure 7-17 40 Gbps switching performance – test scenario 2 .............................................................................38
Figure 7-18 40 Gbps switching performance – test scenario 3 .............................................................................38
Figure 7-19 OvS with DPDK - 40 Gbps switching performance for 256B packets .................................................39
Figure 7-20 VXLAN Scenario with 1 Physical Port and VTEP in the vSwitch.........................................................40
Figure 7-21 VXLAN Performance for 64-byte packets comparing native OvS and OvS with DPDK .................41
Figure 7-22 Impact of VXLAN overhead on processing performance in the PHY-VM-PHY test case for 64B packets ..........................................................................................................................................42
Figure 8-1 Snapshot of OPNFV Test projects and Infrastructure .............................................................................44
Figure 9-1 Output from htop showing high CPU usage for active QEMU threads ................................................48
Tables

Table 3-1 Intel® Xeon® processor E5-2695 v4 Platform – hardware ingredients used in performance tests ..........10
Table 3-2 Software versions used in performance tests ..................................................................................10
Table 3-3 Boot Settings ...............................................................................................................................11
Table 3-4 Compile Option Configurations ..................................................................................................11
Table 3-5 Operating System Settings ..........................................................................................................12
Table 4-1 Number of cores used for each test category (4 physical cores total per CPU) ..............................14
Table 5-1 Throughput and switching performance metrics for two example test-cases ...............................16
Table 6-1 Summary of Test Cases ................................................................................................................18
Table 7-1 PHY-PHY test configuration variables ........................................................................................19
Table 7-2 PHY-PHY test results for 2 physical cores ................................................................................20
Table 7-3 PHY-OVS-PHY test configuration variables for native OvS and OvS with DPDK ......................22
Table 7-4 PHY-OVS-PHY test results for native OvS (1 core, 1 flow per port) ..........................................23
Table 7-5 PHY-OVS-PHY test results for OvS with DPDK (1 core, 1 flow per port) ...............................23
Table 7-6 PHY-OVS-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port) ..................24
Table 7-7 PHY-OVS-PHY test configuration variables for OvS with DPDK (scaling with physical cores) ....24
Table 7-8 PHY-OVS-PHY test results for OvS with DPDK (4 cores, 1k flows per port, HT off) ...............25
Table 7-9 PHY-OVS-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT) ........26
Table 7-10 PHY-VM-PHY test configuration variables for native OvS and OvS with DPDK ....................29
Table 7-11 PHY-VM-PHY test results for native OvS (1 core, 1 flow per port) .........................................30
Table 7-12 PHY-VM-PHY test results for OvS with DPDK (1 core, 1 flow per port) ...............................30
Table 7-13 PHY-VM-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port) .................30
Table 7-14 OvS with DPDK, no hyper-threading (2 physical cores) .................................................................31
Table 7-15 PHY-VM-PHY test configuration variables for OvS with DPDK (scaling with physical cores) ....32
Table 7-16 PHY-VM-PHY test results for OvS with DPDK (1, 2, 4 cores, 1 flow per port, HT off) ............32
Table 7-17 PHY-VM-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT) ........33
Table 7-18 PHY-VM-PHY test results for OvS DPDK (2 physical cores) ....................................................34
Table 7-19 PHY-VM-PHY test configuration variables for OvS with DPDK ..................................................35
Table 7-20 PHY-VM-VM-PHY test results for OvS with DPDK (2 cores and 4 threads) ..........................36
Table 7-21 OvS with DPDK – 40 Gbps switching performance for 256B packets ........................................39
Table 7-22 VXLAN test configuration variables for native OvS and OvS with DPDK ..................................41
Table 7-23 VXLAN Packet Throughput with 2 cores (hyper-threading off) .................................................42
Table 11-1 CPU Affinity Setting on the Host ...............................................................................................60
Table 11-2 QEMU Threads CPU Affinity ....................................................................................................60
Table 12-1 CPU affinity setting on the host ..................................................................................................64
Table 12-2 VM1 QEMU threads CPU affinity ............................................................................................65
Table 12-3 VM2 QEMU threads CPU affinity ............................................................................................65
1.0 Audience and Purpose

Intel® Open Network Platform (Intel ONP) is a Reference Architecture that provides engineering guidance and ecosystem-enablement support to encourage widespread adoption of Software-Defined Networking (SDN) and Network Functions Virtualization (NFV) solutions in Telco, Enterprise, and Cloud. Intel® ONP is released in the form of a software stack and a set of documents (e.g. Intel® Open Network Platform Reference Architecture Guide, Release Notes, and Performance Test Reports) available on 01.org.

The primary audiences for this test report are architects and engineers implementing the Intel® ONP Reference Architecture using open-source software ingredients that include:

- OpenStack*
- OpenDaylight*
- Data Plane Development Kit (DPDK)*
- Open vSwitch* with DPDK
- Fedora*

This test report provides a guide to packet processing performance testing of the Intel® ONP. The report includes baseline performance data and provides system configuration and test cases relevant to SDN/NFV. The purpose of documenting these configurations and methods is not to imply a single “correct” approach, but rather to provide a baseline of well-tested configurations and test procedures. This will help guide architects and engineers who are evaluating and implementing SDN/NFV solutions and can greatly assist in achieving optimal system performance.

Ideally, the same hardware platform specifications and software versions are used for both Intel® ONP Reference Architecture Guide and Performance Test Reports. Exceptions can however occur due to software issues, version revisions and other factors during integration and benchmarking activities. Information on these exceptions is provided in Intel® ONP Release 2.1 Application Note on Hardware and Software Differences between Reference Architecture Guide and Performance Test Report available on 01.org.
2.0 Summary

Benchmarking an SDN/NFV system is not trivial and requires expert knowledge of networking and virtualization technologies. Engineers also need benchmarking and debugging skills, as well as a good understanding of the device-under-test (DUT) across compute, networking, and storage domains. Knowledge of specific network protocols and hands-on experience with relevant open-source software, such as Linux, kernel-based virtual machine (KVM), quick emulator (QEMU), DPDK, OvS, etc., are also required.

Repeatability is essential when testing complex systems and this can be difficult to achieve with manual configuration and test methods. Automated integration, deployment and test methods are needed for developing robust SDN/NFV solutions. Many of these challenges are being addressed through industry forums such as OPNFV. Future versions of Intel® ONP will also provide guidance on adopting automation tools and methods.

This report is built on earlier Intel® ONP test reports (available on 01.org as archived documents). These earlier reports have baseline performance data and configuration procedures (for Linux operating system setup, BIOS configurations, configuration for OvS, VM setup, building DPDK and OvS, etc.).

The focus of this report is to present the packet processing performance using the Intel® Xeon® processor E5-2695 v4 (formerly Broadwell-EP) that can provide up to 18 Xeon®-class physical cores and is ideal for NFV solutions for hosting Virtual Network Functions (VNFs).

In this document “native OvS” refers to Open vSwitch which is a multilayer virtual switch licensed under the open source Apache 2.0 license (http://openvswitch.org/). The DPDK accelerated version of OvS is referred to as “OvS with DPDK” in this document.

This report includes:
- Packet performance tests on Intel® Xeon® processor E5-2695 v4 (formerly Broadwell-EP)
- New versions of software ingredients (compared to Intel® ONP 2.0)
- vHost user for QEMU VM fast-path interface
- 40Gbps performance testing with two Intel® Ethernet X710-DA4 Adapters
- Updated Virtual eXtensible LAN (VXLAN) performance tests
- Tuning methods and troubleshooting tips for achieving good packet processing performance with Open vSwitch with DPDK
- Information on industry NFV test activities
- Performance using host with DPDK i.e. “bare-metal” (used to establish a performance baseline)
- Virtual Switch performance comparing native OvS and OvS with DPDK
- Performance scaling of OvS with DPDK
- Throughput performance with one and two VMs
- VXLAN performance comparing native OvS and OvS with DPDK.
3.0 Platform Specifications

3.1 Hardware Ingredients

Table 3-1 Intel® Xeon® processor E5-2695 v4 Platform – hardware ingredients used in performance tests

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Server Platform</td>
<td>Supermicro X10DRH-I</td>
</tr>
<tr>
<td></td>
<td>Dual Integrated 1GbE ports via Intel® i350-AM2 Gigabit Ethernet</td>
</tr>
<tr>
<td>Chipset</td>
<td>Intel® C612 chipset (formerly Lynx-H Chipset)</td>
</tr>
<tr>
<td>Processor</td>
<td>1x Intel® Xeon® processor E5-2695 v4 (formerly Broadwell-EP)</td>
</tr>
<tr>
<td></td>
<td>2.10 GHz; 120 W; 45 MB cache per processor</td>
</tr>
<tr>
<td></td>
<td>18 cores, 36 hyper-threaded cores per processor</td>
</tr>
<tr>
<td>Memory</td>
<td>64GB Total; Samsung 8GB 2Rx8 PC4-2400MHz, 8GB per channel, 8 Channels</td>
</tr>
<tr>
<td>Local Storage</td>
<td>500 GB HDD Seagate SATA Barracuda 7200.12 (SN:Z6EM258D)</td>
</tr>
<tr>
<td>PCIe</td>
<td>2 x PCI-E 3.0 x8 slot</td>
</tr>
<tr>
<td>NICs</td>
<td>2 x Intel® Ethernet Converged Network Adapter X710-DA4</td>
</tr>
<tr>
<td></td>
<td>Total: 8 Ports; 2 ports from each NIC used in tests.</td>
</tr>
<tr>
<td>BIOS</td>
<td>AMIBIOS Version: 2.0 Release Date: 12/17/2015</td>
</tr>
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3.2 Software Versions

Table 3-2 Software versions used in performance tests

<table>
<thead>
<tr>
<th>Software Component</th>
<th>Version</th>
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<tbody>
<tr>
<td>Host Operating System</td>
<td>Fedora 23 x86_64 (Server version)</td>
</tr>
<tr>
<td>Kernel version: 4.2.3-300.fc23.x86_64</td>
<td></td>
</tr>
<tr>
<td>VM Operating System</td>
<td>Fedora 23 x86_64 (Server version)</td>
</tr>
<tr>
<td>Kernel version: 4.2.3-300.fc23.x86_64</td>
<td></td>
</tr>
<tr>
<td>QEMU-KVM</td>
<td>QEMU-KVM version 2.5.0</td>
</tr>
<tr>
<td>libvirt version: 1.2.18.2-2.fc23.x86_64</td>
<td></td>
</tr>
<tr>
<td>Open vSwitch (native OvS and OvS with DPDK)</td>
<td>Open vSwitch 2.4.9 Commit ID: 53902038abe62c45ff46d7de9dcec30c3d1d861e</td>
</tr>
<tr>
<td>Intel® Ethernet Drivers</td>
<td>i40e-1.4.25</td>
</tr>
<tr>
<td>DPDK</td>
<td>DPDK version: 2.2.0</td>
</tr>
<tr>
<td><a href="http://www.dpdk.org/browse/dpdk/snapshot/dpdk-2.2.0.tar.gz">http://www.dpdk.org/browse/dpdk/snapshot/dpdk-2.2.0.tar.gz</a></td>
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</table>
3.3 Boot Settings

Although Turbo Boost and Energy Efficient Turbo would increase the performance, not all cores would be running at the turbo frequency. CPU turbo frequency would change depending on the number of active cores being utilized and it would impact the consistency of the OvS throughput performance. Thus, it would not be a real representation of the platform performance. Therefore, test configuration in Intel® ONP 2.1 was changed respectively to have the Turbo Boost, Energy Efficient Turbo and P-state disabled in BIOS.

Table 3-3 Boot Settings

<table>
<thead>
<tr>
<th>System Capability</th>
<th>Description</th>
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<tr>
<td>Host Boot Settings</td>
<td>Hugepage size = 1G; No. of Hugepages = 16</td>
</tr>
<tr>
<td></td>
<td>Hugepage size = 2MB; No. of Hugepages = 2048</td>
</tr>
<tr>
<td></td>
<td>Hyper-threading disabled: isolcpus = 1-17</td>
</tr>
<tr>
<td></td>
<td>Hyper-threading enabled: isolcpus = 1-17, 19-35</td>
</tr>
<tr>
<td>BIOS settings</td>
<td>C-state disabled</td>
</tr>
<tr>
<td></td>
<td>P-state disabled</td>
</tr>
<tr>
<td></td>
<td>Hyper-threading enabled/disabled</td>
</tr>
<tr>
<td></td>
<td>Turbo Boost Disabled</td>
</tr>
<tr>
<td></td>
<td>Enhanced Intel® Speedstep® Technology disabled</td>
</tr>
<tr>
<td></td>
<td>Energy Efficient Turbo disabled</td>
</tr>
<tr>
<td></td>
<td>Intel VT-x enabled</td>
</tr>
<tr>
<td>VM Kernel Boot settings</td>
<td>Hugepage size = 1G; No. of Hugepages = 1</td>
</tr>
<tr>
<td></td>
<td>Hugepage size = 2MB; No. of Hugepages = 1024</td>
</tr>
<tr>
<td></td>
<td>isolcpus = 1-2</td>
</tr>
</tbody>
</table>

3.4 Compile Options

Table 3-4 Compile Option Configurations

<table>
<thead>
<tr>
<th>System Capability</th>
<th>Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>DPDK Compilation</td>
<td>CONFIG_RTE_BUILD_COMBINE_LIBS=y</td>
</tr>
<tr>
<td></td>
<td>CONFIG_RTE_LIBRTE_VHOST=y</td>
</tr>
<tr>
<td></td>
<td>CONFIG_RTE_LIBRTE_VHOST_USER=y</td>
</tr>
<tr>
<td></td>
<td>DPDK compiled with &quot;-Ofast -g&quot;</td>
</tr>
<tr>
<td>OvS Compilation</td>
<td>OvS configured and compiled as follows:</td>
</tr>
<tr>
<td></td>
<td># ./configure --with-dpdk=&lt;DPDK SDK PATH&gt;/x86_64-native-linuxapp \</td>
</tr>
<tr>
<td></td>
<td>CFLAGS=&quot;-Ofast -g&quot;</td>
</tr>
<tr>
<td></td>
<td># make CLFAGS=&quot;-Ofast -g&quot;</td>
</tr>
<tr>
<td>DPDK Settings</td>
<td>Build L3fwd: (in l3fwd/main.c)</td>
</tr>
<tr>
<td></td>
<td>#define RTE_TEST_RX_DESC_DEFAULT 2048</td>
</tr>
<tr>
<td></td>
<td>#define RTE_TEST_TX_DESC_DEFAULT 2048</td>
</tr>
</tbody>
</table>
## 3.5 Operating System Settings

<table>
<thead>
<tr>
<th>System Capability</th>
<th>Settings</th>
</tr>
</thead>
<tbody>
<tr>
<td>Linux OS Services Settings</td>
<td># systemctl disable NetworkManager.service</td>
</tr>
<tr>
<td></td>
<td># chkconfig network on</td>
</tr>
<tr>
<td></td>
<td># systemctl restart network.service</td>
</tr>
<tr>
<td></td>
<td># systemctl stop NetworkManager.service</td>
</tr>
<tr>
<td></td>
<td># systemctl stop firewalld.service</td>
</tr>
<tr>
<td></td>
<td># systemctl disable firewalld.service</td>
</tr>
<tr>
<td></td>
<td># systemctl stop irqbalance.service</td>
</tr>
<tr>
<td></td>
<td># killall irqbalance</td>
</tr>
<tr>
<td></td>
<td># systemctl disable irqbalance.service</td>
</tr>
<tr>
<td></td>
<td># service iptables stop</td>
</tr>
<tr>
<td></td>
<td># kill -9 dhclient</td>
</tr>
<tr>
<td></td>
<td># echo 0 &gt; /proc/sys/kernel/randomize_va_space</td>
</tr>
<tr>
<td></td>
<td>SELinux disabled</td>
</tr>
<tr>
<td></td>
<td>net.ipv4.ip_forward=0</td>
</tr>
<tr>
<td>Linux Module Settings</td>
<td># rmmod ipmi_msghandler</td>
</tr>
<tr>
<td></td>
<td># rmmod ipmi_si</td>
</tr>
<tr>
<td></td>
<td># rmmod ipmi_devintf</td>
</tr>
<tr>
<td></td>
<td># rmmod lpc_ich</td>
</tr>
<tr>
<td></td>
<td># rmmod bridge</td>
</tr>
</tbody>
</table>
4.0 Test Configurations

The test setup is shown in Figure 4-1. The system-under-test is Intel® ONP Reference Architecture Release 2.1. The traffic is generated by Ixia running RFC 2544 (IxNetwork 7.40.929.15 EA; Protocols: 4.40.1075.13; IxOS: IxOS 6.80.1100.7 EA). The maximum theoretical system forwarding throughput is 40 Gbps aggregated across four 10GbE ports, except for VXLAN tests which use two ports. Physical ports are paired (one as ingress and one as egress), i.e., one 10 Gbps bidirectional flow “consumes” two ports. Unless otherwise stated, all tests are for zero packet loss.

Note: The System has two 4x10 Gbe formerly Fortville NICs. Two ports from each NICs are used in the setup to achieve the maximum packets performance in the system under test.

The VM network interface used is vhost-user with DPDK acceleration. The vhost-user information is available at http://dpdk.readthedocs.org/en/latest/prog_guide/vhost_lib.html along with DPDK documentation.

![Figure 4-1 High-Level Overview of Test Setup](image-url)
Allocation of cores has large impact on performance. This document include test configurations with hyper-threading enabled and disabled as well as 1, 2 and 4 physical cores. Table 4-1 summarizes the combinations of physical and hyper-threaded cores used for each test case.

<table>
<thead>
<tr>
<th>Test</th>
<th>Hyper-Threading off</th>
<th>Hyper-Threading on</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Number of Physical cores</td>
<td>Number of Hyper-Threaded Cores</td>
</tr>
<tr>
<td>Host Tests</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Virtual Switching Tests</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>PHY-to-VM Tests</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>VM-to-VM Tests</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>VXLAN Tests</td>
<td>✓</td>
<td>✓</td>
</tr>
</tbody>
</table>

### 4.1 Traffic Profiles

The IP traffic profile used conforms to RFC 2544:
- Frame sizes (bytes): 64, 128, 256, 512, 1024, 1280, and 1518
- L3 protocol: IPv4
- L4 protocol: UDP
- All tests are bidirectional with the same data rate being offered from each direction.
- For VXLAN, a header is used to encapsulate IP packets per RFC 7348.
5.0 Test Metrics

5.1 Packet Processing Performance Metrics

RFC 2544 is an Internet Engineering Task Force (IETF) RFC that outlines a benchmarking methodology for network interconnect devices. The methodology results in performance metrics (e.g., latency, frame loss percentage, and maximum data throughput).

In this document, network “throughput” (measured in millions of frames per second) is based on RFC 2544, unless otherwise noted. "Frame size" refers to Ethernet frames ranging from the smallest frames of 64 bytes to the largest of 1518 bytes.

RFC 2544 specifies the following types of tests:

- **Throughput tests** define the maximum number of frames per second that can be transmitted without any error. Throughput is the fastest rate at which the count of test frames transmitted by the DUT is equal to the number of test frames sent to it by the test equipment. Test time during which frames are transmitted must be at least 60 seconds.

- **Latency tests** measure the time required for a frame to travel from the originating device through the network to the destination device.

- **Frame loss tests** measure the network’s response in overload conditions—a critical indicator of the network's ability to support real-time applications in which a large amount of frame loss rapidly degrades service quality.

- **Burst tests** assess the buffering capability of a switch. They measure the maximum number of frames received at full-line rate before a frame is lost. In carrier Ethernet networks, this measurement validates the excess information rate as defined in many service-level agreements (SLAs).

- **System recovery tests** characterize speed of recovery from an overload condition.

- **Reset tests** characterize the speed of recovery from device or software reset.

“Test duration” refers to the measurement period for a particular packet size with an offered load and assumes the system has reached a steady state. Using the RFC 2544 test methodology, this is specified as at least 60 seconds.

5.2 Throughput

The throughput test data provided in this document represents “platform throughput” as measured by the Ixia traffic generator. Switching performance metrics include the number of switching operations for the particular configuration. This is illustrated in Table 5-1 using two examples of configurations with two and three switching operations, respectively. Table 5-1 shows the two configuration examples. Careful analysis of all configuration variables is needed before making performance comparisons that are meaningful.
Table 5-1 Throughput and switching performance metrics for two example test-cases

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Configuration Examples</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>PHY-OVS-PHY</td>
<td>PHY-VM-VM-PHY</td>
</tr>
<tr>
<td>Physical Ports</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>Physical cores</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>Hyper-threaded cores</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>Flows per Port (in each direction)</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>Total Flows</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>Switching Operations</td>
<td>1</td>
<td>3</td>
</tr>
<tr>
<td>Line-rate</td>
<td>40 Gbps</td>
<td>20 Gbps</td>
</tr>
<tr>
<td>OvS with DPDK: Throughput (packets/sec) 128B packets</td>
<td>33,783,782</td>
<td>5,345,806</td>
</tr>
<tr>
<td>OvS with DPDK: Switching (packets/sec) 128B packets</td>
<td>33,783,782</td>
<td>16,037,417</td>
</tr>
</tbody>
</table>

In this example, while the number of physical cores and hyper-threaded cores are same in both scenarios, the number of switching operations is different. Line rate is therefore different and cannot be used as a comparison metric. A more reasonable comparison would be to compare the switching performance as measured by the throughput (packets/second) multiplied by the number of switching operations. In this case we can see that adding 2 VMs introduces two extra switching operations and results in approximately 52% degradation in performance compared to the case with only a vSwitch and no VMs (single switching operation).

5.2.1 Layer 2 Throughput

This test determines the DUT’s maximum Layer 2 forwarding rate without traffic loss, as well as average and minimum/maximum latency for different packet sizes.

This test is performed full duplex with traffic transmitting in both directions.

The DUT must perform packet parsing and Layer 2 address lookups on the ingress port, and then modify the header before forwarding the packet on the egress port.

5.2.2 Layer 3 Throughput

This test determines the DUT’s maximum IPv4 Layer 3 forwarding rate without packet loss, as well as average and minimum/maximum latency for different packet sizes.

This test is performed full duplex with traffic transmitting in both directions.

The DUT must perform packet parsing and route lookups for Layer 3 packets on the ingress port and then forward the packet on the egress port without modifying the header.
5.3 Latency

With latency (i.e., packet delay) and packet delay variation, it is generally the worst-case performance that must be considered. Outliers can create customer disappointment at the carrier scale and cost service providers. The RFC 2544 measurement of latency is extensively used in traditional testing. NFV requires more information on latency, including packet delay variation. Ideally, the delay of all packets should be considered, but in practice some form of sampling is needed (this may not be periodic sampling).

Average and minimum/maximum latency numbers are usually collected with throughput tests; however, the distribution of latency is a more meaningful metric (i.e., a test that collects latency distribution for different packet sizes and over an extended duration to uncover outliers; latency tests should run for at least 1 hour and ideally for 24 hours). Collecting test data for all traffic conditions can take a long time. One approach is to use the highest throughput that has demonstrated zero packet loss for each packet size as determined with throughput tests. RFC 2679 defines a metric for one-way delay of packets across Internet paths and describes a methodology for measuring “Type-P-One-way-Delay” from source to destination.

5.4 Packet Delay Variation (PDV)

RFC 3393 provides definitions of PDV metrics for IP packets and is based on RFC 2679. This RFC notes that variation in packet delay is sometimes called “jitter” and that this term causes confusion because it is used in different ways by different groups of people. The ITU Telecommunication Standardization Sector also recommends various delay variation metrics [Y.1540] [G.1020]. Most of these standards specify multiple ways to quantify PDV. RFC 5481 specifies two forms of measuring variation of packet delay:

- Inter-Packet Delay Variation (IPDV) is where the reference is the previous packet in the stream (according to a sending sequence), and the reference changes for each packet in the stream. In this formulation, properties of variation are coupled with packet sequence. This form was called Instantaneous Packet Delay Variation in early IETF contributions and is similar to the packet spacing difference metric used for inter-arrival jitter calculations in RFC 3550.
- Packet Delay Variation (PDV) is where a single reference is chosen from the stream based on specific criteria. The most common criterion for the reference is the packet with the minimum delay in the sample. This term derives its name from a similar definition for Cell Delay Variation, an ATM performance metric [I.356].

Both metrics are derived from “one-way-delay” metrics and, therefore, require knowledge of time at the source and destination. Results are typically represented by histograms showing statistical distribution of delay variation. Packet loss has great influence for results (extreme cases are described in the RFC). For reporting and SLA purposes, simplicity is important and PDV lends itself better (e.g., percentiles, median, mean, etc.). PDV metrics can also be used with different stream characteristics, such as Poisson streams [RFC 3393] and periodic streams [RFC 3432], depending on the purpose and testing environment.
6.0 Test Cases

A summary of test cases is shown in Table 6-1.

Table 6-1 Summary of Test Cases

<table>
<thead>
<tr>
<th>Ref.</th>
<th>Test Description</th>
<th>Metrics</th>
<th>Packet Size (Bytes)</th>
<th>Test Duration</th>
<th>Flows per Port</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Host Performance (PHY-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.1</td>
<td>L3 Fwd (no pkt modification)</td>
<td>Throughput</td>
<td>64, 72, 128, 256, 512, 768, 1024, 1280, 1518</td>
<td>60 sec</td>
<td>One flow/port in both directions</td>
</tr>
<tr>
<td></td>
<td>4 ports</td>
<td>Latency (min, max, avg)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.2</td>
<td>vSwitch Performance (PHY-OVS-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>L3 Fwd</td>
<td>Throughput</td>
<td>64, 72, 128, 256, 512, 768, 1024, 1280, 1518</td>
<td>60 sec</td>
<td>One flow/port in both directions; One thousand flows/port in both directions</td>
</tr>
<tr>
<td></td>
<td>4 ports</td>
<td>Latency (min, max, avg)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.3</td>
<td>One VM Throughput (PHY-VM-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Single VM (vhost-user)</td>
<td>Throughput</td>
<td>64, 256</td>
<td>60 sec</td>
<td>One flow/port in both directions; One thousand flows/port in both directions</td>
</tr>
<tr>
<td></td>
<td>L3 Fwd</td>
<td>Latency (avg)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4 ports</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.4</td>
<td>Two VMs in series (vhost-user)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>L3 Fwd</td>
<td>Throughput</td>
<td>64, 72, 128, 256, 512, 768, 1024, 1280, 1518</td>
<td>60 sec</td>
<td>One flow/port in both directions</td>
</tr>
<tr>
<td></td>
<td>2 ports</td>
<td>Latency (min, max, avg)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.5</td>
<td>40 Gbps switching Performance</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4 VMs (4 x PHY-VM)</td>
<td>Throughput</td>
<td>256</td>
<td>60 sec</td>
<td>One flow/port in both directions</td>
</tr>
<tr>
<td></td>
<td>4 VMs (2 x PHY-VM and 1 x PHY-VM-VM-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 VMs in series (PHY-VM-VM-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2 ports</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>7.6</td>
<td>VXLAN Performance (PHY-OVS-VM-OVS-PHY)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>VXLAN decap/encap using vSwitch Tunnel End Point (TEP) with 1 VM</td>
<td>Throughput</td>
<td>114, 122, 178, 306, 562, 818, 1074, 1330, 1468</td>
<td>60 sec</td>
<td>One flow/port</td>
</tr>
<tr>
<td></td>
<td>L3 Fwd</td>
<td>Latency (min, max, avg)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1 port</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
7.0 Test Results

7.1 Host Performance (PHY-PHY)

The test setup for measuring host (i.e. no VM) throughput performance is shown in Figure 7-1. The host is installed with DPDK and it uses DPDK's L3 forwarding sample application. This test creates a good baseline for comparing more complex test cases as well as comparing "bare-metal" performance between different platforms. This is very important when trying to establish "apples-for-apples" comparisons for more complex test scenarios between different platforms. If the "bare-metal" performance cannot be calibrated between the platforms on a per-core / per-port basis, more complex scenarios involving virtualization will certainly not provide valid comparisons. Host tests attempt to achieve system throughput of 40 Gbps, using a 4-port configuration with 4 physical cores.

![Figure 7-1 Host Performance test setup (PHY-PHY)](image)

Table 7-1 summarizes the permutations of test configuration variables. In this case all tests uses 4 ports, 1 TX/RX queue per port, and 1 bi-directional flow per port (i.e. total of 4 unidirectional flows). Permutations were tested with 1, 2, and 4 physical cores and hyper-threading was not enabled.

<table>
<thead>
<tr>
<th>Test</th>
<th>Configuration Variable</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Ports</td>
</tr>
<tr>
<td>L3 Forwarding</td>
<td>4</td>
</tr>
</tbody>
</table>
Test results are shown in the Figure 7-2 below for all packet sizes.

![Figure 7-2](image)

**Figure 7-2 Forwarding throughput performance with 1, 2, and 4 physical cores**

L3 test data is presented for the 2 cores configuration in the Table 7-2. Line-rate (i.e. 40 Gbps) is achieved for all packet sizes equal or larger than 128 bytes.

**Table 7-2 PHY-PHY test results for 2 physical cores**

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Min Latency (ns)</th>
<th>Average Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>28,863</td>
<td>42,950,430</td>
<td>72</td>
<td>6,820</td>
<td>87,828</td>
<td>201,900</td>
</tr>
<tr>
<td>72</td>
<td>31,531</td>
<td>42,840,797</td>
<td>79</td>
<td>7,060</td>
<td>90,300</td>
<td>210,860</td>
</tr>
<tr>
<td>128</td>
<td>40,000</td>
<td>33,783,767</td>
<td>100</td>
<td>12,180</td>
<td>21,468</td>
<td>84,740</td>
</tr>
<tr>
<td>256</td>
<td>40,000</td>
<td>18,115,934</td>
<td>100</td>
<td>14,180</td>
<td>18,767</td>
<td>59,820</td>
</tr>
<tr>
<td>512</td>
<td>40,000</td>
<td>9,398,494</td>
<td>100</td>
<td>18,080</td>
<td>23,722</td>
<td>41,220</td>
</tr>
<tr>
<td>768</td>
<td>40,000</td>
<td>6,345,173</td>
<td>100</td>
<td>24,700</td>
<td>29,255</td>
<td>89,620</td>
</tr>
<tr>
<td>1024</td>
<td>40,000</td>
<td>4,789,269</td>
<td>100</td>
<td>31,160</td>
<td>35,450</td>
<td>79,100</td>
</tr>
<tr>
<td>1280</td>
<td>40,000</td>
<td>3,846,151</td>
<td>100</td>
<td>16,220</td>
<td>41,872</td>
<td>108,180</td>
</tr>
<tr>
<td>1518</td>
<td>40,000</td>
<td>3,250,972</td>
<td>100</td>
<td>31,380</td>
<td>47,686</td>
<td>109,240</td>
</tr>
</tbody>
</table>

**Affinity Summary**
1 TxRx queue per core
2 logical core on 2 physical core (SMT not used)

**Affinity Details**
- 0.0% Loss resolution
- ./l3fwd -c Ox -n 4 -- -p Ox -- -config="(0,0,2)(1,0,3)(2,0,2)(3,0,3)"
- 2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.2 Virtual Switching Performance (PHY-OVS-PHY)

Figure 7-3 shows test setup for PHY-OVS-PHY with four 10GbE ports.

Figure 7-3 Virtual Switching Performance test setup (PHY-OVS-PHY)

Virtual switching tests attempt to achieve aggregated system throughput of 40 Gbps using 4 ports to compare the following variables' configurations:

- Native OvS versus OvS with DPDK
- 1, 2, or 4 physical cores
- One flow per port (total four flows) or 1K flows per port (total 4K flows)
- Hyper-threading on or off
- 1 physical core vs 2 hyper-threaded cores
- 2 physical cores vs 4 hyper-threaded cores.
7.2.1 Native OvS and OvS with DPDK

This test compares the throughput performance of native OvS and OvS with DPDK for various packet sizes, assuming one flow per port and single physical core in use (hyper-threads enabled and disabled). The data shows that for small packet sizes throughput increases by over 10x with hyper-threading disabled and 11.7x with hyper-threading enabled using OvS with DPDK. However, with hyper-threading enabled, average latency is increased by 2 times for 64B packets comparing to results without hyper-threading, because the hyper-threaded cores are contending for cache-line access and causes thrashing to occur.

Table 7-3 PHY-OVS-PHY test configuration variables for native OvS and OvS with DPDK

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>Native OvS</td>
<td>4</td>
<td>1</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1</td>
<td>1</td>
<td>0, 2</td>
</tr>
</tbody>
</table>

Figure 7-4 Throughput performance of native OvS (with hyper-threading) and OvS with DPDK (without and with hyper-threading)
### Table 7-4 PHY-OVS-PHY test results for native OvS (1 core, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>748</td>
<td>1,113,190</td>
<td>2</td>
<td>57,913</td>
<td>5,940</td>
<td>8,463,600</td>
</tr>
<tr>
<td>72</td>
<td>864</td>
<td>1,174,039</td>
<td>2</td>
<td>56,699</td>
<td>5,860</td>
<td>13,334,820</td>
</tr>
<tr>
<td>128</td>
<td>1,483</td>
<td>1,252,418</td>
<td>4</td>
<td>55,136</td>
<td>4,540</td>
<td>9,330,820</td>
</tr>
<tr>
<td>256</td>
<td>2,566</td>
<td>1,162,006</td>
<td>6</td>
<td>57,058</td>
<td>6,160</td>
<td>8,477,940</td>
</tr>
<tr>
<td>512</td>
<td>4,422</td>
<td>1,038,999</td>
<td>11</td>
<td>59,589</td>
<td>6,340</td>
<td>5,849,020</td>
</tr>
<tr>
<td>768</td>
<td>6,278</td>
<td>995,915</td>
<td>16</td>
<td>60,711</td>
<td>6,340</td>
<td>4,966,380</td>
</tr>
<tr>
<td>1024</td>
<td>7,709</td>
<td>923,028</td>
<td>19</td>
<td>62,158</td>
<td>6,840</td>
<td>4,736,480</td>
</tr>
<tr>
<td>1280</td>
<td>9,604</td>
<td>923,472</td>
<td>24</td>
<td>63,741</td>
<td>7,320</td>
<td>5,405,040</td>
</tr>
<tr>
<td>1518</td>
<td>9,952</td>
<td>808,857</td>
<td>25</td>
<td>67,298</td>
<td>8,640</td>
<td>4,951,300</td>
</tr>
</tbody>
</table>

**Affinity Summary**: 1 TxRx queue per core  
1 logical core on 1 physical core (SMT not used)

**Affinity Details**: 0% Loss resolution  
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC  
Port0 IRQ's Affinity to lcore2  
Port1 IRQ's Affinity to lcore3  
Port2 IRQ's Affinity to lcore4  
Port3 IRQ's Affinity to lcore5

### Table 7-5 PHY-OVS-PHY test results for OvS with DPDK (1 core, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>7,825</td>
<td>11,644,358</td>
<td>20</td>
<td>25,486</td>
<td>10,200</td>
<td>123,240</td>
</tr>
<tr>
<td>72</td>
<td>8,560</td>
<td>11,630,128</td>
<td>21</td>
<td>19,968</td>
<td>8,820</td>
<td>76,700</td>
</tr>
<tr>
<td>128</td>
<td>13,780</td>
<td>11,638,931</td>
<td>34</td>
<td>27,153</td>
<td>12,560</td>
<td>84,600</td>
</tr>
<tr>
<td>256</td>
<td>25,653</td>
<td>11,618,095</td>
<td>64</td>
<td>21,489</td>
<td>13,440</td>
<td>78,340</td>
</tr>
<tr>
<td>512</td>
<td>40,000</td>
<td>9,398,496</td>
<td>100</td>
<td>37,271</td>
<td>16,800</td>
<td>57,840</td>
</tr>
<tr>
<td>768</td>
<td>40,000</td>
<td>6,345,178</td>
<td>100</td>
<td>28,412</td>
<td>13,320</td>
<td>55,000</td>
</tr>
<tr>
<td>1024</td>
<td>40,000</td>
<td>4,789,272</td>
<td>100</td>
<td>31,387</td>
<td>11,180</td>
<td>56,120</td>
</tr>
<tr>
<td>1280</td>
<td>40,000</td>
<td>3,846,154</td>
<td>100</td>
<td>25,843</td>
<td>10,260</td>
<td>51,420</td>
</tr>
<tr>
<td>1518</td>
<td>40,000</td>
<td>3,250,975</td>
<td>100</td>
<td>27,232</td>
<td>10,080</td>
<td>48,580</td>
</tr>
</tbody>
</table>

**Affinity Summary**: 1 TxRx queue per core  
1 logical core on 1 physical core (SMT not used)

**Affinity Details**: 1PMD thread based OVS and 0.0% Loss resolution  
`/ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4`  
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
Table 7-6 PHY-OVS-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>8,753</td>
<td>13,025,495</td>
<td>22</td>
<td>50,750</td>
<td>8,020</td>
<td>583,980</td>
</tr>
<tr>
<td>72</td>
<td>9,565</td>
<td>12,996,245</td>
<td>24</td>
<td>19,803</td>
<td>8,040</td>
<td>137,160</td>
</tr>
<tr>
<td>128</td>
<td>15,405</td>
<td>13,010,743</td>
<td>39</td>
<td>31,497</td>
<td>8,720</td>
<td>265,980</td>
</tr>
<tr>
<td>256</td>
<td>28,708</td>
<td>13,001,755</td>
<td>72</td>
<td>30,777</td>
<td>11,680</td>
<td>277,260</td>
</tr>
<tr>
<td>512</td>
<td>40,000</td>
<td>9,398,497</td>
<td>100</td>
<td>28,071</td>
<td>16,620</td>
<td>46,240</td>
</tr>
<tr>
<td>768</td>
<td>40,000</td>
<td>6,345,179</td>
<td>100</td>
<td>25,217</td>
<td>12,940</td>
<td>48,940</td>
</tr>
<tr>
<td>1024</td>
<td>40,000</td>
<td>4,789,273</td>
<td>100</td>
<td>23,473</td>
<td>11,760</td>
<td>42,720</td>
</tr>
<tr>
<td>1280</td>
<td>40,000</td>
<td>3,846,154</td>
<td>100</td>
<td>21,684</td>
<td>11,300</td>
<td>39,680</td>
</tr>
<tr>
<td>1518</td>
<td>40,000</td>
<td>3,250,976</td>
<td>100</td>
<td>20,290</td>
<td>10,940</td>
<td>39,280</td>
</tr>
</tbody>
</table>

Affinity Summary
1 TxRx queue per core
2 logical core on 1 physical core (SMT used)

Affinity Details
2PMD thread based OVS and 0.0% Loss resolution
./ovs-vctl set Open_vSwitch . other_config:pmd-cpu-mask=100004
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC

7.2.2 Performance Scaling of OvS with DPDK

Figure 7-5 shows throughput performance of processing 64-byte packets with 1, 2, and 4 physical cores and compares 4 flows (1 flow per port) with 4K total flows (1K flows per port). This data show fairly linear performance scaling as the number of cores is increased.

Today, due to the current configuration of OvS hash lookup tables, significant degradation in performance is observed when using more than 8K flows. This is related to the size of the EMC (exact match cache) which is a hash table in OvS. The current size of the EMC is set to 8K (flows) by default. Using this default configuration, a larger numbers of flows may result in packets using a slow data path.

Table 7-7 PHY-OVS-PHY test configuration variables for OvS with DPDK (scaling with physical cores)

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1, 1k</td>
<td>1, 2, 4</td>
<td>0</td>
</tr>
</tbody>
</table>
Figure 7-5 64-byte throughput performance scaling of OvS with DPDK for 1, 2, 4 cores, no hyper-threading

Table 7-8 PHY-OVS-PHY test results for OvS with DPDK (4 cores, 1k flows per port, HT off)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>23,255</td>
<td>34,605,824</td>
<td>58</td>
<td>10,868</td>
<td>7,380</td>
<td>131,320</td>
</tr>
<tr>
<td>72</td>
<td>25,885</td>
<td>35,169,672</td>
<td>65</td>
<td>12,833</td>
<td>8,160</td>
<td>157,540</td>
</tr>
<tr>
<td>128</td>
<td>40,000</td>
<td>33,783,761</td>
<td>100</td>
<td>65,189</td>
<td>14,180</td>
<td>118,380</td>
</tr>
<tr>
<td>256</td>
<td>40,000</td>
<td>18,115,943</td>
<td>100</td>
<td>17,872</td>
<td>9,920</td>
<td>37,420</td>
</tr>
<tr>
<td>512</td>
<td>40,000</td>
<td>9,398,497</td>
<td>100</td>
<td>16,555</td>
<td>8,640</td>
<td>34,860</td>
</tr>
<tr>
<td>768</td>
<td>40,000</td>
<td>6,345,178</td>
<td>100</td>
<td>17,696</td>
<td>8,380</td>
<td>34,260</td>
</tr>
<tr>
<td>1024</td>
<td>40,000</td>
<td>4,789,272</td>
<td>100</td>
<td>18,591</td>
<td>8,200</td>
<td>36,340</td>
</tr>
<tr>
<td>1280</td>
<td>40,000</td>
<td>3,846,154</td>
<td>100</td>
<td>18,183</td>
<td>7,980</td>
<td>40,900</td>
</tr>
<tr>
<td>1518</td>
<td>40,000</td>
<td>3,250,975</td>
<td>100</td>
<td>19,880</td>
<td>7,680</td>
<td>37,300</td>
</tr>
</tbody>
</table>

Affinity Details
4PMD thread based OvS and 0.0% Loss resolution
/ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3c
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.2.3 Performance with Hyper-Threading

The impact of hyper-threading on performance is illustrated below by comparing 64B throughput with 1 and 2 physical cores and hyper-threading turned on and off. There is 12% performance gain with 1 core and hyper-threading enabled, while 19% performance gain with 2 cores and hyper-threading enabled at 64B packets. With hyper-threading enabled, we gain the advantage of the throughput performance increment, but there is trade off in latency performance at 64B packets, with the observation of higher latency shown in section 7.2.1.

Table 7-9 PHY-OVS-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT)

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1, 1k</td>
<td>1, 2</td>
<td>0, 2, 4</td>
</tr>
</tbody>
</table>

![PHY-OVS-PHY OvS with DPDK Throughput Performance for 64B packet size with 40Gbps on Intel® Xeon® E5-2695 v4 (4 flows)](image)

Figure 7-6 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4 flows)
Figure 7-7 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows)
7.3 One VM Throughput (PHY-VM-PHY)

This test uses a single VM with two bidirectional flows across four 10 GbE ports as shown in Figure 7-8. Maximum theoretical platform throughput is 40Gbps (four flows aggregated).

Figure 7-8 One VM Throughput Performance test setup

Note: Four switching operations take place while packets are being routed through the system.

The single VM tests attempt to achieve aggregated system throughput of 40 Gbps using 4 ports to compare the following configurations for small packet sizes:

- L3 performance of native OvS versus OvS with DPDK
- L3 performance of OvS with DPDK
- Hyper-threading on or off (1 flow per port and 1k flows per port using 1 or 2 physical cores)
- Using 1 core, 2 cores, 4 cores (1 flow per port and 1k flows per port).

7.3.1 Native OvS and OvS with DPDK

This test compares the throughput performance of native OvS and OvS with DPDK.

In Figure 7-9 both native OvS and OvS with DPDK use one physical core. The relative performance ratio of OvS with DPDK is shown with and without hyper-threading.
Table 7-10 PHY-VM-PHY test configuration variables for native OvS and OvS with DPDK

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>Native OvS</td>
<td>4</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1</td>
<td>1</td>
<td>0.2</td>
</tr>
</tbody>
</table>

Figure 7-9 Throughput performance with 1 VM comparing native OvS and OvS with DPDK (using one physical core)

Table 7-11 shows measured small packet performance data for native OvS with hyper-threading on. Throughput performance is ~1% of line-rate at 64B packets. Table 7-12 shows measured small packet performance of OvS with DPDK also with one physical core (with hyper-threading). The throughput performance of OvS with DPDK is 7.4 times and 9.8 times greater than native OvS for 64B packets without and with hyper-threading respectively. There is a reduction of average latency at about 33% and 58% by using DPDK with OvS for 1 core configuration without and with hyper-threading respectively compared to native OvS at 64B packets.
Table 7-11 PHY-VM-PHY test results for native OvS (1 core, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Throughput with Zero Packet Loss</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>316 Mbps, 470,262 Packets/sec</td>
<td>1</td>
<td>141,000</td>
</tr>
<tr>
<td>256</td>
<td>799 Mbps, 362,054 Packets/sec</td>
<td>2</td>
<td>113,768</td>
</tr>
</tbody>
</table>

Affinity Details:
- 0% Loss resolution
- 2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
- Port0 IRQ’s Affinity to lcore2
- Port1 IRQ’s Affinity to lcore3
- Port2 IRQ’s Affinity to lcore4
- Port3 IRQ’s Affinity to lcore5
- On a VM: ./testpmd -c 0x6 -n 4 --burst=64 --txd=2048 --rxd=2048 --txqflags=0xf00

Table 7-12 PHY-VM-PHY test results for OvS with DPDK (1 core, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Throughput with Zero Packet Loss</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>2,334 Mbps, 3,472,608 Packets/sec</td>
<td>6</td>
<td>94,077</td>
</tr>
<tr>
<td>256</td>
<td>7,090 Mbps, 3,211,156 Packets/sec</td>
<td>18</td>
<td>84,118</td>
</tr>
</tbody>
</table>

Affinity Details:
- 1PMD thread based OvS and 0.0% Loss resolution
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4
- On a VM: ./testpmd -c 0x6 -n 4 --burst=64 --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
- 2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC

Table 7-13 PHY-VM-PHY test results for OvS with DPDK (1 core with HT, 1 flow per port)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Throughput with Zero Packet Loss</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>3,107 Mbps, 4,623,558 Packets/sec</td>
<td>8</td>
<td>59,170</td>
</tr>
<tr>
<td>256</td>
<td>9,333 Mbps, 4,226,995 Packets/sec</td>
<td>18</td>
<td>56,986</td>
</tr>
</tbody>
</table>

Affinity Details:
- 2PMD thread based OvS and 0.0% Loss resolution
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=400004
- On a VM: ./testpmd -c 0x6 -n 4 --burst=64 --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
- 2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
Figure 7-10 shows improved performance of OvS with DPDK using two physical cores instead of only one. This test illustrates the ability of OvS with DPDK to increase performance by using additional cores. For both 64B and 256B packets the performance with two cores is nearly twice as high as with one core.

![Figure 7-10](image)

**Figure 7-10** Throughput performance with a 1 VM comparing native OvS and OvS with DPDK using one and two physical cores (no hyper-threading)

Table 7-14 shows measured small packet performance data for OvS with DPDK using 2 physical cores.

### Table 7-14 OvS with DPDK, no hyper-threading (2 physical cores)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>L3 Forwarding — Bidirectional</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Throughput with Zero Packet Loss</td>
</tr>
<tr>
<td></td>
<td>Mbps</td>
</tr>
<tr>
<td>64</td>
<td>4,654</td>
</tr>
<tr>
<td>256</td>
<td>4,847</td>
</tr>
</tbody>
</table>

**Affinity Details**

2PMD thread based OvS and 0.0% Loss resolution

`. OVSVSctl set Open_vSwitch . other_config:pmd-cpu-mask=c

On a VM:

`. testpmd -c 0x6 -n 4 --burst=64 --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan

2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.3.2 Performance Scaling of OvS with DPDK

Table 7-15 PHY-VM-PHY test configuration variables for OvS with DPDK (scaling with physical cores)

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1, 1k</td>
<td>1, 2, 4</td>
<td>0</td>
</tr>
</tbody>
</table>

Figure 7-11 64-byte throughput performance scaling of OvS with DPDK - 1 VM (PHY-VM-PHY) with 1, 2, 4 cores, no hyper-threading

Table 7-16 PHY-VM-PHY test results for OvS with DPDK (1, 2, 4 cores, 1 flow per port, HT off)

<table>
<thead>
<tr>
<th>Number of cores</th>
<th>L3 Forwarding — Bidirectional</th>
<th>64B throughput with Zero Packet Loss</th>
<th>Average Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Mbps</td>
<td>Packets/sec</td>
</tr>
<tr>
<td>1</td>
<td>2,334</td>
<td>3,472,608</td>
<td>6</td>
</tr>
<tr>
<td>2</td>
<td>4,654</td>
<td>6,925,459</td>
<td>12</td>
</tr>
<tr>
<td>4</td>
<td>6,820</td>
<td>10,148,118</td>
<td>17</td>
</tr>
</tbody>
</table>

Affinity Details
1PMD, 2PMD, 4PMD thread based OvS and 0.0% Loss resolution
./ovs-vxctl set Open_vSwitch . other_config:pmd-cpu-mask=4 (1 core)
./ovs-vxctl set Open_vSwitch . other_config:pmd-cpu-mask=c (2 cores)
./ovs-vxctl set Open_vSwitch . other_config:pmd-cpu-mask=3c (4 cores)
On a VM:
./testpmd -c 0x6 -n 4 -- --burst=64 -i --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.3.3 Performance with Hyper-Threading

The test data in this section compares throughput performance with 1 VM using DPDK with and without hyper-threading (1 flow per port and 1k flows per port using 1 or 2 physical cores). There is about 33% and 61% gain in performance throughput for 1 core with hyper-threading enabled with 4 flows and 4k flows respectively. For 2 cores configuration, there is about 31% and 44% gain in performance throughput with hyper-threading enabled with 4 flows and 4k flows respectively. See Figure 7-12 and Figure 7-13.

Table 7-17 PHY-VM-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT)

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>OvS with DPDK</td>
<td>4</td>
<td>1, 1k</td>
<td>1, 2</td>
<td>0, 2, 4</td>
</tr>
</tbody>
</table>

Table 7-17 PHY-VM-PHY test configuration variables for OvS with DPDK and 64-byte packets (impact of HT)

Figure 7-12 Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4 flows, 1 VM)
PHY-VM-PHY OvS with DPDK Throughput Performance for 64B packet size with 40Gbps on Intel® Xeon® E5-2695 v4 (4K flows)

![Throughput Performance Graph](image)

**Figure 7-13** Impact of Hyper-threading on throughput performance of OvS with DPDK (64-byte packets, 1 and 2 cores, 4k flows, 1 VM)

Table 7-18 shows the measured performance data for 2 physical cores with hyper-threading off and on.

**Table 7-18 PHY-VM-PHY test results for OvS DPDK (2 physical cores)**

<table>
<thead>
<tr>
<th>Hyper-threading</th>
<th>64B throughput with Zero Packet Loss</th>
<th>Average Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Mbps</td>
<td>Packets/sec</td>
</tr>
<tr>
<td>1 flow per port</td>
<td>Off</td>
<td>4,654</td>
</tr>
<tr>
<td></td>
<td>On</td>
<td>6,085</td>
</tr>
<tr>
<td>1k flows per port</td>
<td>Off</td>
<td>3,687</td>
</tr>
<tr>
<td></td>
<td>On</td>
<td>5,311</td>
</tr>
</tbody>
</table>

**Affinity Details**

- 2PMD and 4PMD thread based OvS and 0.0% Loss resolution
- `./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C (HT off)`
- `./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=30000C (HT on)`
- On a VM: `./testpmd -c 0x6 -n 4 --burst=64 -i --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan`
- 2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.4 Two VM Throughput (PHY-VM-VM-PHY)

Figure 7-14 shows the VM-VM test setup with 2 x 10 GbE ports (maximum 20 Gbps aggregate throughput) with packets being forwarded from the first VM to the second VM (and in the opposite direction). In comparison with PHY-VM-PHY with 1 VM and with 2 x 10GbE ports, there is an additional switching operation occurs in between 2 VMs for PHY-VM-VM-PHY. Therefore, instead of 2 switching operations in PHY-VM-PHY (with 2 x 10GbE ports), PHY-VM-VM-PHY has 3 switching operations.

Figure 7-14 Two-VM Throughput performance test setup

Note: There are 3 switching operations taking place while packets are being routed through the system.

This test compares two VM throughput with DPDK using 1 flow per port in each direction (total 2 flows).

Table 7-19 PHY-VM-VM-PHY test configuration variables for OvS with DPDK

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>OvS with DPDK</td>
<td>2</td>
<td>1</td>
<td>1, 2, 4</td>
<td>0, 2, 4</td>
</tr>
</tbody>
</table>

Figure 7-15 shows packet throughput with 2 flows (one flow per port in each direction) using 1 physical core, 1 hyper-threaded core, 2 physical cores, 2 hyper-threaded cores and 4 physical cores.
Figure 7-15 Two-VM Throughput (PHY-VM-VM-PHY) with 2 ports and 2 Flows

Table 7-20 shows throughput and latency data for each packet size for 4 hyper-threaded cores. Further scaling can be achieved by increasing number of cores.

Table 7-20 PHY-VM-VM-PHY test results for OvS with PDK (2 cores and 4 threads)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>64</td>
<td>2,733</td>
<td>4,066,977</td>
<td>14</td>
<td>39,002</td>
<td>15,860</td>
<td>67,060</td>
</tr>
<tr>
<td>72</td>
<td>2,810</td>
<td>3,818,416</td>
<td>14</td>
<td>37,769</td>
<td>16,680</td>
<td>69,780</td>
</tr>
<tr>
<td>128</td>
<td>4,589</td>
<td>3,876,062</td>
<td>23</td>
<td>39,601</td>
<td>16,920</td>
<td>71,680</td>
</tr>
<tr>
<td>256</td>
<td>8,166</td>
<td>3,698,551</td>
<td>41</td>
<td>114,450</td>
<td>16,960</td>
<td>144,780</td>
</tr>
<tr>
<td>512</td>
<td>14,373</td>
<td>3,377,171</td>
<td>72</td>
<td>109,136</td>
<td>16,060</td>
<td>142,000</td>
</tr>
<tr>
<td>768</td>
<td>16,887</td>
<td>2,678,759</td>
<td>84</td>
<td>105,818</td>
<td>16,120</td>
<td>135,020</td>
</tr>
<tr>
<td>1024</td>
<td>19,246</td>
<td>2,304,343</td>
<td>96</td>
<td>102,703</td>
<td>16,960</td>
<td>150,200</td>
</tr>
<tr>
<td>1280</td>
<td>20,000</td>
<td>1,923,075</td>
<td>100</td>
<td>80,966</td>
<td>17,420</td>
<td>99,540</td>
</tr>
<tr>
<td>1518</td>
<td>20,000</td>
<td>1,625,487</td>
<td>100</td>
<td>60,980</td>
<td>17,420</td>
<td>78,300</td>
</tr>
</tbody>
</table>

Affinity Details:
4PMD thread based OvS and 0.0% Loss resolution
./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=30000c
On a VM: ./testpmd -c 0x6 -n 4 --burst=64 -i --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
2 Quad Port Intel® X710-DA4 NICs; 2 ports in use per each NIC
7.5 40 Gbps Switching Performance

The goal of the tests is to determine the number of cores (number of PMD threads) to achieve 40 Gbps switching performance. Three different test scenarios were conducted to evaluate the number of OvS-DPDK PMD threads. Test Scenario 1 is depicted in Figure 7-16. A total of 8 cores are needed to achieve 40 Gbps switching performance for test scenario 1 at 256 bytes packet size. Two cores are used for the switching, while 6 additional cores are used for passing the data to the VMs (i.e. for use by vHost and associated PMD). In this scenario each VM is transmitting and receiving 5 Gbps. The traffic over the wire is 20 Gbps transmit and 20 Gbps receive. Hence the OvS is switching 40 Gbps when accounting for the bidirectional nature of the traffic.

Figure 7-16 40 Gbps switching performance – test scenario 1

Test Scenario 2 is depicted in Figure 7-17. A total of 8 cores are needed to achieve 40 Gbps switching performance for test scenario 2 at 256 bytes packet size. This scenario is similar to Scenario 1, but some of the traffic is being switched between VM2 and VM3. The vSwitch is still doing 40 Gbps of switching as before, but with the additional VM to VM traffic, this will limit the throughput to 4 Gbps for each set of flows, with 16 Gbps on the wire.
Test Scenario 3 is depicted in Figure 7-18. A total of 8 cores are needed to achieve 40 Gbps switching performance for test scenario 3 at 256 bytes packet size. This scenario is for a simple service function chain of two VMs.
Figure 7-19 below shows results of core scaling for the 40 Gbps switching performance test with the use of three aforementioned test scenarios.

![OVS-DPDK 40Gbps Switching Performance for 256 bytes packets](image)

Figure 7-19 OvS with DPDK - 40 Gbps switching performance for 256B packets

Table 7-21 OvS with DPDK – 40 Gbps switching performance for 256B packets

<table>
<thead>
<tr>
<th>Number of cores</th>
<th>Scenario 1</th>
<th>Scenario 2</th>
<th>Scenario 3</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Mbps</td>
<td>Packets/sec</td>
<td>Mbps</td>
</tr>
<tr>
<td>2</td>
<td>21,012</td>
<td>9,516,358</td>
<td>18,228</td>
</tr>
<tr>
<td>5</td>
<td>28,824</td>
<td>13,054,274</td>
<td>30,100</td>
</tr>
<tr>
<td>6</td>
<td>33,464</td>
<td>15,156,012</td>
<td>32,498</td>
</tr>
<tr>
<td>8</td>
<td>40,000</td>
<td>18,115,950</td>
<td>38,724</td>
</tr>
</tbody>
</table>

Affinity Details
- 2PMD, 5PMD, 6PMD and 8PMD thread based OvS and 0.0% Loss resolution
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=c (2 cores)
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=7c (5 cores)
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=807c (6 cores)
- ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=807c (8 cores)

On a VM:
- ./testpmd -c 0x6 -n 4 -- --burst=64 -i --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
- 2 ports from 1x Quad-Port Intel® X710-DA4 NIC
7.6 VXLAN Performance (PHY-OVS-VM-OVS-PHY)

This test case investigates performance of VXLAN ([https://tools.ietf.org/html/rfc7348](https://tools.ietf.org/html/rfc7348)) using native Open vSwitch* and Open vSwitch* with DPDK. The performance data provides a baseline for scenarios using VXLAN Tunnel End Points (VTEPs) in the vSwitch and establishes a test methodology for future comparisons. The test data here compare VXLAN performance for Native OvS and OvS with DPDK as well as VXLAN performance overhead for Native OvS and OvS with DPDK. The methodology described here attempts to emulate the scenario in Figure 7-20 that shows a VXLAN scenario using 1 x 10GbE port (maximum 10 Gbps aggregate throughput using two flows). VXLAN de-encapsulation and encapsulation processing occurs in the vSwitch VTEP.

![VXLAN Scenario with 1 Physical Port and VTEP in the vSwitch](image)

**Figure 7-20 VXLAN Scenario with 1 Physical Port and VTEP in the vSwitch**

### 7.6.1 VXLAN Test Methodology

In this test methodology, 1 port is connected to IXIA. IXIA generates packets with VXLAN header and send the VXLAN packets to the host (device under test). VTEP running inside the host de-encapsulates the packet received and send IPv4 packet to the VM and packets received from VM are encapsulated in VXLAN header by VTEP and sent them back to the IXIA.
The following steps show the flow of packets:

1. IXIA generates VXLAN packets.
2. VXLAN packet is received by VTEP running inside the Host.
3. Host de-encapsulates the packet and sends it to VM.
4. VM forwards the packet to the VTEP.
5. VTEP running inside the Host encapsulates the packet by adding VXLAN header.
6. Packet with VXLAN header is sent to IXIA.
7. VXLAN encapsulation adds 50 byte header to each packet.

7.6.2 OvS and OvS with DPDK

VXLAN tests attempt to achieve system throughput of 10 Gbps using 1 physical ports and 1 unidirectional flow (see Table 7-22). Performance data shows comparison between:

- Native OvS and OvS with DPDK
- OvS with DPDK when using 1 and 2 physical cores.

Figure 7-21 shows VXLAN performance comparing native OvS and OvS with DPDK for 1 core and 2 cores configurations for all packet sizes. Throughput and latency data for 2 cores and hyper-threading off are provided in Table 7-23.

Table 7-22 VXLAN test configuration variables for native OvS and OvS with DPDK

<table>
<thead>
<tr>
<th>Configuration variables</th>
<th>Ports</th>
<th>Flows per Port in each Direction</th>
<th>Physical Cores</th>
<th>Hyper-threaded cores</th>
</tr>
</thead>
<tbody>
<tr>
<td>Native OvS</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>OvS with DPDK</td>
<td>1</td>
<td>1</td>
<td>1, 2</td>
<td>0</td>
</tr>
</tbody>
</table>

Figure 7-21 VXLAN Performance for 64-byte packets comparing native OvS and OvS with DPDK
Table 7-23 VXLAN Packet Throughput with 2 cores (hyper-threading off)

<table>
<thead>
<tr>
<th>Packet Size</th>
<th>Mbps</th>
<th>Packets/sec</th>
<th>% Line Rate</th>
<th>Average Latency (ns)</th>
<th>Minimum Latency (ns)</th>
<th>Maximum Latency (ns)</th>
</tr>
</thead>
<tbody>
<tr>
<td>114</td>
<td>3,929</td>
<td>3,664,660</td>
<td>39</td>
<td>30,167</td>
<td>14,220</td>
<td>76,200</td>
</tr>
<tr>
<td>122</td>
<td>4,035</td>
<td>3,551,815</td>
<td>40</td>
<td>32,956</td>
<td>11,700</td>
<td>76,720</td>
</tr>
<tr>
<td>178</td>
<td>5,533</td>
<td>3,493,315</td>
<td>55</td>
<td>32,582</td>
<td>13,180</td>
<td>74,760</td>
</tr>
<tr>
<td>306</td>
<td>8,492</td>
<td>3,256,060</td>
<td>85</td>
<td>35,577</td>
<td>13,520</td>
<td>75,620</td>
</tr>
<tr>
<td>562</td>
<td>10,000</td>
<td>2,147,766</td>
<td>100</td>
<td>33,330</td>
<td>19,060</td>
<td>49,600</td>
</tr>
<tr>
<td>818</td>
<td>10,000</td>
<td>1,491,647</td>
<td>100</td>
<td>23,799</td>
<td>13,620</td>
<td>36,440</td>
</tr>
<tr>
<td>1074</td>
<td>10,000</td>
<td>1,142,596</td>
<td>100</td>
<td>21,110</td>
<td>13,080</td>
<td>33,380</td>
</tr>
<tr>
<td>1330</td>
<td>10,000</td>
<td>925,926</td>
<td>100</td>
<td>21,455</td>
<td>14,060</td>
<td>37,880</td>
</tr>
<tr>
<td>1468</td>
<td>10,000</td>
<td>840,054</td>
<td>100</td>
<td>19,743</td>
<td>12,920</td>
<td>32,500</td>
</tr>
</tbody>
</table>

Affinity Details
2PMD threads based OVS and 0% Loss resolution
/ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C
1 port from 1x Quad-Port Intel® X710-DA4 NIC

Figure 7-22 shows the impact of VXLAN overhead on the throughput compared to the PHY-VM-PHY test results using Native OvS and OvS with DPDK for small packet size (64 bytes). There is approximately 70% VXLAN performance overhead in Native OvS. Using OvS with DPDK the VXLAN performance overhead is significantly reduced: 39% VXLAN performance overhead with single core and ~47% VXLAN performance overhead with two cores are observed.

Figure 7-22 Impact of VXLAN overhead on processing performance in the PHY-VM-PHY test case for 64B packets
8.0 Industry Benchmarks

8.1 ETSI NFV

The European Telecommunications Standards Institute (ETSI) NFV (Phase II) is developing test methodologies and test specifications relevant to performance testing. Certain draft specification documents are available publically here: https://docbox.etsi.org/ISG/NFV/Open/Drafts/. This includes a “NFV Pre-Deployment Validation” specification with the following:

- Test methods for pre-deployment validation:
  - Validating physical DUTs and systems-under-test:
    - Data plane validation
    - Control plane validation
    - Management plane validation
  - Impact of virtualization on test methods
  - Considerations on choice of virtualized versus hardware based test appliances
- Pre-deployment validation of NFV infrastructure
- Pre-deployment validation of VNFs:
  - VNF life-cycle testing:
    - VNF instantiation testing
    - VNF termination
  - VNF data plane benchmarking
- Pre-deployment validation of network services
- Reliability & resiliency requirements
- Security considerations.

8.2 IETF

The Benchmark Working Group (BMWG) is one of the longest-running working groups in IETF. This group was re-chartered in 2014 to include benchmarking for virtualized network functions (VNFs) and their infrastructure. An active Internet draft, "Considerations for Benchmarking Virtual Network Functions and Their Infrastructure," is available here: https://tools.ietf.org/html/draft-ietf-bmwg-virtual-net-00. Many RFCs referenced originated in the BMWG, including foundational RFC 1242 and RFC 2544:

- RFC 1242 Benchmarking Terminology for Network Interconnection Devices
- RFC 2544 Benchmarking Methodology for Network Interconnect Devices
- RFC 2285 Benchmarking Terminology for LAN Switching Devices
- RFC 2889 Benchmarking Methodology for LAN Switching Devices
- RFC 3918 Methodology for IP Multicast Benchmarking
- RFC 4737 Packet Reordering Metrics
- RFC 5481 Packet Delay Variation Applicability Statement
- RFC 6201 Device Reset Characterization
8.3 Open Platform for NFV (OPNFV)

OPNFV ([https://wiki.opnfv.org](https://wiki.opnfv.org)) is a carrier-grade, integrated, open-source platform to accelerate the introduction of new NFV products and services. As an open-source project, OPNFV aims to bring together the work of standards bodies, open-source communities, and commercial suppliers to deliver a de facto open-source NFV platform for the industry. By integrating components from upstream projects, the community can conduct performance and use case-based testing to ensure the platform's suitability for NFV use cases.

Figure 8-1 illustrates the wide variety of performance test projects currently in OPNFV (this is a snapshot and evolving rapidly) and includes:

- infrastructure KPI verification
- platform performance benchmarking
- vSwitch performance
- system bottlenecks
- storage performance benchmarking
- controller performance.

For more information on test projects for OPNFV upcoming release (Brahmaputra) refer to OPNFV Wiki: [https://wiki.opnfv.org/brahmaputra_testing_page](https://wiki.opnfv.org/brahmaputra_testing_page).

Figure 8-1 Snapshot of OPNFV Test projects and Infrastructure


The Internet draft describes the progress of the OPNFV project on vSwitch performance with additional considerations when vSwitches are implemented in general-purpose hardware. The project intends to build on the current and completed work of the Benchmarking Working Group in IETF.
9.0 Performance Tuning

9.1 Tuning Methods

There are a few important tuning methods that can improve throughput performance for PHY-PHY, PHY-VM, and VM-VM test cases:

- CPU core isolation for OvS-DPDK
- HugePage size 1 GB
- CPU core affinity for ovs-vswitchd and OvS PMD threads
- CPU core affinity for the VM (qemu-kvm)

This section provides some fundamental optimization and tunings for the OvS with DPDK setup. Refer to https://github.com/openvswitch/ovs/blob/master/INSTALL.DPDK.md#performance-tuning for more information on tuning-related optimization.

9.2 CPU Core Isolation for OvS-DPDK

While the threads used by OvS are pinned to logical cores on the system, the Linux scheduler can also run other tasks on those cores. To help prevent additional workloads from running on them, the isolcpus Linux* kernel parameter can be used to isolate the cores from the general Linux scheduler. Add the isolcpus Linux* parameter in the Linux boot kernel of the host machine. For example, if the OvS vswitchd and qemu-kvm process are to run on logical cores 2, 4, and 6, the following should be added to the kernel parameter list:

isolcpus=2,4,6

9.3 HugePage Size 1 GB

HugePage support is required for the large-memory pool allocation used for packet buffers. By using HugePage allocations, performance is increased because fewer pages are needed, and therefore less translation lookaside buffers (TLBs, high-speed translation caches). This reduces the time it takes to translate a virtual page address to a physical page address. Without HugePages, high TLB miss rates would occur with the standard 4K page size, slowing performance.

The allocation of HugePages should be done at boot time or as soon as possible after system boot to prevent memory from being fragmented in physical memory. To reserve HugePages at boot time, a parameter is passed to the Linux* kernel on the kernel command line. For example, to reserve 16G of HugePage memory in the form of 16 1G pages, the following options should be passed to the kernel:

default_hugepagesz=1G hugepagesz=1G hugepages=16

Note: For 1G HugePages, it is not possible to reserve the HugePage memory after the system has booted.

After the machine is up and running, mount the huge table file system:

```
# mount -t hugetlbfs -o pagesize=1G none /dev/hugepages
```
9.4 CPU Core Affinity for ovs-vswitchd and OvS PMD Threads

With PMD multi-threading support, OvS creates one PMD thread for each NUMA node as default. The PMD thread handles the I/O of all DPDK interfaces on the same NUMA node. The following command can be used to configure the multi-threading behavior:

```
# ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=<hex string>
```

The above command asks for a CPU mask for setting the affinity of PMD threads. A set bit in the mask means a PMD thread is created and pinned to the corresponding CPU core. Ideally, for maximum throughput, the PMD thread should not be scheduled out, which temporarily halts its execution. Therefore, with the CPU core isolation being on the host machine during boot time, the CPU-isolated cores will be used to set the affinity of the PMD threads. For example, to configure PMD threads on core 2 and 3 using 'pmd-cpu-mask':

```
# ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C
```

Check that the OvS PMD thread is set to the correct CPU1 and ovs-vswitchd threads are set to CPU2 and CPU3 using this command:

```
# top -p `pidof ovs-vswitchd` -H -d1
```

**Note:** The PMD threads on a NUMA node are created only if there is at least one DPDK interface from the NUMA node that has been added to OvS. To understand where most of the time is spent and whether the caches are effective, these commands can be used:

```
# ovs-appctl dpif-netdev/pmd-stats-clear #To reset statistics
# ovs-appctl dpif-netdev/pmd-stats-show
```

9.5 CPU Core Affinity for the Virtual Machine (qemu-kvm)

When configuring a PHY-VM test environment, it is important to set the CPU core affinity for the virtual machine (VM). Depending on the number of cores being assigned to the VM, the CPU core affinity should be set according to the QEMU threads. For example, to configure a VM with 4 cores, start the VM on CPU 4-6 (0x70):

```
# taskset 70 qemu-system-x86_64 -m 4096 -smp 4 -cpu host -hda /root/vm-images/vm-fc21.img -boot c -enable-kvm -pidfile /tmp/vm1.pid -monitor unix:/tmp/vm1monitor,server,nowait -name 'FC21-VM1' -net none -no-reboot -
```
Intel® Open Network Platform Release 2.1
Performance Test Report

object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on -numa node,memdev=mem -mem-prealloc -net none 
-chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 
-netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-net-pci,netdev=net1,mac=00:00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mg_rxbuf=off 
-chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 
-netdev type=vhost-user,id=net2,chardev=char2,vhostforce -device virtio-net-pci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mg_rxbuf=off
--nographic -vnc :14

Once the VM is running, there will be multiple QEMU threads that are spawned running on the host. Check the main QEMU thread process ID (PID) to track the spawned threads:

```
# ps -e |grep qemu
2511 pts/3    22:27:53 qemu-system-x86
```

Use the `top` command to provide a list of the main and child process QEMU threads. The main QEMU thread PID 2511 is always active with utilization close to 100% of CPU:

```
# top -p 2511 -H -d1
```

Then, use `htop` to check the % CPU usage in runtime for each QEMU child thread and determine the active QEMU threads:

```
# htop -p 2520,2511,2518,2519,2521,2523
```
Output:

From the htop output screen, you can view two active QEMU threads that have a high CPU usage. In this example, PID 2511 and PID 2520 (screen output) are using 100% CPU. We have to set these two active threads to specific CPU logical cores. We are going to set PID 2511 to CPU4 (0x10), and PID 2520 to CPU 5 (0x20). The other 4 threads (PID: 2518, 2519, 2521, 2523) are going to be set to CPU6 (0x40).

It is important to assign each active (100% CPU) QEMU thread to separate CPU cores to sustain good optimal throughput performance. If the active QEMU threads do not use core-affinity, the overall throughput performance is impacted.

9.6 Troubleshooting Tips for OvS

In the OvS controller, there are a few management tools in ovs-vswitchd that are useful to monitor the status of ports and OpenFlow activities:

- ovs-vsctl manages the switch through interaction with ovsdb-server.
- ovs-ofctl is a management utility for OpenFlow.
- ovs-appctl is a utility for managing logging levels.

After creating and configuring the ports, the ovs-vsctl command tool is useful to check the overall view of the bridges and ports created in the ovsdb-server database:

```
# ovs-vsctl show
7bdd3285-c5db-4944-b963-3ecedf661a41
Bridge "br0"
  Port "br0"
    Interface "br0"
      type: internal
  Port "dpdk0"
    Interface "dpdk0"
      type: dpdk
  Port "dpdk1"
    Interface "dpdk1"
      type: dpdk
```

The ovs-ofctl command tool is useful to check the OpenFlow flow configuration and port statistics. To check port information on a particular bridge, such as the port's media access control (MAC) address and number, ovs-ofctl show <bridge-name> or ovs-ofctl dump-ports-desc <bridge-name> provides the following information on all ports:

```
OFPT_FEATURES_REPLY (xid=0x2): dpid:0000001b21a272e4
```
n_tables:254, n_buffers:256

capabilities: FLOW_STATS TABLE_STATS PORT_STATS QUEUE_STATS ARP_MATCH_IP
actions: output enqueue set_vlan_vid set_vlan_pcp strip_vlan mod_dl_src mod_dl_dst mod_nw_src mod_nw_dst mod_nw_tos mod_tp_src mod_tp_dst

1(dpdk0): addr:00:1b:21:a2:72:e4
  config: 0
  state: LINK_DOWN
  current: AUTO_NEG
  speed: 0 Mbps now, 0 Mbps max

2(vxlan0): addr:c2:7a:99:d6:01:e2
  config: 0
  state: 0
  speed: 0 Mbps now, 0 Mbps max

LOCAL(br-int): addr:00:1b:21:a2:72:e4
  config: 0
  state: 0
  current: 10MB-FD COPPER
  speed: 10 Mbps now, 0 Mbps max

When the test is running, you can monitor packets sending and receiving at the ports configured in OvS by checking the flow and port statistics. For example, if you want to check if the packets are being received and sent in a flow, ovs-ofctl dump-flows <bridge-name> prints all the configured flow statistics. The figure below shows the flows configured for sending and receiving exist and are being used with n_packets equal to non-zero.

```
# /root/ovs/utilities/ovs-ofctl dump-flows br-int
NXST_FLOW reply (xid=0x4): cookie=0x0, duration=177593.242s, table=0, n_packets=1300667542, n_bytes=78040052520, idle_age=65534, hard_age=65534, ip,in_port=2 actions=output:1
```

The ovs-ofctl dump-ports <bridge-name> command prints port statistics for RX/TX packets, packets that are dropped, and packet errors (if they occur). In this example, there are packet errors in port 1. One of the reasons may be that the packet rate being received at port 1 is too high and beyond the port's capacity. The packet sending rate to the port, therefore, needs to be reduced to fix the packet error. If there is a packet drop in the OvS, check the CPU core affinity assignment for the QEMU threads for the PHY-VM test case, and if the HugePage size is set correctly, and the ovs-vsswitchd and OvS PMD threads are running on isolated cores.

```
# /root/ovs/utilities/ovs-ofctl dump-ports br-int
OFPST_PORT reply (xid=0x2): 3 ports
  port 2: rx_pkts=0, bytes=0, drop=0, errs=0, frame=0, over=0, crc=0
    tx_pkts=8, bytes=648, drop=0, errs=0, coll=0
  port 1: rx_pkts=578932881, bytes=37051704384, drop=0, errs=176810889, frame=0, over=0, crc=0
    tx_pkts=1300667551, bytes=83242723450, drop=0, errs=0, coll=0
  port LOCAL: rx_pkts=14, bytes=1156, drop=0, errs=0, frame=0, over=0, crc=0
    tx_pkts=0, bytes=0, drop=0, errs=0, coll=0
```

To check the Address Resolution Protocol (ARP) cache content, ovs-appctl tnl/arp/show prints the learned MAC address and IP address.

```
# /root/ovs/utilities/ovs-appctl tnl/arp/show
IP       MAC           Bridge
---------------------------------------------
2.2.2.1   00:1b:21:a2:72:e5   br0
2.2.2.2   00:1b:21:a2:72:e6   br0
```
10.0 OvS Test Setup

10.1 Configure the Host Machine

1. Stop and disable the interruption request (IRQ) balance:
   
   ```
   # killall irqbalance
   # systemctl stop irqbalance.service
   # systemctl disable irqbalance.service
   ```

2. Stop and disable the Firewall and iptables:
   
   ```
   # systemctl stop firewalld.service
   # systemctl disable firewalld.service
   # systemctl stop iptables.service
   ```

3. Disable Security-enhanced Linux (SELinux):
   
   ```
   [root@localhost ~]# vi /etc/selinux/config
   SELINUX=disabled
   ```

4. Disable address space layout randomization:
   
   ```
   # echo "# Disable Address Space Layout Randomization (ASLR)" > /etc/sysctl.d/ aslr.conf
   # echo "kernel.randomize_va_space=0" >> /etc/sysctl.d/aslr.conf
   ```

5. Disable IPv4 forwarding:
   
   ```
   # echo "# Enable IPv4 Forwarding" > /etc/sysctl.d/ip_forward.conf
   # echo "net.ipv4.ip_forward=0" >> /etc/sysctl.d/ip_forward.conf
   
   # systemctl restart systemd-sysctl.service
   # cat /proc/sys/kernel/randomize_va_space
   0
   # cat /proc/sys/net/ipv4/ip_forward
   0
   ```

6. Remove the following modules:
   
   ```
   # rmmod ipmi_msghandler
   # rmmod ipmi_si
   # rmmod ipmi_devintf
   ```

10.2 Set the Kernel Boot Parameters

1. With hyper-threading enabled, add the following to the kernel boot parameters /etc/default/grub:
   
   ```
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=16 hugepagesz=2M hugepages=2048
intel_iommu=off isolcpus=1-17,19-35 rhgb quiet"
   ```

2. With hyper-threading disabled, add the following to the kernel boot parameters /etc/default/grub:
   
   ```
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=16 hugepagesz=2M hugepages=2048
intel_iommu=off isolcpus=1-17 rhgb quiet"
   ```
3. Save the file and update the GRUB config file:
   
   ```bash
   # grub2-mkconfig -o /boot/grub2/grub.cfg
   ```

4. Reboot the host machine and check to make sure 1GB and 2MB HugePage sizes are created. You should see 16 1GB HugePages and 2048 2MB HugePages:
   
   ```bash
   # ls /sys/devices/system/node/node0/hugepages/hugepages-*
   hugepages-1048576kB/  hugepages-2048kB/
   # cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
   16
   # cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
   2048
   ```

### 10.3 Compile DPDK 2.2

1. Go to the DPDK-2.2.0 directory and run the following:
   
   ```bash
   # make install T=x86_64-native-linuxapp-gcc
   # cd x86_64-ivshmem-linuxapp-gcc
   ```

2. Edit the config file (`vim .config`) and set the configuration options:
   
   ```
   CONFIG_RTE_BUILD_COMBINE_LIBS=y
   CONFIG_RTE_LIBRTE_VHOST=y
   CONFIG_RTE_LIBRTE_VHOST_USER=y
   ```

3. Save the config file and run `make`:
   
   ```bash
   # EXTRA_CFLAGS="-g -Ofast"
   # make
   ```

### 10.4 Install OvS

1. Go to the OvS directory and run:
   
   ```bash
   # ./boot.sh
   # ./configure --with-dpdk=/root/dpdk-2.2.0/x86_64-native-linuxapp-gcc \ 
   CFLAGS="-Ofast -g"
   # make 'CFLAGS=-g -Ofast'
   ```

### 10.5 Prepare to Start OvS

1. Mount the 1GB HugePage and 2MB HugePage:
   
   ```bash
   # mkdir -p /mnt/huge
   # mkdir -p /mnt/huge_2mb
   # mount -t hugetlbfs nodev /mnt/huge
   # mount -t hugetlbfs nodev /mnt/huge_2mb -o pagesize=2MB
   ```

2. Check that HugePages are mounted:
   
   ```bash
   # mount
   nodev on /mnt/huge type hugetlbfs (rw,relatime)
   ```
3. Remove the following Linux modules and load the modules for OvS:

```bash
# rmmod ixgbe
# rmmod igb_uio
# rmmod cuse
# rmmod openvswitch
# rmmod uio
# rmmod eventfd_link
# rmmod ioeventfd
# rm -rf /dev/vhost-net
# modprobe uio
# insmod $DPDK_BUILD/kmod/igb_uio.ko
```

4. Check the PCI ID for the 10GbE NIC ports:

```bash
# lspci | grep Ethernet
```

```
01:00.0 Ethernet controller: Intel Corporation Ethernet Controller X710 for 10GbE SFP+ (rev 01)
01:00.1 Ethernet controller: Intel Corporation Ethernet Controller X710 for 10GbE SFP+ (rev 01)
01:00.2 Ethernet controller: Intel Corporation Ethernet Controller X710 for 10GbE SFP+ (rev 01)
01:00.3 Ethernet controller: Intel Corporation Ethernet Controller X710 for 10GbE SFP+ (rev 01)
```

### 10.6 Bind 10 GbE NIC Ports to the igb_uio Driver

1. To create a 4-port configuration:

```bash
# python $DPDK_DIR/tools/dpkNic_bind.py --bind=igb_uio 01:00.0
# python $DPDK_DIR/tools/dpkNic_bind.py --bind=igb_uio 01:00.1
# python $DPDK_DIR/tools/dpkNic_bind.py --bind=igb_uio 01:00.2
# python $DPDK_DIR/tools/dpkNic_bind.py --bind=igb_uio 01:00.3
# python $DPDK_DIR/tools/dpkNic_bind.py --status
```

Network devices using the DPDK-compatible driver:
```
============================================
0000:01:00.0 'Ethernet Controller X710 for 10GbE SFP+' drv=igb_uio unused=i40e
0000:01:00.1 'Ethernet Controller X710 for 10GbE SFP+' drv=igb_uio unused=i40e
0000:01:00.2 'Ethernet Controller X710 for 10GbE SFP+' drv=igb_uio unused=i40e
0000:01:00.3 'Ethernet Controller X710 for 10GbE SFP+' drv=igb_uio unused=i40e
```

Network devices using the kernel driver:
```
===================================
0000:00:19.0 'Ethernet Connection I217-LM' if=enp0s25 drv=e1000e unused=igb_uio
*Active*
0000:05:00.0 'I210 Gigabit Network Connection' if=enp5s0 drv=igb unused=igb_uio
```

Other network devices:
10.7 Remove and Terminate Previous-Run OvS and Prepare

```
# pkill -9 ovs
# rm -rf /usr/local/var/run/openvswitch
# rm -rf /usr/local/etc/openvswitch/
# rm -f /tmp/conf.db
# mkdir -p /usr/local/etc/openvswitch/
# mkdir -p /usr/local/var/run/openvswitch
```

10.1 Initialize the New OvS Database

1. Initialize the new OvS database:
   ```
   # export OVS_DIR=/root/OVS/ovs
   # cd $OVS_DIR
   # ./ovsdb/ovsdb-tool create /usr/local/etc/openvswitch/conf.db \
   #   ./vswitchd/vswitch.ovsschema
   ```

2. Start the database server:
   ```
   # ./ovsdb/ovsdb-server --remote=punix:/usr/local/var/run/openvswitch/db.sock \
   #   --remote=db:Open_vSwitch,Open_vSwitch,manager_options \
   #   --pidfile --detach
   ```

3. Initialize the OvS database:
   ```
   # ./utilities/ovs-vsctl --no-wait init
   ```

10.2 Start OvS-vSwitchd

1. Start OvS with DPDK portion using 2GB on CPU2 (0x2):
   ```
   # ./vswitchd/ovs-vswitchd --dpdk -c 0x2 -n 4 --socket-mem 2048 \
   #   -- unix:/usr/local/var/run/openvswitch/db.sock --pidfile
   ```

10.3 Tune OvS-vswitchd

You can check the thread siblings list (when hyper-threading is enabled) with the following:

```
# cat /sys/devices/system/cpu/cpuN/topology/thread_siblings_list
```

Based on the core thread siblings, you can set/check the PMD mask so that the multiple logical cores are on the same physical core.
1 PMD Configuration

1. Set the default OvS PMD thread usage to CPU2 (0x4):
   
   ```
   # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4
   # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
   ```

2 PMD Configuration

1. For 1 physical core, 2 logical cores (2 PMDs) on a system with HT enabled, check the thread siblings:
   
   ```
   # cat /sys/devices/system/cpu/cpu1/topology/thread_siblings_list
   2,9
   ```

   2. Then set the pmd-cpu-mask to CPU2 and CPU10 (0x404):
      
      ```
      # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=404
      # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
      ```

3. For 2 physical cores and 2 logical cores (2 PMDs) on system HT disabled, set the default OvS PMD thread usage to CPU2 and CPU3 (0x3C):
   
   ```
   # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3C
   # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
   ```

4 PMD Configuration

1. For 2 physical cores, 2 logical cores (4 PMDs) on system with HT enabled, check the thread siblings:
   
   ```
   # cat /sys/devices/system/cpu/cpu2/topology/thread_siblings_list
   2,9
   # cat /sys/devices/system/cpu/cpu3/topology/thread_siblings_list
   3,10
   ```

   2. Then set the pmd-cpu-mask to CPU2, CPU3, CPU10, and CPU11 (0x2C0).
      
      ```
      # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C0C
      # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
      ```

3. For 4 physical cores (4 PMDs) on system HT disabled, set the default OvS PMD thread usage and set the default OvS PMD thread usage to CPU2, CPU3, CPU4, and CPU5 (0x3C):
   
   ```
   # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=3C
   # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
   ```

### 10.4 Create the Ports

4-Port Configuration

```bash
# cd /root/ovs
# ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
# ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk1 -- set Interface dpdk1 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk2 -- set Interface dpdk2 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk3 -- set Interface dpdk3 type=dpdk
# ./utilities/ovs-vsctl show
```
10.5 Add the Port Flows

1. Clear current flows:
   ```
   # export OVS_DIR=/root/ovs
   # cd $OVS_DIR
   # ./utilities/ovs-ofctl del-flows br0
   ```

2. Add flow:
   ```
   # ./utilities/ovs-ofctl add-flow br0 \
   in_port=1,dl_type=0x800,idle_timeout=0,action=output:2
   # ./utilities/ovs-ofctl add-flow br0 \
   in_port=2,dl_type=0x800,idle_timeout=0,action=output:1
   # ./utilities/ovs-ofctl add-flow br0 \
   in_port=3,dl_type=0x800,idle_timeout=0,action=output:4
   # ./utilities/ovs-ofctl add-flow br0 \
   in_port=4,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ovs-ofctl dump-flows br0
   ```
11.0 PHY-VM-PHY Test Setup

Follow the steps on the PHY-to-PHY test setup up to the section 10.3 Tune OvS-vswitchd, and set up 1 core with 1 PMD thread configuration (without hyper-threading) for the PHY-to-VM tests. Follow the instructions in this section to continue on the PHY-to-VM.

11.1 Create the Ports

4-Port configuration

```bash
# cd /root/ovs
# ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
# ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk1 -- set Interface dpdk1 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk2 -- set Interface dpdk2 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk3 -- set Interface dpdk3 type=dpdk
# ./utilities/ovs-vsctl add-br br0 vhost-user0 
    -- set Interface vhost-user0 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user1 
    -- set Interface vhost-user1 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user2 
    -- set Interface vhost-user2 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user3 
    -- set Interface vhost-user3 type=dpdkvhostuser
# ./utilities/ovs-vsctl show
```

11.2 Add the Port Flows

1. Clear current flows

```bash
# export OVS_DIR=/root/ovs
# cd $OVS_DIR
# ./utilities/ovs-ofctl del-flows br0
```

2. Add Flow

```bash
# ./utilities/ovs-ofctl add-flow br0 
    in_port=1,dl_type=0x800,idle_timeout=0,action=output:5
# ./utilities/ovs-ofctl add-flow br0 
    in_port=2,dl_type=0x800,idle_timeout=0,action=output:6
# ./utilities/ovs-ofctl add-flow br0 
    in_port=3,dl_type=0x800,idle_timeout=0,action=output:7
# ./utilities/ovs-ofctl add-flow br0 
    in_port=4,dl_type=0x800,idle_timeout=0,action=output:8
# ./utilities/ovs-ofctl add-flows br0 
    in_port=5,dl_type=0x800,idle_timeout=0,action=output:1
```
11.3 Power on the VM

1. Start the VM on CPU 4, CPU 5, and CPU 6 (0x70) with the following configuration:
   ```
   # taskset 70 qemu-system-x86_64 -m 4096 -cpu host -hda /root/vm-images/vm-fc21.img -boot c -enable-kvm -pidfile /tmp/vm1.pid -monitor unix:/tmp/vmlmonitor,server,nowait -name 'FC21-VM1' -net none -no-reboot -object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on -numa node,memdev=mem -mem-prealloc -chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 -netdev type=vhost-user,id=net1,chardev=char1,hostforce -device virtio-net-pci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mrg_rxbuf=off -chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 -netdev type=vhost-user,id=net2,chardev=char2,hostforce -device virtio-net-pci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mrg_rxbuf=off -chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 -netdev type=vhost-user,id=net1,chardev=char1,hostforce -device virtio-net-pci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mrg_rxbuf=off -chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 -netdev type=vhost-user,id=net2,chardev=char2,hostforce -device virtio-net-pci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_ts o6=off,guest_ecn=off,mrg_rxbuf=off
   --nographic -vnc :1
   ```

11.4 Set the VM Kernel Boot Parameters

1. Add the following to the kernel boot parameters /etc/default/grub:
   ```
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024 isolcpus=1,2 rhgb quiet"
   ```

2. Save the file and update the GRUB config file:
   ```
   # grub2-mkconfig -o /boot/grub2/grub.cfg
   ```

3. Reboot the VM and check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePage and 1024 2MB HugePages:
   ```
   # ls /sys/devices/system/node/node0/hugepages/hugepages-*
   hugepages-1048576kB/  hugepages-2048kB/
   ```
11.5 Set up the VM HugePages

1. Mount the HugePage for 1 GB and 2 MB:
   ```
   # mount -t hugetlbfs hugetlbfs /mnt/huge
   # mount -t hugetlbfs none /mnt/huge_2mb -o pagesize=2MB
   ```

11.6 Set up DPDK 2.2

1. Download DPDK 2.2.0 and compile it:
   ```
   # make install T=x86_64-native-linuxapp-gcc
   ```

2. Edit the test-pmd apps input and output queue size to 2K for better throughput performance:
   ```
   # vi /root/dpdk-2.2.0/app/test-pmd/test-pmd.c
   
   /*
   * Configurable number of RX/TX ring descriptors.
   */
   
   #define RTE_TEST_RX_DESC_DEFAULT 2048
   #define RTE_TEST_TX_DESC_DEFAULT 2048
   ```

3. Save and build the test-pmd app:
   ```
   # export RTE_SDK=/root/dpdk-2.2.0
   # export RTE_TARGET=x86_64-native-linuxapp-gcc
   # make
   ```

11.7 Set up the vhost Network in the VM

1. Load the UIO kernel module in the VM:
   ```
   # modprobe uio
   # insmod /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko
   ```

2. Check the PCI ID for the 10GbE NIC ports:
   ```
   # lspci -nn
   
   00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   ```
00:06.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
00:07.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]

3. Bind the user-side vhost network devices with the igb_uio driver:

```bash
# /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:04.0
# /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:05.0
# /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:06.0
# /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:07.0
# /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -status
```

Network devices using DPDK-compatible driver
============================================
0000:00:04.0 'Virtio network device' drv=igb_uio unused=virtio_pci
0000:00:05.0 'Virtio network device' drv=igb_uio unused=virtio_pci
0000:00:06.0 'Virtio network device' drv=igb_uio unused=virtio_pci
0000:00:07.0 'Virtio network device' drv=igb_uio unused=virtio_pci

Network devices using kernel driver
===================================
<none>

11.8 Start the test-pmd Application in the VM

1. Run `test-pmd app` on vCPU1 and vCPU2 (0x6):

```bash
# cd /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/build/app/test-pmd
# ./testpmd -c 0x3 -n 4 -- --burst=64 -i --disable-hw-vlan --txd=2048 \ 
--rxd=2048 --txqflags=0xf00
```

2. In the application, enter the `fwd` and `mac_retry` commands:

```bash
testpmd> set fwd mac_retry
```

3. Set the `mac_retry` packet forwarding mode.

4. Start the PMD forwarding operation:

```bash
testpmd> start
mac_retry packet forwarding - CRC stripping disabled - packets/burst=64
nb forwarding cores=1 - nb forwarding ports=2
RX queues=1 - RX desc=2048 - RX free threshold=32
RX threshold registers: pthresh=8 hthresh=8 wthresh=0
TX queues=1 - TX desc=2048 - TX free threshold=0
TX threshold registers: pthresh=32 hthresh=0 wthresh=0
TX RS bit threshold=0 - TXQ flags=0xf00```
11.9 CPU Affinity Tuning

The tables below show the host's CPU core affinity settings for PHY-to-VM test configuration for 1 physical core (no hyper-threading). When the VM starts, there are multiple QEMU threads spawned. Refer to section 9.5 CPU Core Affinity for the Virtual Machine (qemu-kvm), to set the active QEMU threads to the correct core affinity.

Table 11-1 CPU Affinity Setting on the Host

<table>
<thead>
<tr>
<th>Logical Core</th>
<th>Process</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ovs-vswitchd</td>
</tr>
<tr>
<td>2</td>
<td>PMD0</td>
</tr>
<tr>
<td>4, 5, 6</td>
<td>QEMU</td>
</tr>
</tbody>
</table>

Table 11-2 QEMU Threads CPU Affinity

<table>
<thead>
<tr>
<th>Logical Core</th>
<th>Process</th>
<th>CPU% (from htop)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>QEMU (main thread)</td>
<td>100</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>100</td>
</tr>
<tr>
<td>6</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>QEMU</td>
<td>0</td>
</tr>
</tbody>
</table>

Note: Two active threads (with 100% CPU) are set to 2 different logical cores.
12.0 VM-VM Test Setup

Refer to section 10.0 OvS Test Setup and follow up to the section 10.3 Tune OvS-vswitchd, to set up the host configurations, and then set up 1 core with 1 PMD thread configuration (without hyper-threading) for 2 VMs series tests. Follow the instructions below to continue on the VM-to-VM setup.

12.1 Create the Ports

```bash
# cd /root/ovs
# ./utilities/ovs-vsctl show
# ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
# ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
# ./utilities/ovs-vsctl add-port br0 dpdk1 -- set Interface dpdk1 type=dpdk
# ./utilities/ovs-vsctl add-port br0 vhost-user0 \  
  -- set Interface vhost-user0 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user1 \  
  -- set Interface vhost-user1 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user2 \  
  -- set Interface vhost-user2 type=dpdkvhostuser
# ./utilities/ovs-vsctl add-port br0 vhost-user3 \  
  -- set Interface vhost-user3 type=dpdkvhostuser
# ./utilities/ovs-vsctl show
```

12.2 Add the Port Flows

1. Clear current flows

```bash
# export OVS_DIR=/root/ovs
# cd $OVS_DIR
# ./utilities/ovs-ofctl del-flows br0
```

2. Add Flow

```bash
# ./utilities/ovs-ofctl add-flow br0 \  
  in_port=1,dl_type=0x800,idle_timeout=0,action=output:3
# ./utilities/ovs-ofctl add-flow br0 \  
  in_port=2,dl_type=0x800,idle_timeout=0,action=output:6
# ./utilities/ovs-ofctl add-flow br0 \  
  in_port=3,dl_type=0x800,idle_timeout=0,action=output:1
# ./utilities/ovs-ofctl add-flow br0 \  
  in_port=4,dl_type=0x800,idle_timeout=0,action=output:5
# ./utilities/ovs-ofctl add-flow br0 \  
  in_port=5,dl_type=0x800,idle_timeout=0,action=output:4
# ./utilities/ovs-ofctl dump-flows br0
```
12.3 Power on the VM

1. Start the first VM on CPU 3, CPU 4, and CPU 5 (0x38) with the following configuration:

   ```bash
   # taskset 38 qemu-system-x86_64 -m 4096 -cpu host -hda /root/vm-
   images/vm2-vc21.img -boot c -enable-kvm -pidfile /tmp/vm2.pid -monitor
   unix:/tmp/vm2monitor,server,nowait -name 'FC21-VM2' -net none -no-reboot -
   object memory-backend-file,idaemem,size=4096M,mem-path=/dev/hugepages,share=on
   -numa node,memdev=mem -mem-prealloc \
   -chardev socket,idaememdev=memdev,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \
   -netdev type=vhost-user,idaememdev=memdev,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off \
   -chardev socket,idaememdev=memdev,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \
   -netdev type=vhost-user,idaememdev=memdev,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off \
   --nographic -vnc :14
   ```

12.3.1 VM Kernel Boot Parameters

1. Add the following to the kernel boot parameters /etc/default/grub in the VM:

   ```bash
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024
isolcpus=1,2 rhgb quiet"
   ```

2. Save the file and update the GRUB config file:

   ```bash
   # grub2-mkconfig -o /boot/grub2/grub.cfg
   ```

3. Reboot the VM and then check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePages and 1024 2MB HugePages:

   ```bash
   # ls /sys/devices/system/node/node0/hugepages/hugepages-*
   hugepages-1048576kB/    hugepages-2048kB/
   # cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/.nr_hugepages
   1
   # cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/.nr_hugepages
   1024
   ```

4. Start the second VM by making a copy of the first VM. Start the second VM on CPU 5, CPU6, and CPU7 (0xE0) with the following command:

   ```bash
   # taskset E0 qemu-system-x86_64 -m 4096 -cpu host -hda /root/vm-
   images/vm2-vc21.img -boot c -enable-kvm -pidfile /tmp/vm2.pid -monitor
   unix:/tmp/vm2monitor,server,nowait -name 'FC21-VM2' -net none -no-reboot -
   object memory-backend-file,idaemem,size=4096M,mem-path=/dev/hugepages,share=on
   -numa node,memdev=mem -mem-prealloc \
   -chardev socket,idaememdev=memdev,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \
   -netdev type=vhost-user,idaememdev=memdev,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off \
   -chardev socket,idaememdev=memdev,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \
   -netdev type=vhost-user,idaememdev=memdev,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off \
   ```
12.4 Set up the VM HugePages

1. Mount the HugePage for 1GB and 2MB:

   ```
   # mount -t hugetlbfs hugetlbfs /mnt/huge
   # mount -t hugetlbfs none /mnt/huge_2mb -o pagesize=2MB
   ```

12.5 Set up DPDK 2.2

1. Download DPDK 2.2.0 and compile it:

   ```
   # make install T=x86_64-native-linuxapp-gcc
   ```

2. Edit the test-pmd app input and output queue size to 2K for better throughput performance:

   ```
   # vi /root/dpdk-2.2.0/app/test-pmd/test-pmd.c
   /*
   * Configurable number of RX/TX ring descriptors.
   */
   #define RTE_TEST_RX_DESC_DEFAULT 2048
   #define RTE_TEST_TX_DESC_DEFAULT 2048
   ```

3. Save and build the test-pmd app:

   ```
   # export RTE_SDK=/root/dpdk-2.2.0
   # export RTE_TARGET=x86_64-native-linuxapp-gcc
   # make
   ```

12.6 Set up the vHost Network in the VM

1. Load the UIO kernel module in the VM:

   ```
   # modprobe uio
   # insmod /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko
   ```

2. Check the PCI ID for the 10GbE NIC ports:

   ```
   # lscpi -nn
   00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   ```

3. Bind the user side vhost network devices with the igb_uio driver:

   ```
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:04:0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:05:0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py --status
   ```

   Network devices using DPDK-compatible driver
12.7 Start test-pmd Application in the VM

1. Run the test-pmd app on vCPU1 and vCPU2 (0x6):
   ```
   # cd /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/build/app/test-pmd
   # ./testpmd -c 0x3 -n 4 --burst=64 -i --txd=2048 --rxd=2048 \n   --txqflags=0xf00 --disable-hw-vlan
   ```
2. In the application, enter the fwd and mac retry commands:
   ```
   testpmd> set fwd mac_retry
   Set mac_retry packet forwarding mode
   ```
3. Start the PMD forwarding operation:
   ```
   testpmd> start
   mac_retry packet forwarding - CRC stripping disabled - packets/burst=64
   nb forwarding cores=1 - nb forwarding ports=2
   RX queues=1 - RX desc=2048 - RX free threshold=32
   RX threshold registers: pthresh=8 hthresh=8 wthresh=0
   TX queues=1 - TX desc=2048 - TX free threshold=0
   TX threshold registers: pthresh=32 hthresh=0 wthresh=0
   TX RS bit threshold=0 - TXQ flags=0xf00
   ```

12.8 CPU Affinity Tuning

The tables below show the host’s CPU core affinity settings for VM-to-VM tests configuration for 1 physical core (no hyper-threading). When the two VMs start, there will be multiple QEMU threads spawned. Refer to section 9.5 CPU Core Affinity for the Virtual Machine (qemu-kvm), to set the active QEMU threads to the correct core affinity.

Table 12-1 CPU affinity setting on the host

<table>
<thead>
<tr>
<th>Logical Core</th>
<th>Process</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ovs-vswitchd</td>
</tr>
<tr>
<td>2</td>
<td>PMD0</td>
</tr>
<tr>
<td>3, 4, 5</td>
<td>QEMU (VM1)</td>
</tr>
<tr>
<td>5, 6, 7</td>
<td>QEMU (VM2)</td>
</tr>
</tbody>
</table>
### Table 12-2 VM1 QEMU threads CPU affinity

<table>
<thead>
<tr>
<th>Logical Core</th>
<th>Process</th>
<th>CPU% (from htop)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>QEMU (main thread for VM1)</td>
<td>100</td>
</tr>
<tr>
<td>4</td>
<td>QEMU</td>
<td>100</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
</tbody>
</table>

**Note:** Two active threads (with 100% CPU) are set to 2 different logical cores

### Table 12-3 VM2 QEMU threads CPU affinity

<table>
<thead>
<tr>
<th>Logical Core</th>
<th>Process</th>
<th>CPU% (from htop)</th>
</tr>
</thead>
<tbody>
<tr>
<td>6</td>
<td>QEMU (main thread for VM2)</td>
<td>100</td>
</tr>
<tr>
<td>7</td>
<td>QEMU</td>
<td>100</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>QEMU</td>
<td>0</td>
</tr>
</tbody>
</table>

**Note:** Two active threads (with 100% CPU) are set to 2 different logical cores
13.0 VXLAN Test Setup

Follow the instructions below to configure VXLAN test setup. Test setup configurations include using native OvS and OvS with DPDK.

13.1 Native OvS Setup

To setup and start regular OvS in Host please refer to section 10.1 Configure the Host Machine and follow the instructions below.

13.1.1 Set the Kernel Boot Parameters

1. With hyper-threading enabled disabled, add the following to the kernel boot parameters /etc/default/grub for 2 sockets:

   ```
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=16 hugepagesz=2M hugepages=2048
   intel_iommu=off isolcpus=1-7 rhgb quiet"
   ```

2. Save the file and update the GRUB config file:

   ```
   # grub2-mkconfig -o /boot/grub2/grub.cfg
   ```

3. Reboot the host machine and check to make sure 1GB and 2MB HugePage sizes are created. You should see 16 1GB HugePages and 2048 2MB HugePages:

   ```
   # ls /sys/devices/system/node/node0/hugepages/hugepages-*/
hugepages-1048576kB/ hugepages-2048kB/
# cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
16
# cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
2048
   ```

13.1.2 Compile and Install OvS

1. Go to the OvS directory and run:

   ```
   # ./boot.sh
   # ./configure
   # make
   # make install
   # ./configure --with-linux=/lib/modules/4.2.3-300.fc23.x86_64/build 
   # CFLAGS="-Ofast -g"
   # make 'CFLAGS=-g -Ofast'
   ```
13.1.3 Prepare to Start OvS

1. Mount the 1GB HugePage and 2MB HugePage:
   ```
   # mkdir -p /mnt/huge
   # mkdir -p /mnt/huge_2mb
   # mount -t hugetlbfs nodev /mnt/huge
   # mount -t hugetlbfs nodev /mnt/huge_2mb -o pagesize=2MB
   ```

2. Check that HugePages are mounted:
   ```
   # mount
   nodev on /mnt/huge type hugetlbfs (rw,relatime)
   nodev on /mnt/huge_2mb type hugetlbfs (rw,relatime,pagesize=2MB)
   ```

3. Load the modules:
   ```
   # modprobe openvswitch
   # modprobe i40e
   ```

4. Remove and terminate previous-run OvS and prepare:
   ```
   # pkill -9 ovs
   # rm -rf /usr/local/var/run/openvswitch
   # rm -rf /usr/local/etc/openvswitch/
   # rm -f /tmp/conf.db
   # mkdir -p /usr/local/etc/openvswitch
   # mkdir -p /usr/local/var/run/openvswitch
   ```

5. Initialize the new OvS database and start the server:
   ```
   # export OVS_DIR=/root/ovs
   # cd $OVS_DIR
   # ./ovsdb/ovsdb-tool create /usr/local/etc/openvswitch/conf.db \
   ./vswitchd/vswitch.ovsschema
   ```

6. Start the database server:
   ```
   # ./ovsdb/ovsdb-server --remote=punix:/usr/local/var/run/openvswitch/db.sock \ 
   --remote=db:Open_vSwitch,Open_vSwitch,manager_options \ 
   --pidfile -detach
   ```

7. Initialize the OvS database:
   ```
   # ./utilities/ovs-vsctl --no-wait init
   ```

8. Start OvS-vswitchd:
   ```
   # ./vswitchd/ovs-vswitchd
   ```

13.1.4 Create the Ports and VXLAN VTEP

1. Create the VXLAN tunnel between 2 hosts:
   ```
   # ./utilities/ovs-vsctl add-br br0
   # ifconfig br0 2.2.2.1/24
   # ./utilities/ovs-vsctl add-port br0 eth3
   # ./utilities/ovs-appctl ovs/route/add 2.2.2.2/24 br0
   ```

2. Create an internal bridge:
   ```
   # ./utilities/ovs-vsctl add-br br-int
   ```
1. Clear current flows:
   # cd $OVS_DIR
   # ./utilities/ovs-ofctl del-flows br-int

2. Add flow for port 1 (physical) to port 2 (VTEP):
   # ./utilities/ovs-ofctl add-flow br-int \
   in_port=1,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ovs-ofctl add-flow br-int \
   in_port=2,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ovs-ofctl dump-flows br-int

13.1.6 Power on the VM

1. Start the VM on CPU 4, CPU 5, and CPU 6 (0x70) with the following configuration:
   # taskset 70 qemu-system-x86_64 -cpu host -boot c -m 4096M -smp 3 -hdad
   /root/vm-images-fedora/Fed23_VM.img --enable-kvm -name 'VM1' -vnc :1 -pidfile \
   /tmp/vml.pid --nocheck -monitor unix:/tmp/vmlvmonitor,server,nowait -object \
   memory-backend-file,id=mem,size=4096M,mem-path=/mnt/huge,share=on -numa \
   node,memdev=mem -mem-prealloc -net none -netdev \
   type=tap,id=net1,script=no,downscript=no,ifname=tap1,vhost=on -device virtio- \
   net-pci,netdev=net1,mac=00:00:00:00:00:01,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off -netdev \
   type=tap,id=net2,script=no,downscript=no,ifname=tap2,vhost=on -device virtio- \
   net-pci,netdev=net2,mac=00:00:00:00:00:02,csum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off &
13.1.7 VM Kernel Boot Parameters

1. Add the following to the kernel boot parameters /etc/default/grub in the VM:

   ```
   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024
isolcpus=1,2 rhgb quiet"
   ```

2. Save the file and update the GRUB config file:

   ```
   # grub2
   grub2-mkconfig -o /boot/grub2/grub.cfg
   ```

3. Reboot the VM and then check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePages and 1024 2MB HugePages:

   ```
   # ls /sys/devices/system/node/node0/hugepages/hugepages-*
   hugepages-1048576kB/
   hugepages-2048kB/
   # cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
   1
   # cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
   1024
   ```

13.1.8 Set up the VM HugePages

1. Mount the HugePage for 1GB and 2MB:

   ```
   # mount -t hugetlbfs hugetlbfs /mnt/huge
   # mount -t hugetlbfs none /mnt/huge_2mb -o pagesize=2MB
   ```

13.1.9 Set up the vHost Network in the VM

1. Load the UIO kernel module in the VM:

   ```
   # modprobe uio
   # insmod /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko
   ```

2. Check the PCI ID for the 10GbE NIC ports:

   ```
   # lscpi -nn
   00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device [1af4:1000]
   ```

3. Bind the user side vhost network devices with the igb_uio driver:

   ```
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:04.0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:05.0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py --status
   ```

   Network devices using DPDK-compatible driver
   =============================================
   0000:00:04.0 'Virtio network device' drv=igb_uio unused=virtio_pci
   0000:00:05.0 'Virtio network device' drv=igb_uio unused=virtio_pci
Network devices using kernel driver
===================================
<none>

### 13.1.10 Start test-pmd Application in the VM

1. Run the test-pmd app on vCPU1 and vCPU2 (0x6):
   ```
   # cd /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/build/app/test-pmd
   # ./testpmd -c 0x3 -n 4 --burst=32 -i --txd=2048 --rxd=2048 --txqflags=0xf00 --disable-hw-vlan
   ```
2. In the application, enter the fwd and mac_retry commands:
   ```
   testpmd> set fwd mac retry
   Set mac_retry packet forwarding mode
   ```
3. Start the PMD forwarding operation:
   ```
   testpmd> start
   mac_retry packet forwarding - CRC stripping disabled - packets/burst=32
   nb forwarding cores=1 - nb forwarding ports=2
   RX queues=1 - RX desc=2048 - RX free threshold=32
   RX threshold registers: pthresh=8 hthresh=8 wthresh=0
   TX queues=1 - TX desc=2048 - TX free threshold=0
   TX threshold registers: pthresh=32 hthresh=0 wthresh=0
   TX RS bit threshold=0 - TXQ flags=0xf00
   ```

### 13.2 OvS with DPDK Setup

To set up and start OvS with DPDK in Host refer to section 10.0 OvS Test Setup and follow up to the section 10.2 Start OvS-vSwitchd. Then follow the instructions below to configure the VXLAN test setup.

#### 13.2.1 Tune OvS-vSwitchd for VXLAN

Once the OvS-vSwitchd is running, we setup the CPU core affinity for the OvS PMD threads to 1 core, and 2 cores respectively.

**One-PMD Configuration**

1. Set the default OvS PMD thread usage to CPU2 (0x4):
   ```
   # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=4
   # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
   ```

**Two-PMD Configuration**

1. For 2 physical cores and 2 logical cores (2 PMDs) on system HT disabled, set the default OvS PMD thread usage to CPU2 and CPU3 (0xC):
   ```
   # ./ovs-vsctl set Open_vSwitch . other_config:pmd-cpu-mask=C
   # ./ovs-vsctl set Open_vSwitch . other_config:max-idle=30000
   ```
13.2.2 Create the Ports and VXLAN VTEP

1. Create the VXLAN tunnel between 2 hosts:
   
   ```
   # ./utilities/ovs-vsctl add-br br0 -- set bridge br0 datapath_type=netdev
   # ifconfig br0 2.2.2.1/24
   # ./utilities/ovs-vsctl add-port br0 dpdk0 -- set Interface dpdk0 type=dpdk
   # ./utilities/ovs-appctl ovs/route/add 2.2.2.2/24 br0
   ```

2. Create an internal bridge:

   ```
   # ./utilities/ovs-vsctl add-br br-int
   # ifconfig br-int 1.1.1.1/24
   # ./utilities/ovs-vsctl add-port br-int vhost-user0 -- set Interface vhost-user0 type=dpdkvhostuser
   # ./utilities/ovs-vsctl add-port br-int vhost-user1 -- set Interface vhost-user1 type=dpdkvhostuser
   ```

3. Add VXLAN VTEP:

   ```
   # ./utilities/ovs-vsctl add-port br-int vxlan0 -- set Interface \
   vxlan0 type=vxlan options:remote_ip=2.2.2.2 options:key=1000
   # ./utilities/ovs-vsctl show
   # ./utilities/ovs-appctl ovs/route/show
   ```

13.2.3 Add the Port Flows

1. Clear current flows:

   ```
   # cd $OVS_DIR
   # ./utilities/ofctl del-flows br-int
   ```

2. Add flow for port 1 (physical) to port 2 (VTEP):

   ```
   # ./utilities/ofctl add-flow br-int \
   in_port=1,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ofctl add-flow br-int \
   in_port=2,dl_type=0x800,idle_timeout=0,action=output:3
   # ./utilities/ofctl dump-flows br-int
   ```

13.2.4 Power on the VM

1. Start the VM on CPU 4, CPU 5, and CPU 6 (0x70) with the following configuration:

   ```
   # taskset 70 qemu-system-x86_64 -m 4096 -smp 3 -cpu host -hda /root/vm-images/vm2-fc21.img -boot c -enable-kvm -pidfile /tmp/vml.pid -monitor unix:/tmp/vm2monitor,server,nowait -name 'FC21-VM2' -net none -no-reboot -object memory-backend-file,id=mem,size=4096M,mem-path=/dev/hugepages,share=on -numa node,memdev=mem -mem-prealloc \
   -chardev socket,id=char1,path=/usr/local/var/run/openvswitch/vhost-user0 \
   -netdev type=vhost-user,id=net1,chardev=char1,vhostforce -device virtio-net-pci,netdev=net1,mac=00:00:00:00:00:01,csnum=off,gso=off,guest_tso4=off,guest_tso6=off,guest_ecn=off,mrg_rxbuf=off \
   -chardev socket,id=char2,path=/usr/local/var/run/openvswitch/vhost-user1 \
   ```
13.2.5 VM Kernel Boot Parameters

1. Add the following to the kernel boot parameters /etc/default/grub in the VM:

   GRUB_CMDLINE_LINUX="rd.lvm.lv=fedora-server/root rd.lvm.lv=fedora-server/swap
default_hugepagesz=1G hugepagesz=1G hugepages=1 hugepagesz=2M hugepages=1024
isolcpus=1,2 rhgb quiet"

2. Save the file and update the GRUB config file:

   # grub2-mkconfig -o /boot/grub2/grub.cfg

3. Reboot the VM and then check to make sure 1GB and 2MB HugePage sizes are created. You should see one 1GB HugePages and 1024 2MB HugePages:

   # ls /sys/devices/system/node/node0/hugepages/hugepages-*
   hugepages-1048576kB/
   hugepages-2048kB/
   # cat /sys/devices/system/node/node0/hugepages/hugepages-1048576kB/nr_hugepages
   1
   # cat /sys/devices/system/node/node0/hugepages/hugepages-2048kB/nr_hugepages
   1024

13.2.6 Set up the VM HugePages

1. Mount the HugePage for 1GB and 2MB:

   # mount -t hugetlbfs hugetlbfs /mnt/huge
   # mount -t hugetlbfs none /mnt/huge_2mb -o pagesize=2MB

13.2.7 Set up the vHost Network in the VM

1. Load the UIO kernel module in the VM:

   # modprobe uio
   # insmod /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/kmod/igb_uio.ko

2. Check the PCI ID for the 10GbE NIC ports:

   # lscpi -nn
   00:04.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
   [laf4:1000]
   00:05.0 Ethernet controller [0200]: Red Hat, Inc Virtio network device
   [laf4:1000]

3. Bind the user side vhost network devices with the igb_uio driver:

   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:04:0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py -b igb_uio 00:05:0
   # /root/dpdk-2.2.0/tools/dpdk_nic_bind.py --status
Network devices using DPDK-compatible driver
=================================================
0000:00:04.0 'Virtio network device' drv=igb_uio unused=virtio_pci
0000:00:05.0 'Virtio network device' drv=igb_uio unused=virtio_pci

Network devices using kernel driver
=================================================
<none>

**13.2.8 Start test-pmd Application in the VM**

1. Run the test-pmd app on vCPU1 and vCPU2 (0x6):

   ```bash
   # cd /root/dpdk-2.2.0/x86_64-native-linuxapp-gcc/build/app/test-pmd
   # ./testpmd -c 0x3 -n 4 --burst=32 -i --txd=2048 --rxd=2048 \
   --txqflags=0xf00 --disable-hw-vlan
   ```

2. In the application, enter the fwd and mac_retry commands:

   ```
   testpmd> set fwd mac_retry
   Set mac_retry packet forwarding mode
   ```

3. Start the PMD forwarding operation:

   ```
   testpmd> start
   mac_retry packet forwarding - CRC stripping disabled - packets/burst=32
   nb forwarding cores=1 - nb forwarding ports=2
   RX queues=1 - RX desc=2048 - RX free threshold=32
   RX threshold registers: pthresh=8 hthresh=8 wthresh=0
   TX queues=1 - TX desc=2048 - TX free threshold=0
   TX threshold registers: pthresh=32 hthresh=0 wthresh=0
   TX RS bit threshold=0 - TXQ flags=0xf00
   ```
## Appendix A: Acronyms and Abbreviations

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ARP</td>
<td>Address Resolution Protocol</td>
</tr>
<tr>
<td>BMWG</td>
<td>Benchmark Working Group</td>
</tr>
<tr>
<td>CPU</td>
<td>Central Processing Unit</td>
</tr>
<tr>
<td>DPDK</td>
<td>Data Plane Development Kit</td>
</tr>
<tr>
<td>DUT</td>
<td>Device-Under-Test</td>
</tr>
<tr>
<td>EMC</td>
<td>Exact Match Cache</td>
</tr>
<tr>
<td>ETSI</td>
<td>European Telecommunications Standards Institute</td>
</tr>
<tr>
<td>GbE</td>
<td>Gigabit Ethernet</td>
</tr>
<tr>
<td>GRUB</td>
<td>GRand Unified Bootloader</td>
</tr>
<tr>
<td>IETF</td>
<td>Internet Engineering Task Force</td>
</tr>
<tr>
<td>IPDV</td>
<td>Inter-Packet Delay Variation</td>
</tr>
<tr>
<td>IPv4</td>
<td>Internet Protocol version 4</td>
</tr>
<tr>
<td>IRQ</td>
<td>Interruption Request</td>
</tr>
<tr>
<td>ITU</td>
<td>International Telecommunication Union</td>
</tr>
<tr>
<td>ITU-T</td>
<td>ITU Telecommunication Standardization Sector</td>
</tr>
<tr>
<td>KVM</td>
<td>Kernel-based Virtual Machine</td>
</tr>
<tr>
<td>LAN</td>
<td>Local Area Network</td>
</tr>
<tr>
<td>MAC</td>
<td>Media Access Control</td>
</tr>
<tr>
<td>NFV</td>
<td>Network Functions Virtualization</td>
</tr>
<tr>
<td>NIC</td>
<td>Network Interface Card</td>
</tr>
<tr>
<td>NUMA</td>
<td>Non-Uniform Memory Access</td>
</tr>
<tr>
<td>ONP</td>
<td>Intel® Open Network Platform</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Description</td>
</tr>
<tr>
<td>--------------</td>
<td>-------------</td>
</tr>
<tr>
<td>OPNFV</td>
<td>Open Platform for NFV</td>
</tr>
<tr>
<td>OvS</td>
<td>Open vSwitch</td>
</tr>
<tr>
<td>PCI</td>
<td>Peripheral Component Interconnect</td>
</tr>
<tr>
<td>PDV</td>
<td>Packet Delay Variation</td>
</tr>
<tr>
<td>PHY</td>
<td>Physical Layer</td>
</tr>
<tr>
<td>PID</td>
<td>Process ID</td>
</tr>
<tr>
<td>PMD</td>
<td>Poll Mode Driver</td>
</tr>
<tr>
<td>QEMU</td>
<td>Quick Emulator</td>
</tr>
<tr>
<td>RFC</td>
<td>Request for Comments</td>
</tr>
<tr>
<td>SDN</td>
<td>Software-Defined Networking</td>
</tr>
<tr>
<td>SELinux</td>
<td>Security-Enhanced Linux</td>
</tr>
<tr>
<td>SLA</td>
<td>Service-Level Agreement</td>
</tr>
<tr>
<td>TLB</td>
<td>Translation Lookaside Buffer</td>
</tr>
<tr>
<td>vCPE</td>
<td>Virtual Customer Premises Equipment</td>
</tr>
<tr>
<td>vhost</td>
<td>Virtual Host</td>
</tr>
<tr>
<td>VM</td>
<td>Virtual Machine</td>
</tr>
<tr>
<td>VNF</td>
<td>Virtualized Network Function</td>
</tr>
<tr>
<td>VTEP</td>
<td>VXLAN Tunnel End Point</td>
</tr>
<tr>
<td>VXLAN</td>
<td>Virtual eXtensible LAN</td>
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</tbody>
</table>
## Appendix B: References

<table>
<thead>
<tr>
<th>Title</th>
<th>Reference</th>
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<tr>
<td>01.org: Intel® Open Network Platform</td>
<td><a href="https://01.org/packet-processing/intel%C2%AE-onp-servers">https://01.org/packet-processing/intel%C2%AE-onp-servers</a></td>
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<td>01.org: Intel® ONP 2.1 Reference Architecture Guide</td>
<td><a href="https://01.org/packet-processing/intel%C2%AE-onp-servers">https://01.org/packet-processing/intel%C2%AE-onp-servers</a></td>
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<tr>
<td>01.org: Intel® ONP 2.1 Release Notes</td>
<td><a href="https://01.org/packet-processing/intel%C2%AE-onp-servers">https://01.org/packet-processing/intel%C2%AE-onp-servers</a></td>
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<td>Intel® Xeon® processor E5-2695 v4</td>
<td><a href="https://ark.intel.com">https://ark.intel.com</a></td>
</tr>
<tr>
<td>Supermicro X10DRH-I</td>
<td><a href="https://www.supermicro.com/products/motherboard/Xeon/C600/X10DRH-i.cfm">https://www.supermicro.com/products/motherboard/Xeon/C600/X10DRH-i.cfm</a></td>
</tr>
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